

```
[B::List e:EMAC_cpswUpdatePhyStatus]
ステップ オーバー Diverge 出口へ 呼出元 実行 停止 表示切替 検索: emac_cpsw.c
addr/line source
1215     if(gmacObject->PhyDuplex == EMAC_PHY_FULL_DUPLEX_MODE)
1218     {
        /* enable full duplex in mac control*/
        macControl |= (((uint32_t)1U) << CSL_CPGMAC_SL_MACCONTROL_REG_FULLDUPLEX_SHIFT);
    }
    else
    {
        /* disable full duplex in mac control */
        macControl &= ~(((uint32_t)1U) << CSL_CPGMAC_SL_MACCONTROL_REG_FULLDUPLEX_SHIFT);
    }
1225     gmacObject->PhySpeed = ENETPHY_GetSpeed(hPhyDev);
1227     if (gmacObject->PhySpeed != EMAC_PHY_GIGABIT_MODE)
1229     {
        gmacObject->PhySpeed = (gmacObject->PhySpeed >> 10U);
        /* not in gigabit mode, disable it in mac control*/
        macControl &= ~(((uint32_t)1U) << CSL_CPGMAC_SL_MACCONTROL_REG_GIG_SHIFT);
    }
    else
    {
        /* in gigabit mode, enable it in mac control*/
        macControl |= (((uint32_t)1U) << CSL_CPGMAC_SL_MACCONTROL_REG_GIG_SHIFT);
    }
1239     gmacObject->PhyNum = ENETPHY_GetPhyNum(hPhyDev);
1241     if(OU == gmacObject->PhySpeed)
1243     {
        macControl |= (((uint32_t)1U) << CSL_CPGMAC_SL_MACCONTROL_REG_CTL_EN_SHIFT);
    }
    else
    {
        macControl &= ~(((uint32_t)1U) << CSL_CPGMAC_SL_MACCONTROL_REG_CTL_EN_SHIFT);
    }
    }

    /* Write mac control register from stored value */
    CSL_CPGMAC_SL_setMacControlReg((CSL_cpgmacSlHandle) cpsw3gCfg->baseConfig.port[instanceNum].sliver_base, macControl);
}
```

At this timing, the return value of ENETPHY\_GetSpeed () (gmacObject-> PhySpeed) is 0.

gmacObject-> PhySpeed is 0. macControl change 0x21 to 0x00040021.

CSL\_CPGMAC\_SL\_MACCONTROL\_REG\_CTL\_EN\_SHIFT is bit 18

```
2 |src#ip#cpmac#V2#cslr_cpgmac_sl.h(170):↓  
3 |#define CSL_CPGMAC_SL_MACCONTROL_REG_CTL_EN_SHIFT ((Uint32)(0x00000012u))↓  
4 |↓  
5 |[585]
```