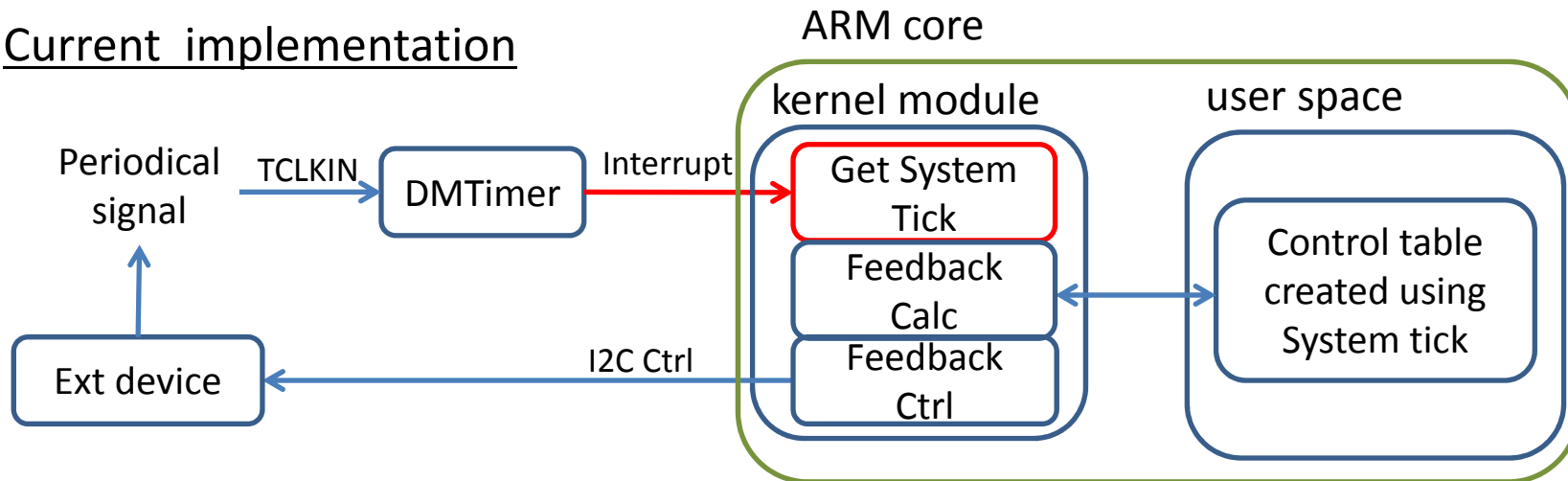
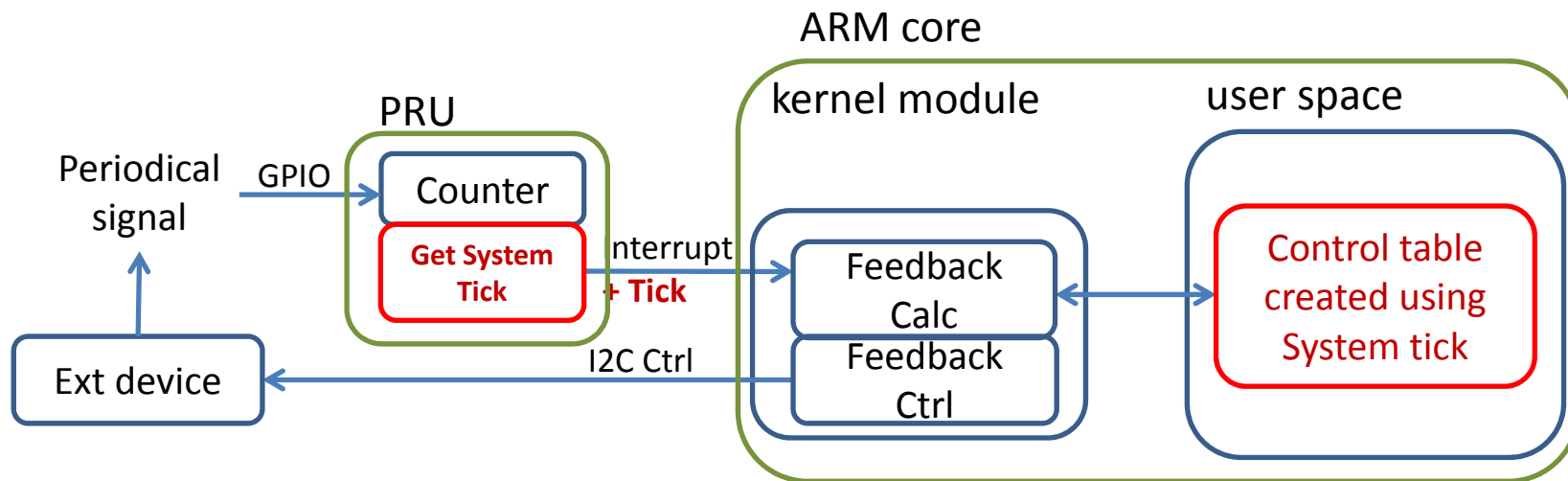


Current implementation



more than +/- 10usec observed from DMTimer interrupt to Get System Tick

Planned implementation using PRU



Common tick accessible from both PRU and ARM are needed