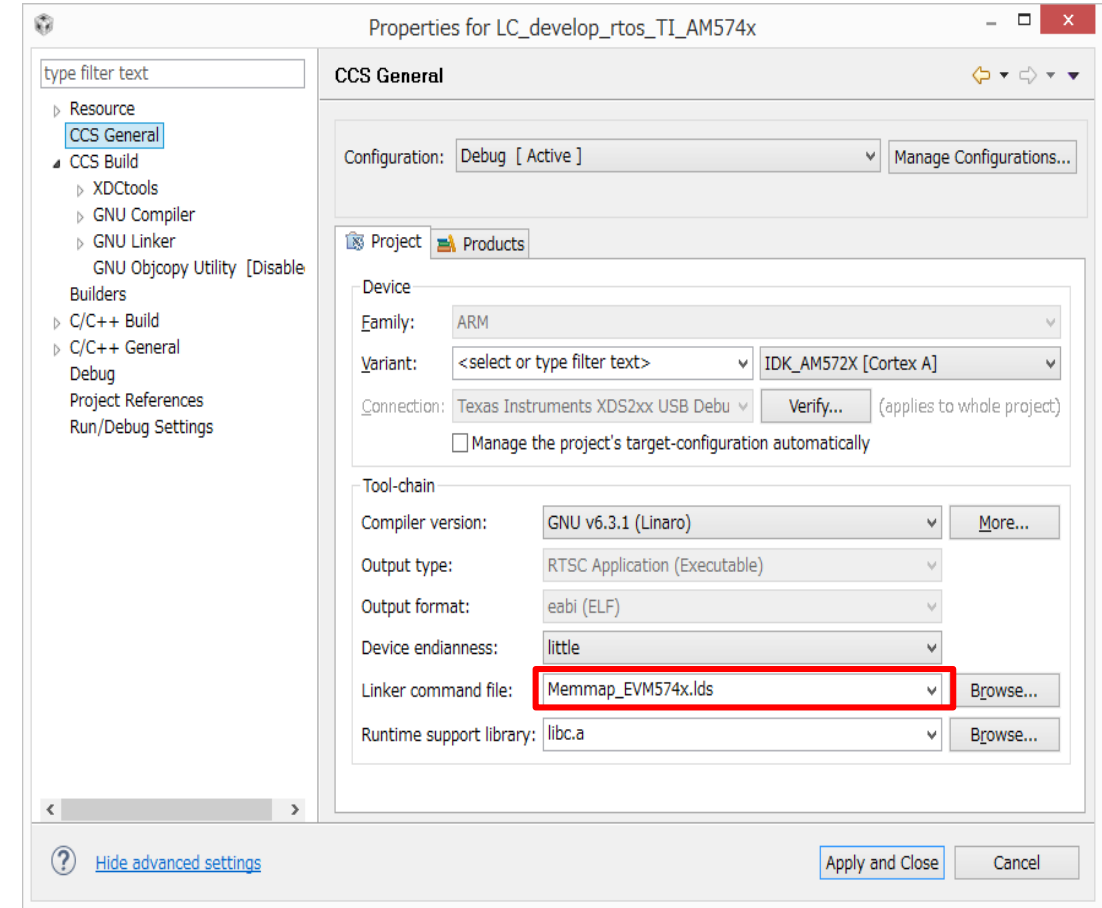


1:They sent us the information about their .lds file

```
MEMORY
{
  OCMC_RAM1 : o = 0x40300000, l = 0x00080000 /* 512kB L3 OCMC RAM1 */
  OCMC_RAM2 : o = 0x40400000, l = 0x00100000 /* 1MB L3 OCMC RAM2 */
  OCMC_RAM3 : o = 0x40500000, l = 0x00100000 /* 1MB L3 OCMC RAM3 */
  /* DDR0 : o = 0x80000000, l = 0x40000000 /**/ 1GB external DDR Bank 0 */
  /* DDR1 : o = 0xC0000000, l = 0x40000000 /**/ 1GB external DDR Bank 1 */
  LOAD_ROM1 : o = 0x80000000, l = 0x10000000
  LOAD_ROM2 : o = 0x91000000, l = 0x00180000
  LOAD_ROM3 : o = 0x91180000, l = 0x00020000
  LOAD_ROM4 : o = 0x911A0000, l = 0x00060000
  LOAD_RAM1 : o = 0x91200000, l = 0x00400000
  LOAD_RAM2 : o = 0x91600000, l = 0x6ea00000
}

.RW_DATA2 :
{
  . = ALIGN(4);
  * (.bss)
} > LOAD_RAM2
```

2:They sent us the information about CCS setting.



3:They found below warnings.

```
C:/ti/gcc-arm-none-eabi-6-2017-q1-update/bin/arm-none-eabi-gcc.exe @"ccsLinker.opt" -o"LC_develop_rtos_TI_AM574x.out"
c:/ti/gcc-arm-none-eabi-6-2017-q1-update/bin/./lib/gcc/arm-none-eabi/6.3.1/./../../../../arm-none-eabi/bin/ld.exe:configPkg/linker.cmd:31: warning: redeclaration of memory region `OCMC_RAM1'
c:/ti/gcc-arm-none-eabi-6-2017-q1-update/bin/./lib/gcc/arm-none-eabi/6.3.1/./../../../../arm-none-eabi/bin/ld.exe:configPkg/linker.cmd:32: warning: redeclaration of memory region `OCMC_RAM2'
c:/ti/gcc-arm-none-eabi-6-2017-q1-update/bin/./lib/gcc/arm-none-eabi/6.3.1/./../../../../arm-none-eabi/bin/ld.exe:configPkg/linker.cmd:33: warning: redeclaration of memory region `OCMC_RAM3'
Finished building target: "LC_develop_rtos_TI_AM574x.out"
```