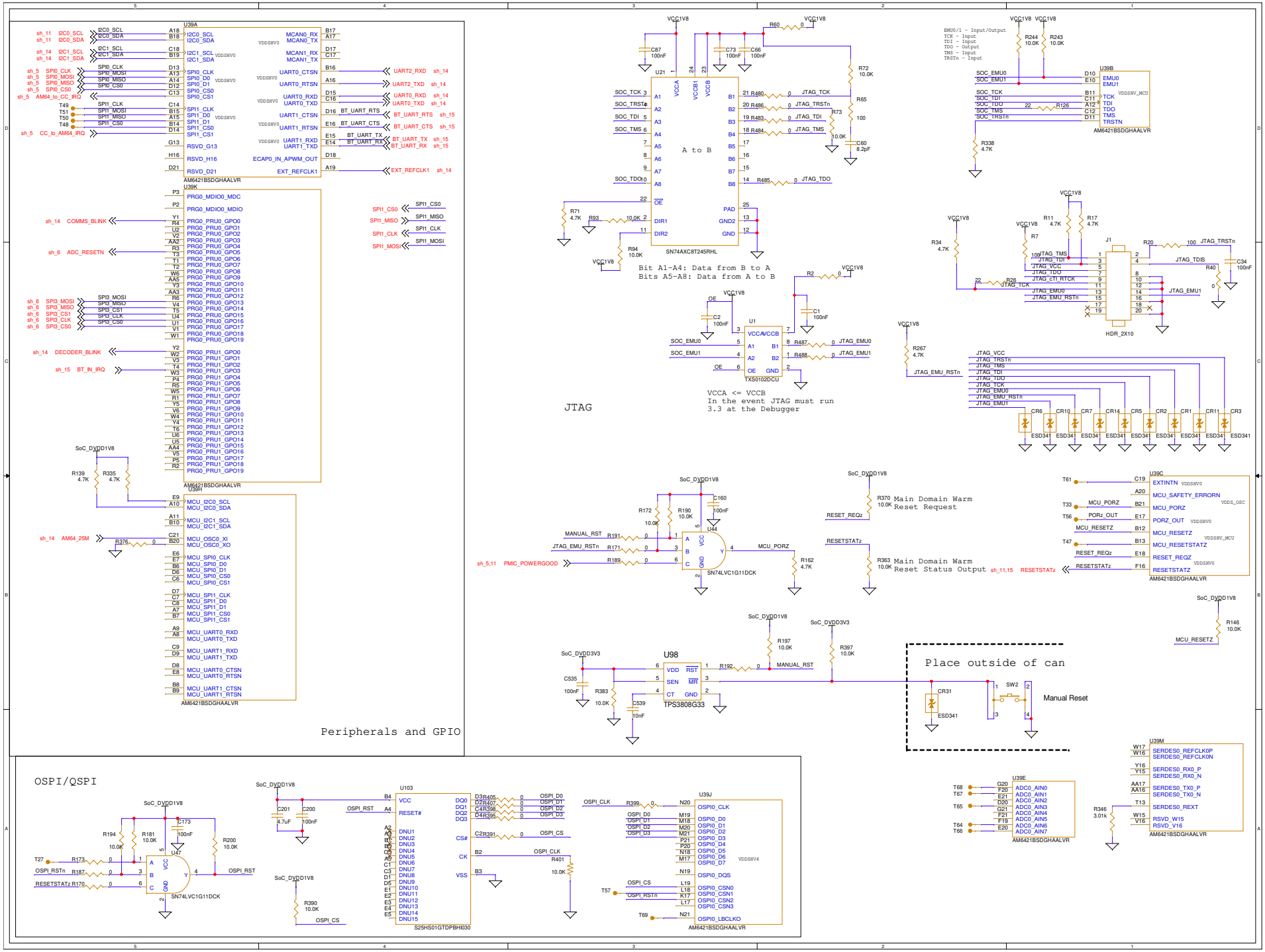
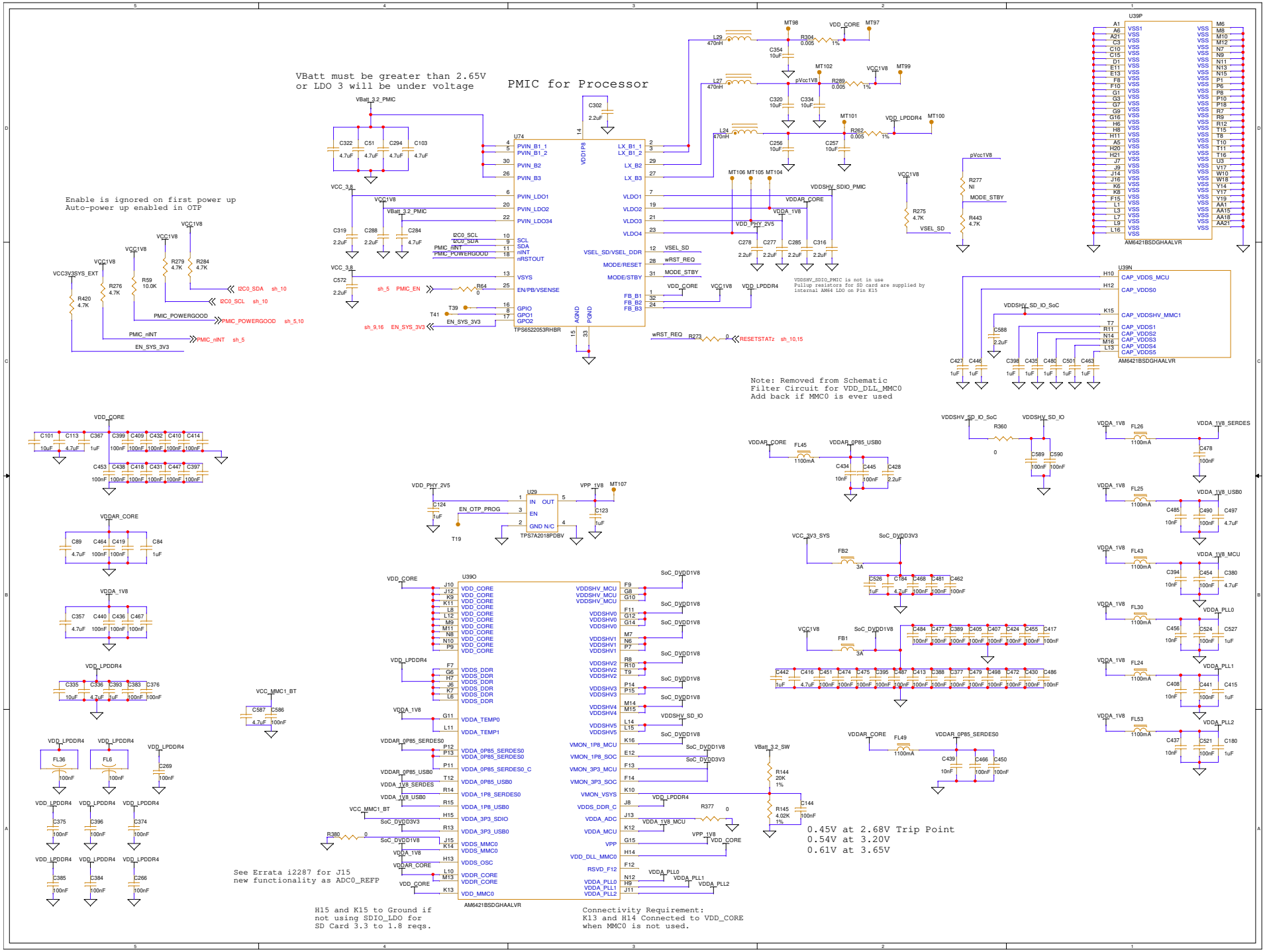

Peripherals and GPIO
JTAG
Bit A1-A4: Data from B to A
Bits A5-A8: Data from A to B
VCCA <= VCCB
In the event JTAG must run
3.3 at the Debugger
Place outside of can
Manual Reset
OSPI/QSPI
AM6421BSDGHAALVR
SN74AXC8T245RHL
TXS0102DCU
SN74LVC1G11DCK
TPS3808G33
S25HS01GTDPBHI030


PMIC for Processor
VBatt must be greater than 2.65V
or LDO 3 will be under voltage
Enable is ignored on first power up
Auto-power up enabled in OTP
U74
TPS652053RHBR
U39P
AM6421BSDGHAALVR
U39N
AM6421BSDGHAALVR
U29
TPS7A2018PDBV
U39O
AM6421BSDGHAALVR
VDDSHV_SDIO_PMIC is not in use
Pullup resistors for SD card are supplied by
internal AM64 LDO on Pin K15
Note: Removed from Schematic
Filter Circuit for VDD_DLL_MMC0
Add back if MMC0 is ever used
See Errata i2287 for J15
new functionality as ADC0_REFP
H15 and K15 to Ground if
not using SDIO_LDO for
SD Card 3.3 to 1.8 reqs.
Connectivity Requirement:
K13 and H14 Connected to VDD_CORE
when MMC0 is not used.
0.45V at 2.68V Trip Point
0.54V at 3.20V
0.61V at 3.65V

ERROR: undefinedfilename
OFFENDING COMMAND: project

STACK: