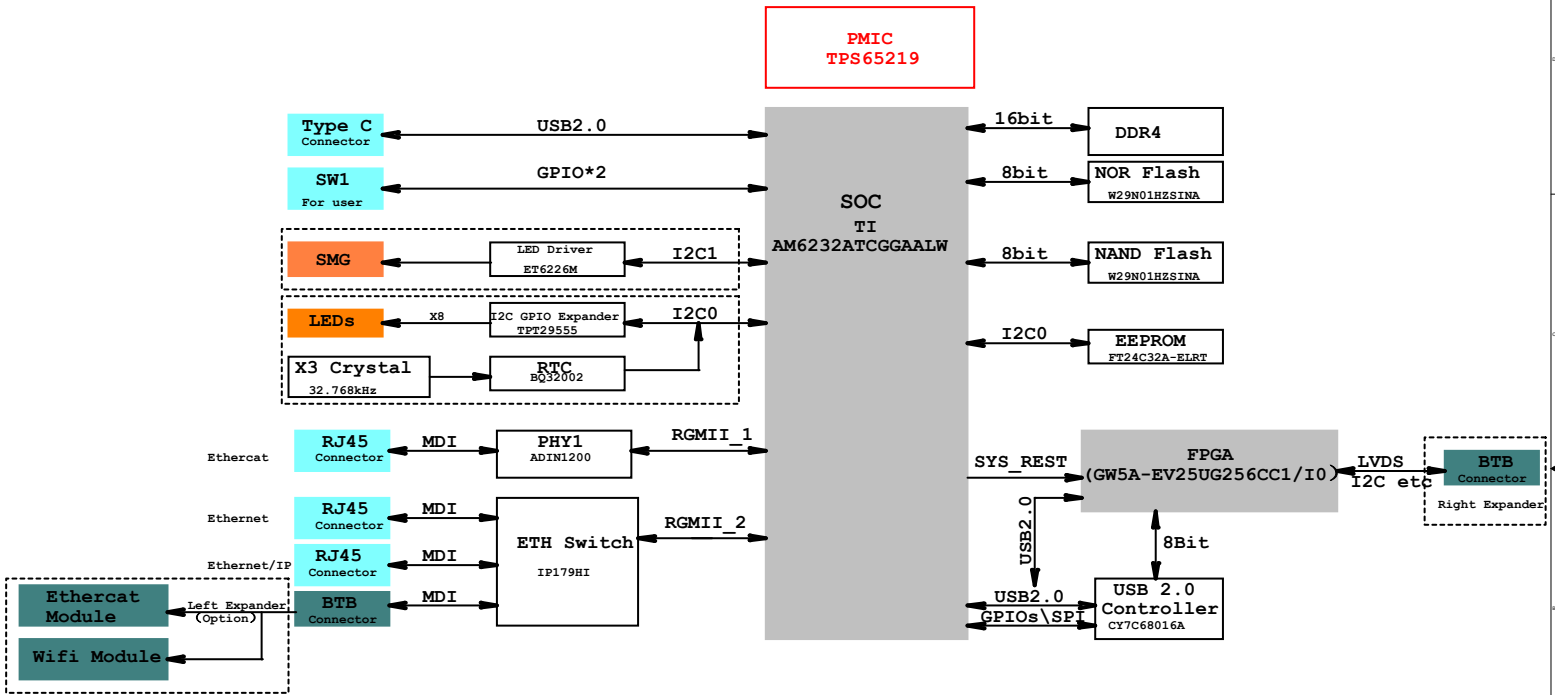


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012	012_SoC DDS & NAND FLASH
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020	020_FPGA_LVDS
021	021_FPGA_USB
022	022_BUTTON & MH
023	023_GPIOs
024	024_CONNECTOR

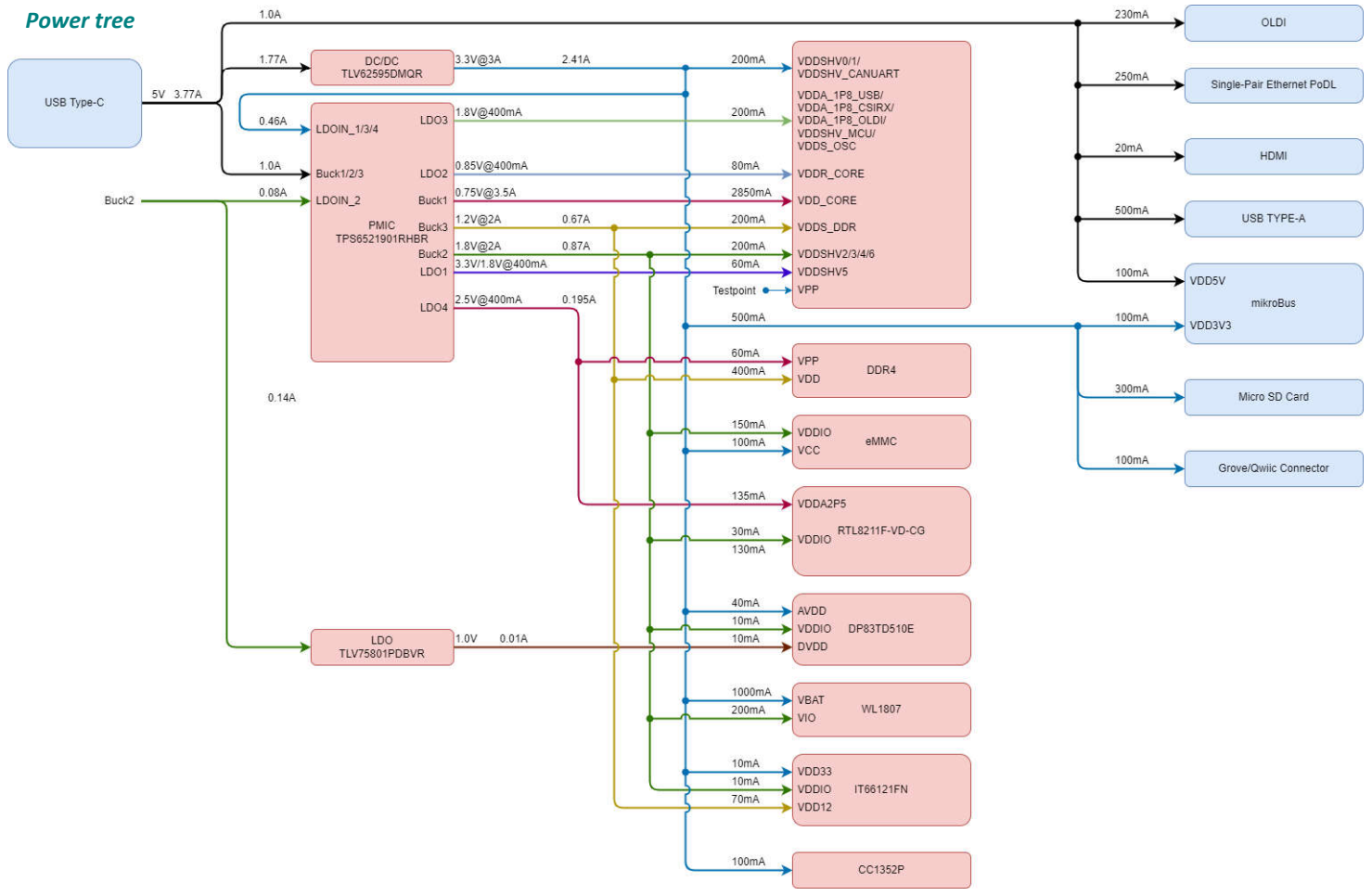
REVISION HISTORY			
VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR
1.0			Dylan Chen
1.2			

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File: RX PLC Mesh board		
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Function Block Diagram



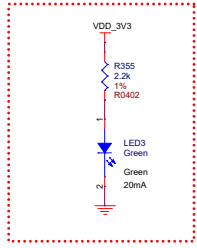
Power tree



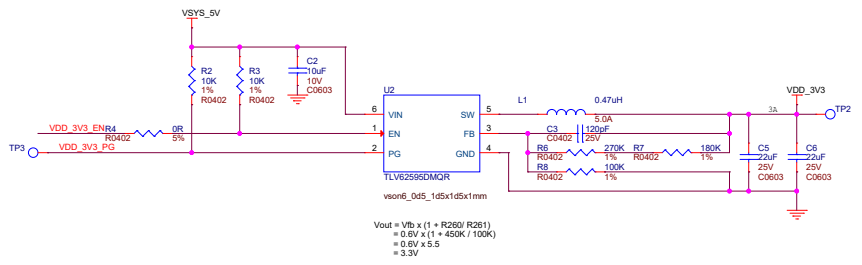
I2C Usage Diagram

	IC	Address
I2C0	PMIC (U3)	0x30 (0b0110000)
	EEPROM (U15)	0x50 (0b1010000)
	RTC (U16)	0xB8 (0b1101000)
	I/O Expander (On LED board)	0x20 (0b0100000)
I2C1	LED driver (On LED board)	

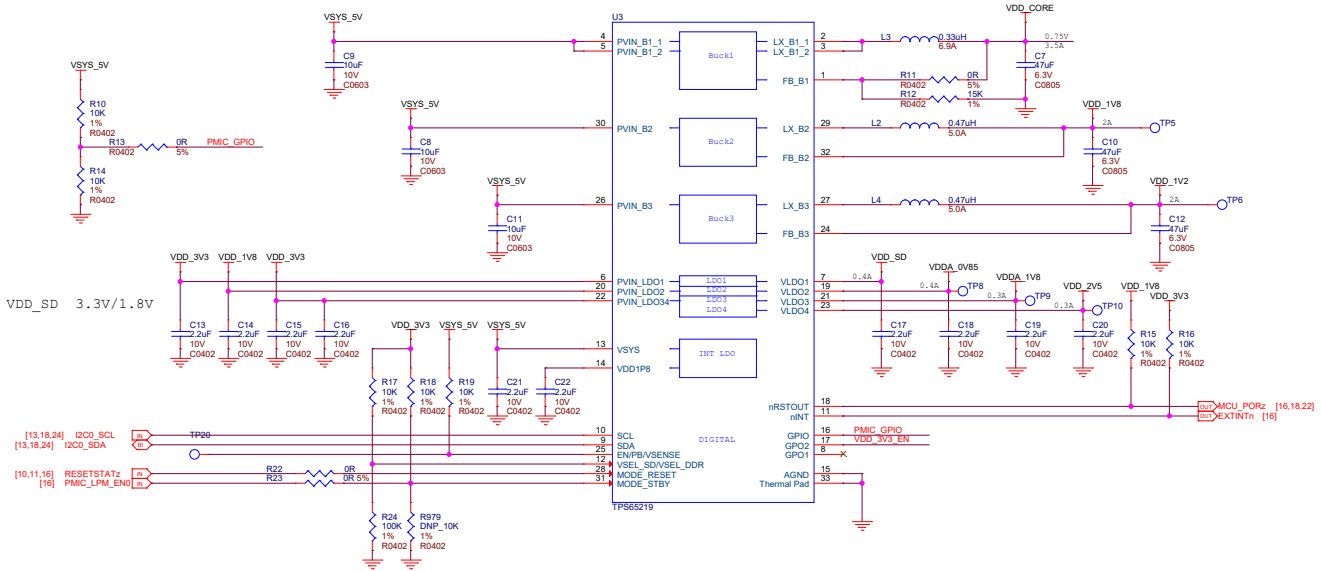
PWR LED



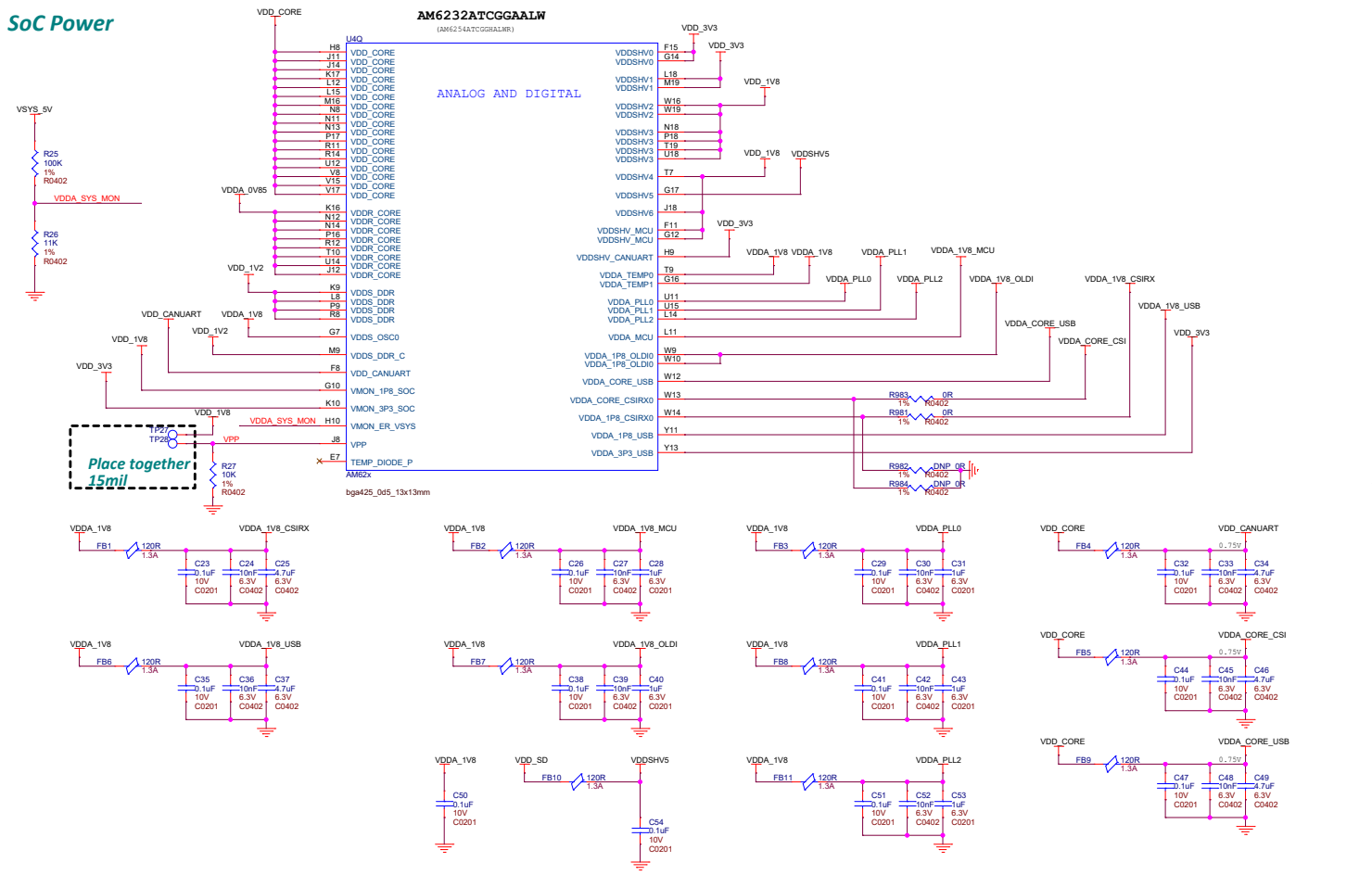
DCDC 3V3

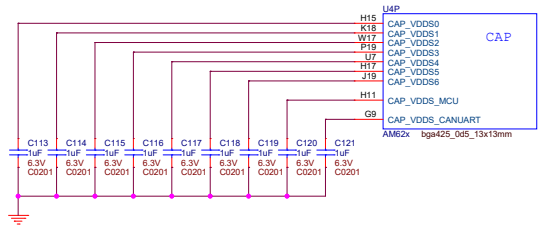
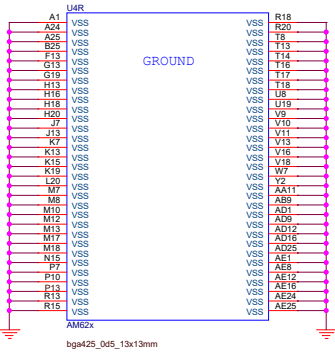
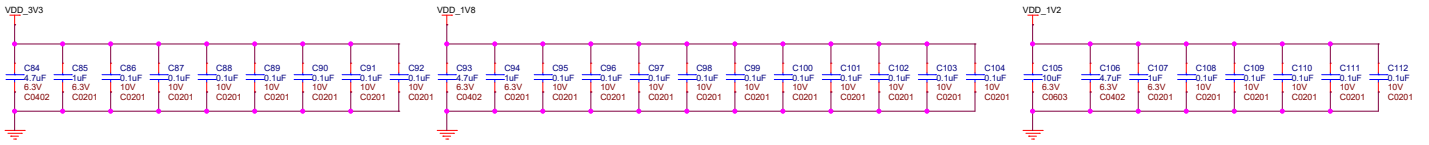
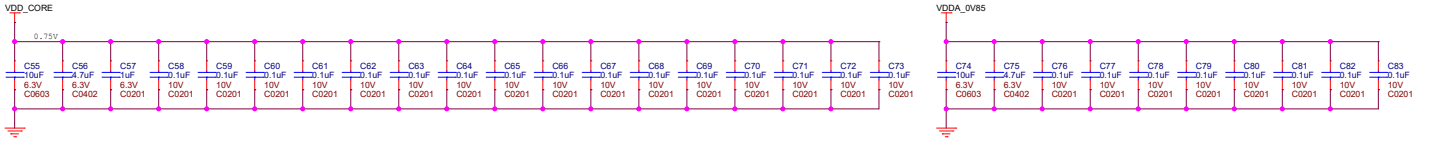


PMIC



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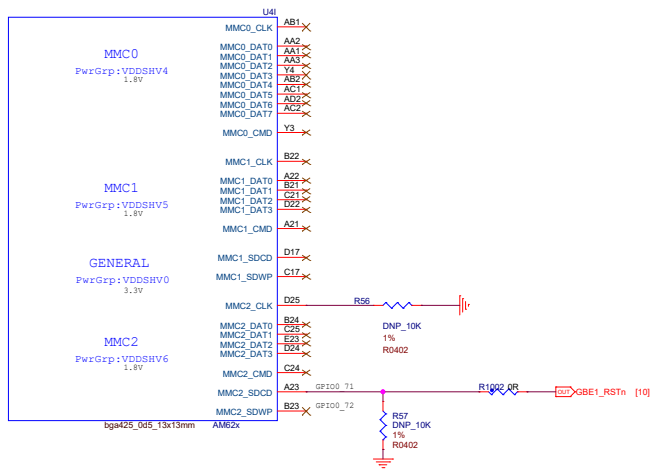




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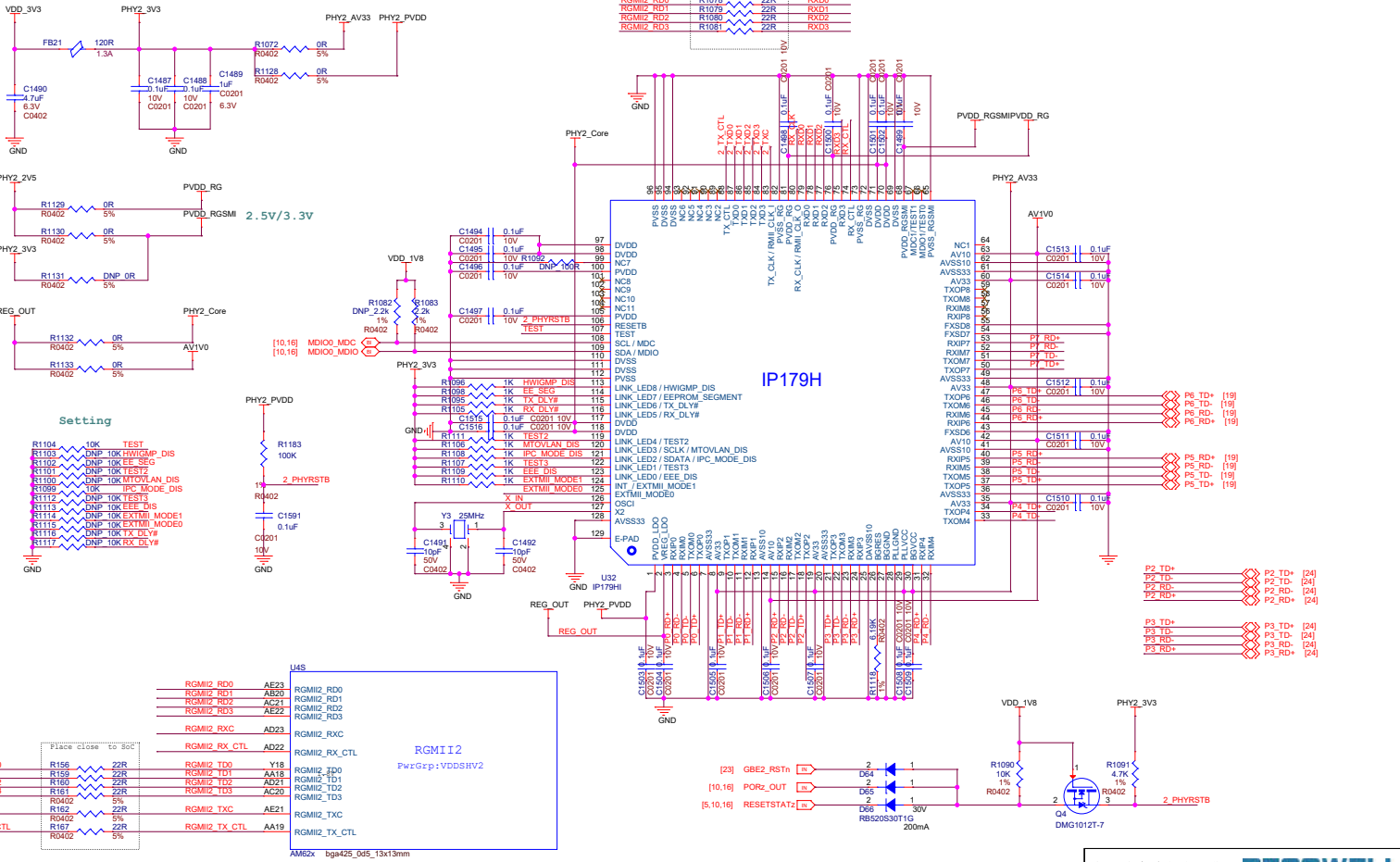
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eMMC/SD



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Gigabit Ethernet 1



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国产兆易创新型号
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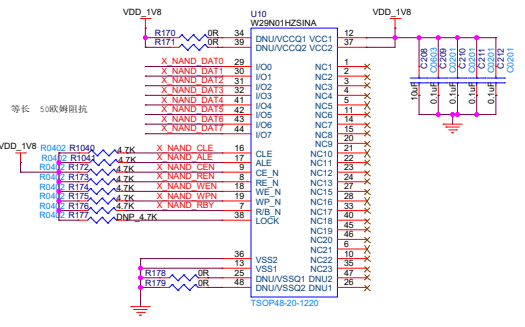
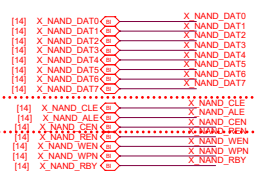
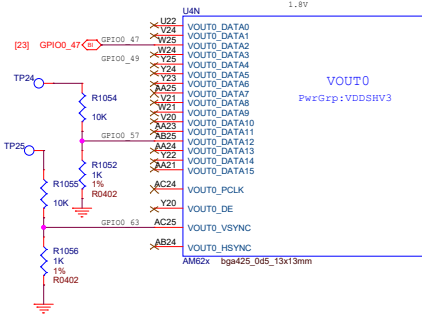
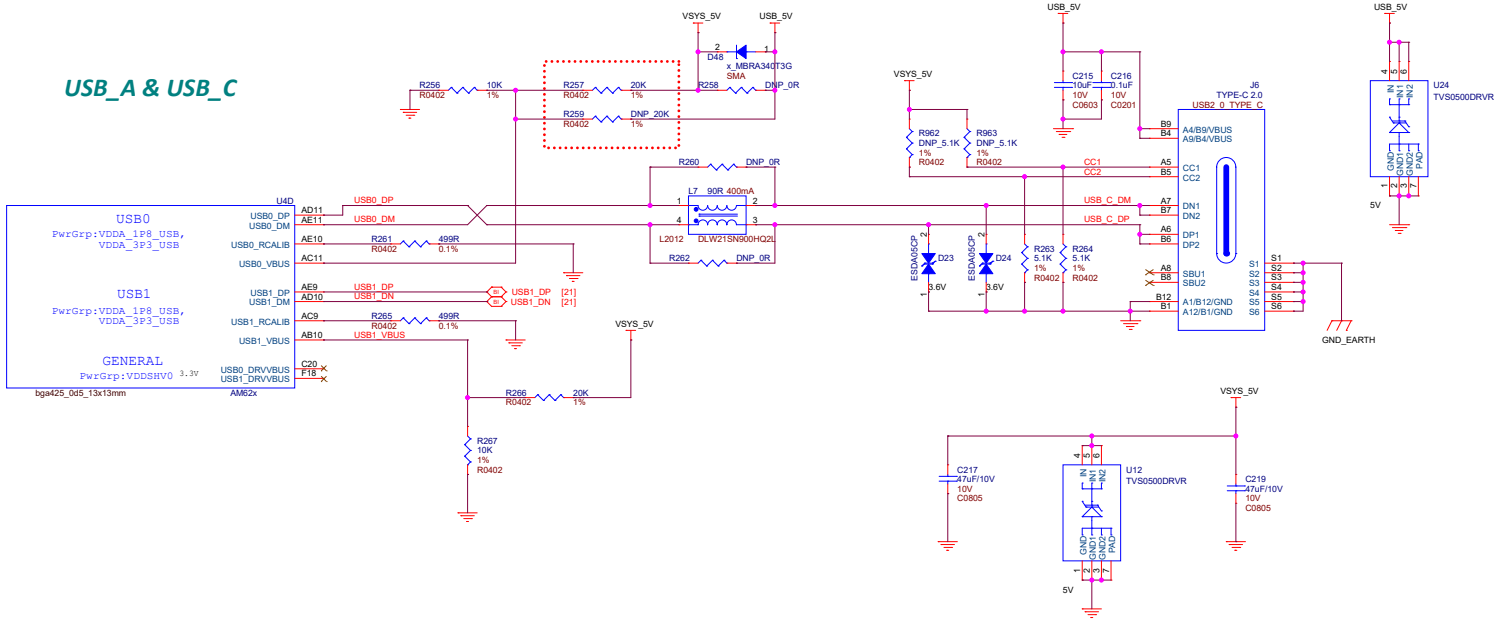


表 2-41 NAND Flash 接口布线要求

参数	要求
阻抗控制	50±10%单端
最大走线长度	TBD
信号等长	≤10mm
信号间最小距离	≥3H
时钟信号与其他信号间距	≥5H

USB_A & USB_C

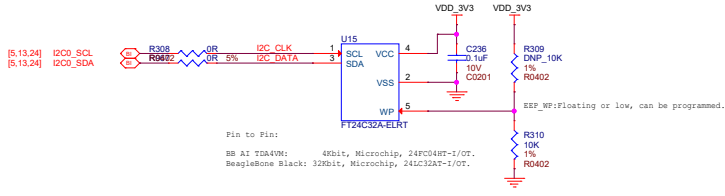


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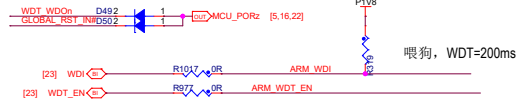
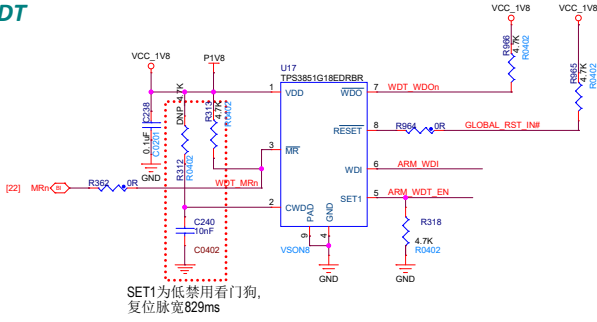
BOARD ID EEPROM 4Kbit

RTC

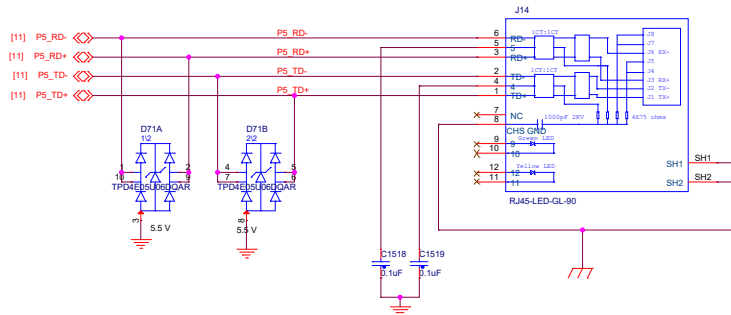
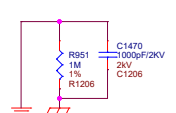
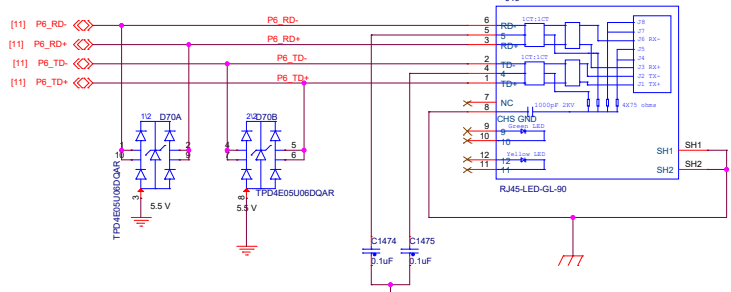
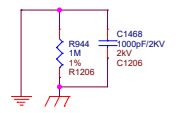
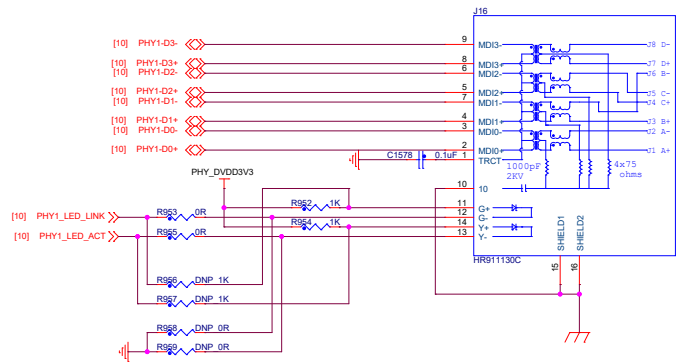


RTC电路放在电源板

WDT



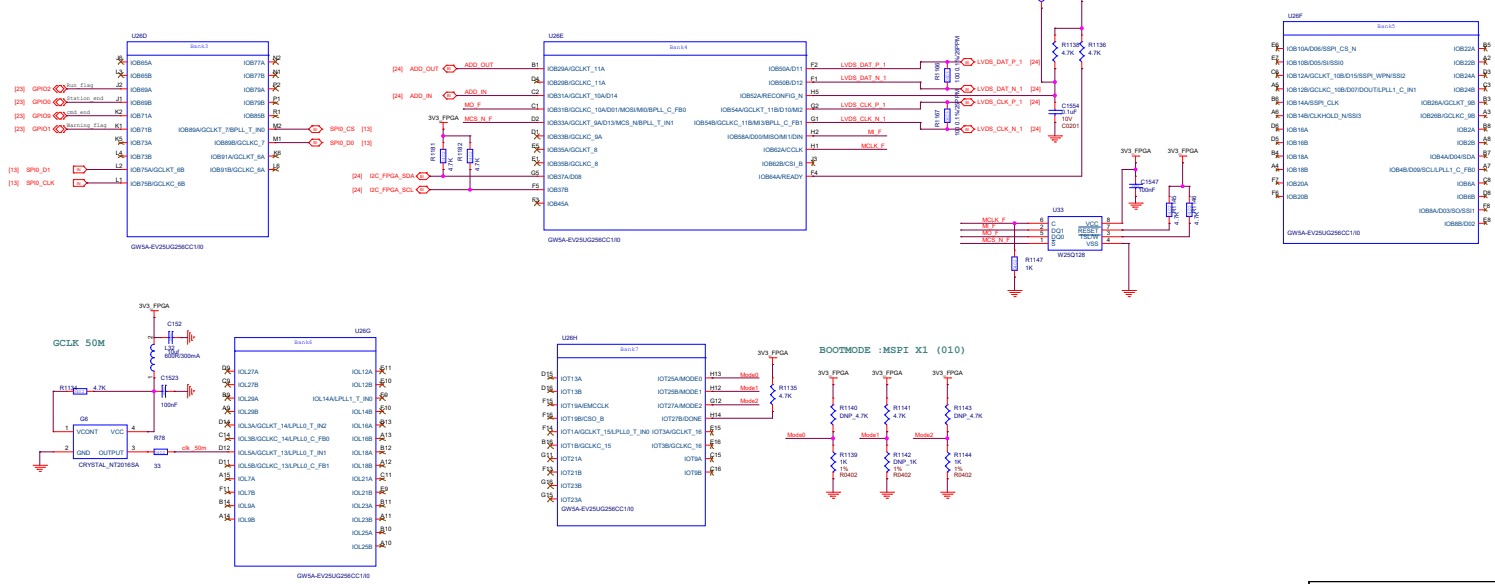
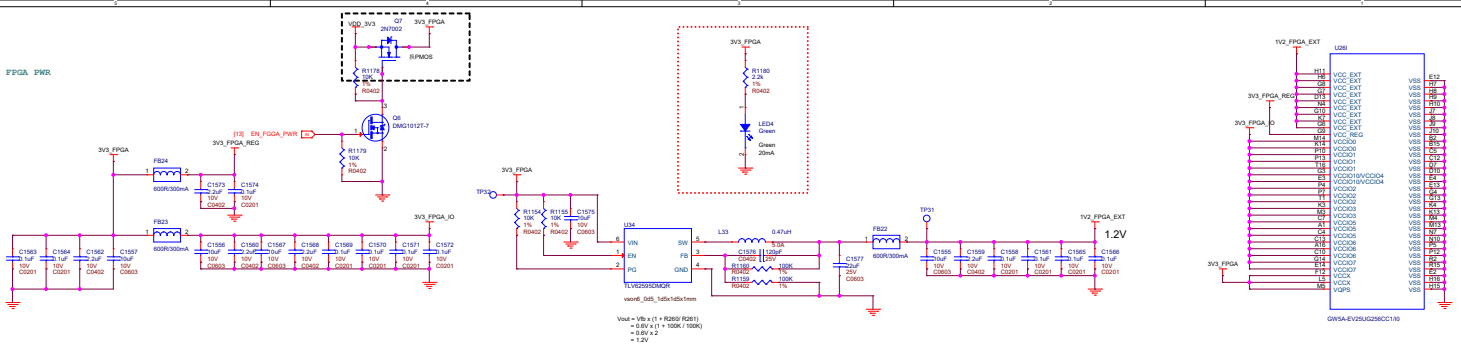
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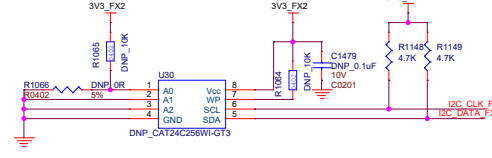
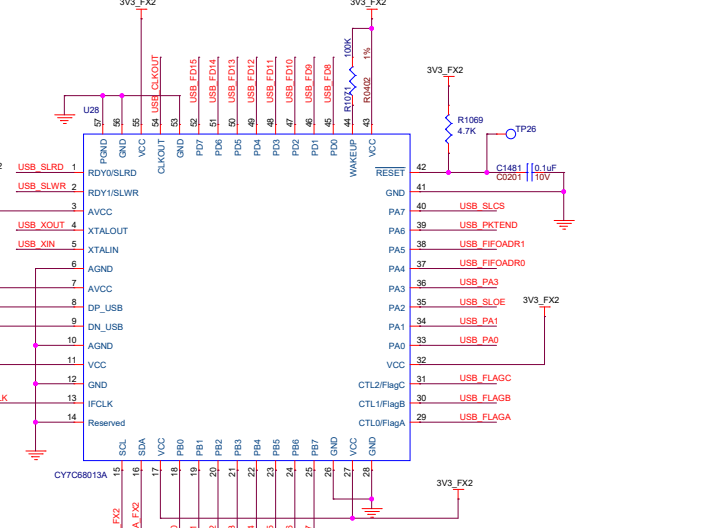
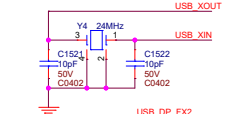
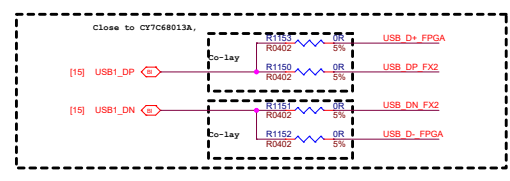
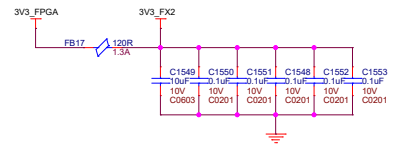
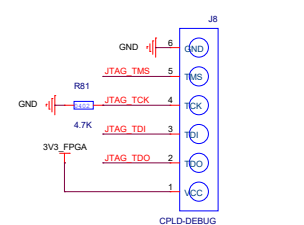
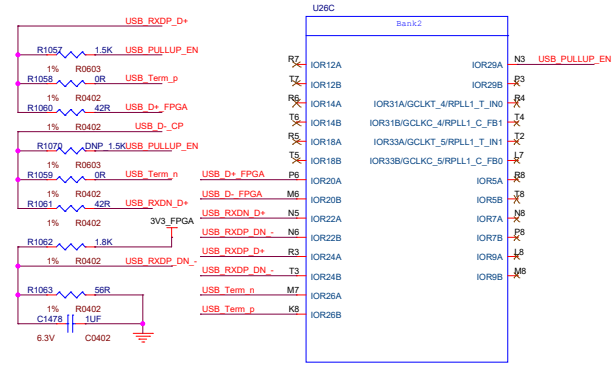
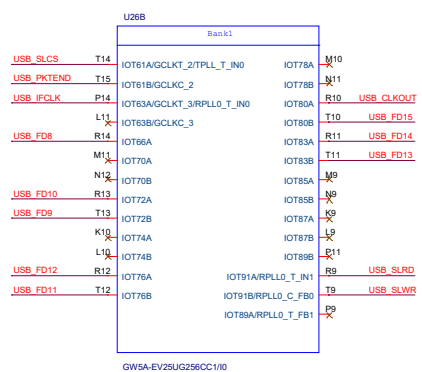
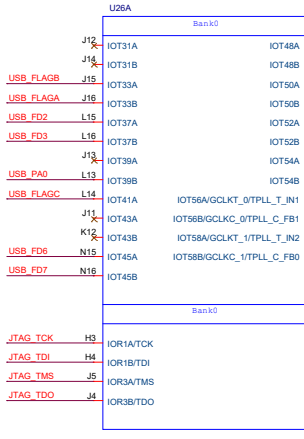


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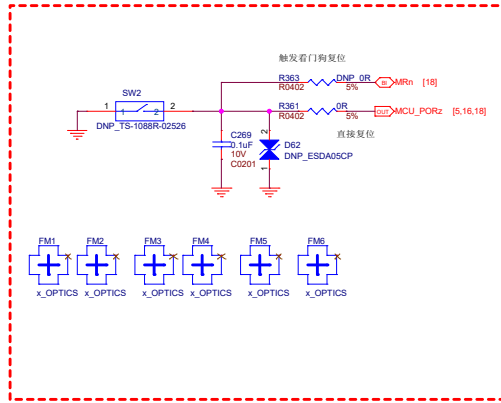
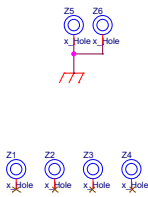
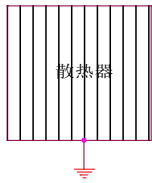
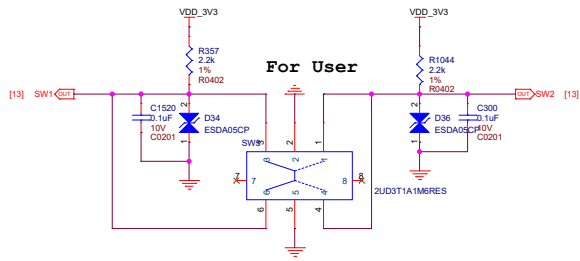
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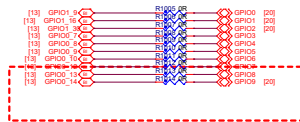
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3.3V



1.8V



