

Debug console showing Texas Instruments XDS110 USB Debug Probe connections and the call stack for the current execution.

Expression	Type
highCap	unknown
support1_8V	unknown
switched_to_v18	unknown
blockSize	unknown
blockCount	unsigned long long
size	unsigned long long
MMCSDConfigParams	struct MMCSD_ConfigParams
cmdComp	unknown
cmdTimeout	unknown
xferInProgress	unknown

Source code view showing the implementation of `HSMCSDIsHighSpeedSupported` in `MMCSD_v1_ild.c`. The function is called from `MMCSD_log` when the card supports SDR104, SDR50, or DDR50.

```

1560 MMCSD_log("With the response from CMD6, Trying each speed starting with prefera
1561
1562 /* Go through the groups in a preferential manner and select the highest speed
1563 for(i=0;i<5;i++)
1564 {
1565
1566 /* Go through the capabilities register and match with the card's speed
1567 to arrive at an agreeable speed */
1568 /* See if 104 is available in response bits 415-400, i.e bytes[12] and bytest
1569 /* Please refer to the status response to CMD6 in the physical specification *
1570
1571 if((cmd6_groups[i]==MMCSD_CMD6_GRP1_SDR104) && object->switched_to_v18 && HSM
cmd16_grp1_fn=MMCSD_CMD6_GRP1_SDR104;
MMCSD_log("CMD6 Says Card Supports SDR104\n");
} else if ((cmd6_groups[i]==MMCSD_CMD6_GRP1_SDR50) && object->switched_to_v18
cmd16_grp1_fn=MMCSD_CMD6_GRP1_SDR50;
MMCSD_log("CMD6 Says Card Supports SDR50\n");
} else if ((cmd6_groups[i]==MMCSD_CMD6_GRP1_DDR50) && object->switched_to_v18 && HSMCSDIsUHSSDR50Supported(hwAttrs->baseAddr) && (dataBuffer
cmd16_grp1_fn=MMCSD_CMD6_GRP1_DDR50;
MMCSD_log("CMD6 Says Card Supports DDR50\n");
1581 } else if ((cmd6_groups[i]==MMCSD_CMD6_GRP1_HS) && HSMCSDIsHighSpeedSupported(hwAttrs->baseAddr) && (dataBuffer[13] & (1<< MMCSD_CMD6_GRP1_H
cmd16_grp1_fn=MMCSD_CMD6_GRP1_HS;
MMCSD_log("CMD6 Says Card Supports HS\n");
1584 } else if ((cmd6_groups[i]==MMCSD_CMD6_GRP1_DEFAULT) && (dataBuffer[13] & (1<< MMCSD_CMD6_GRP1_DEFAULT
cmd16_grp1_fn=MMCSD_CMD6_GRP1_DEFAULT;
MMCSD_log("CMD6 Says Card Supports default\n");
1587 } else {
1588 continue;
1589 }
1590 ret=MMCSD_switch_card_curr_limit (handle,MMCSD_CMD6_GRP4

```

The function `HSMCSDIsHighSpeedSupported` is defined as follows:

```

334
335 regVal &= ~(CSL_MMCHS_CAPA_VS33_MASK | CSL_MMCHS_CAPA_VS30_MASK |
336 CSL_MMCHS_CAPA_VS18_MASK);
337 regVal |= voltMask;
338 HW_WR_REG32((baseAddr + CSL_MMCHS_CAPA), regVal);
339 }
340
341 uint32_t HSMCSDIsHighSpeedSupported(uint32_t baseAddr)
342 {
343 return (HW_RD_FIELD32((baseAddr + CSL_MMCHS_CAPA), CSL_MMCHS_CAPA_HSS));
344 }
345

```

Annotations in the image highlight the function call in the source code and the function definition in the header file.

We found to run into "HSMCSDIsHighSpeedSupported(uint32_t baseAddr)" in CCS single step debug.