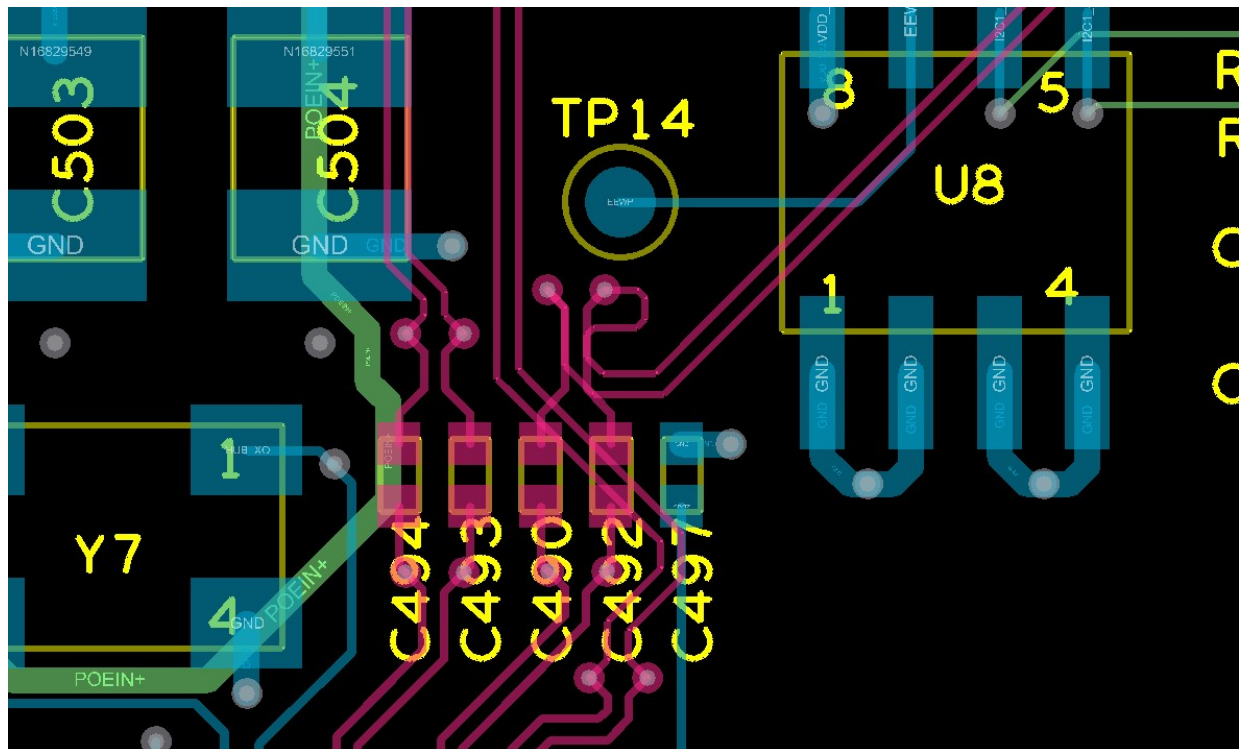
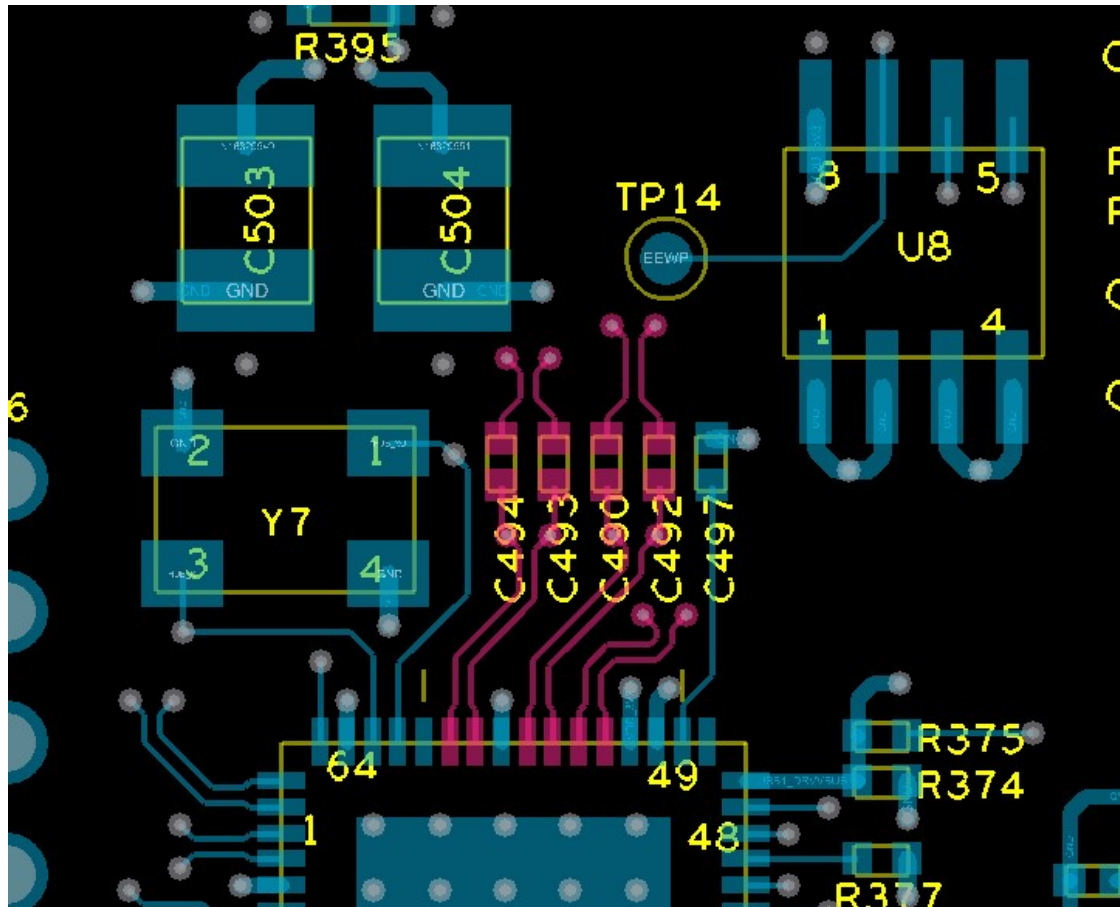


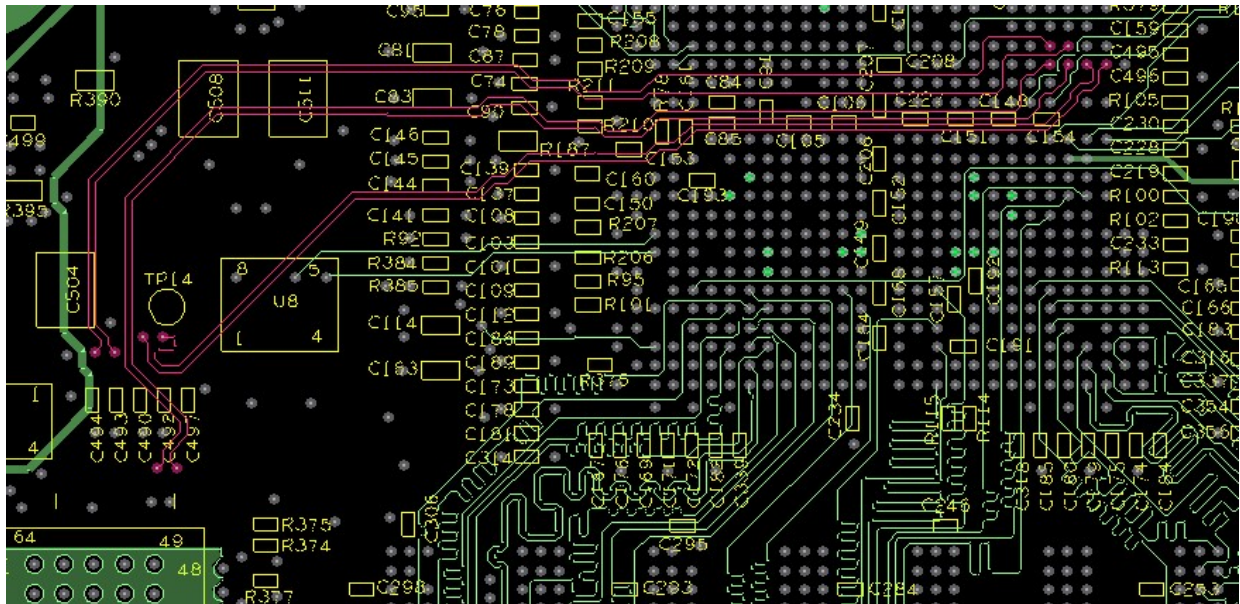
1) Zoomed view of C490 and C492



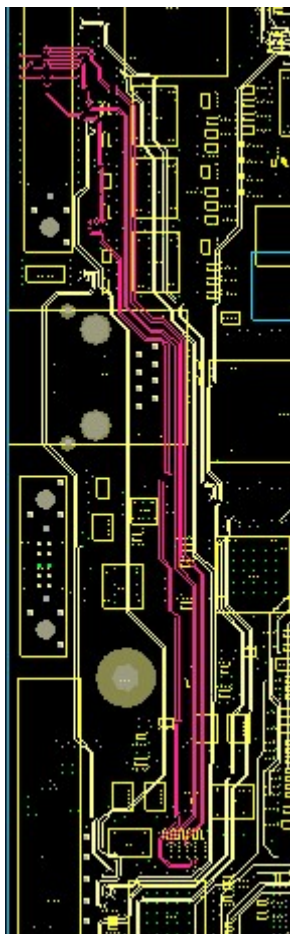
2) HUB signals are on Top layer



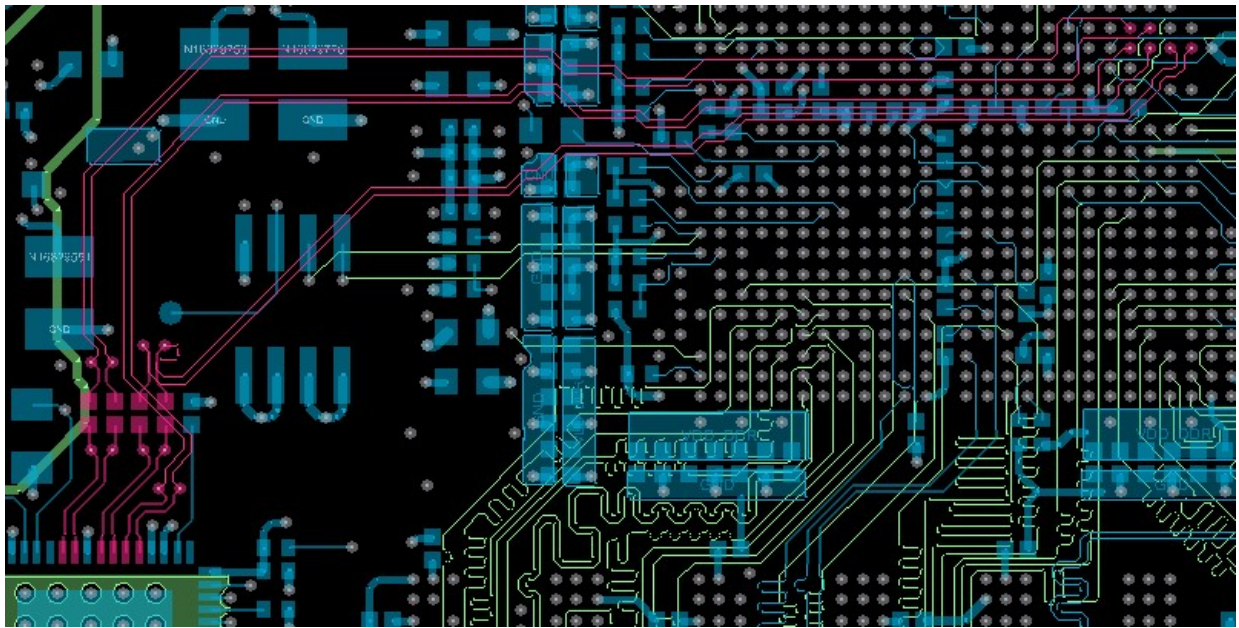
3) Processor to hub signals are on layer 5



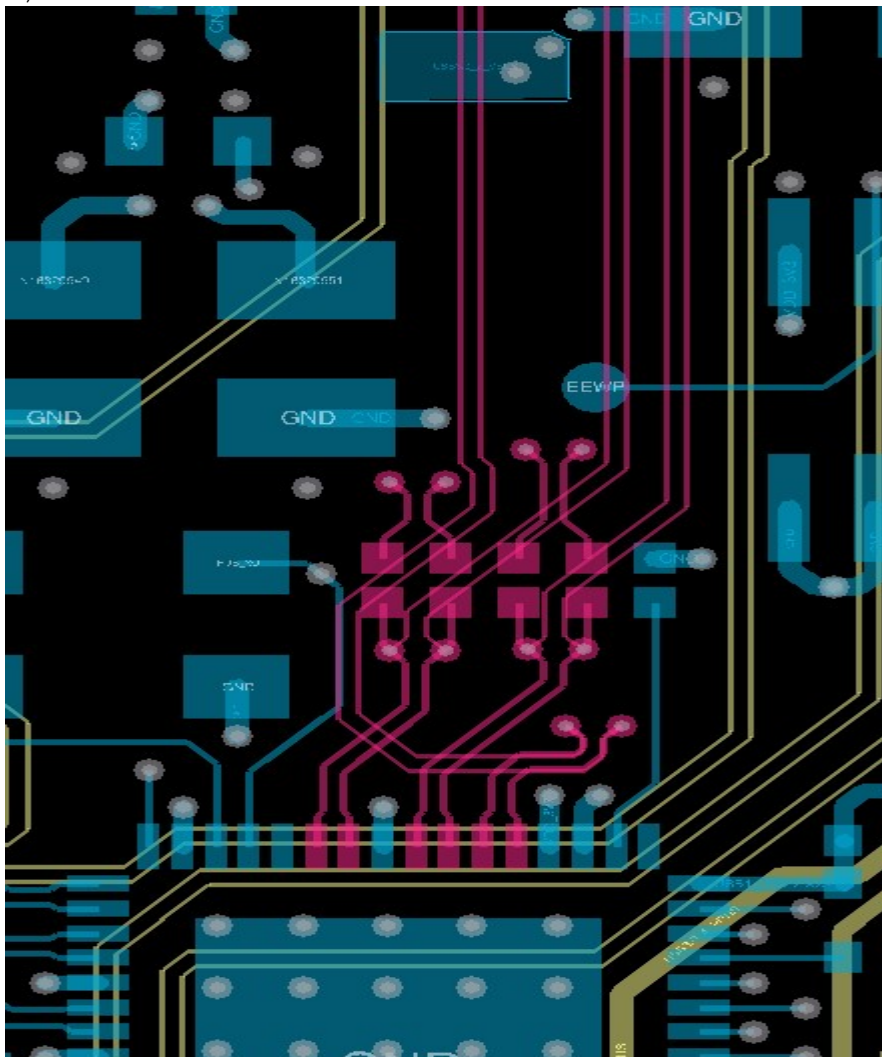
4) Those marked signals to processor to connector are on layer7



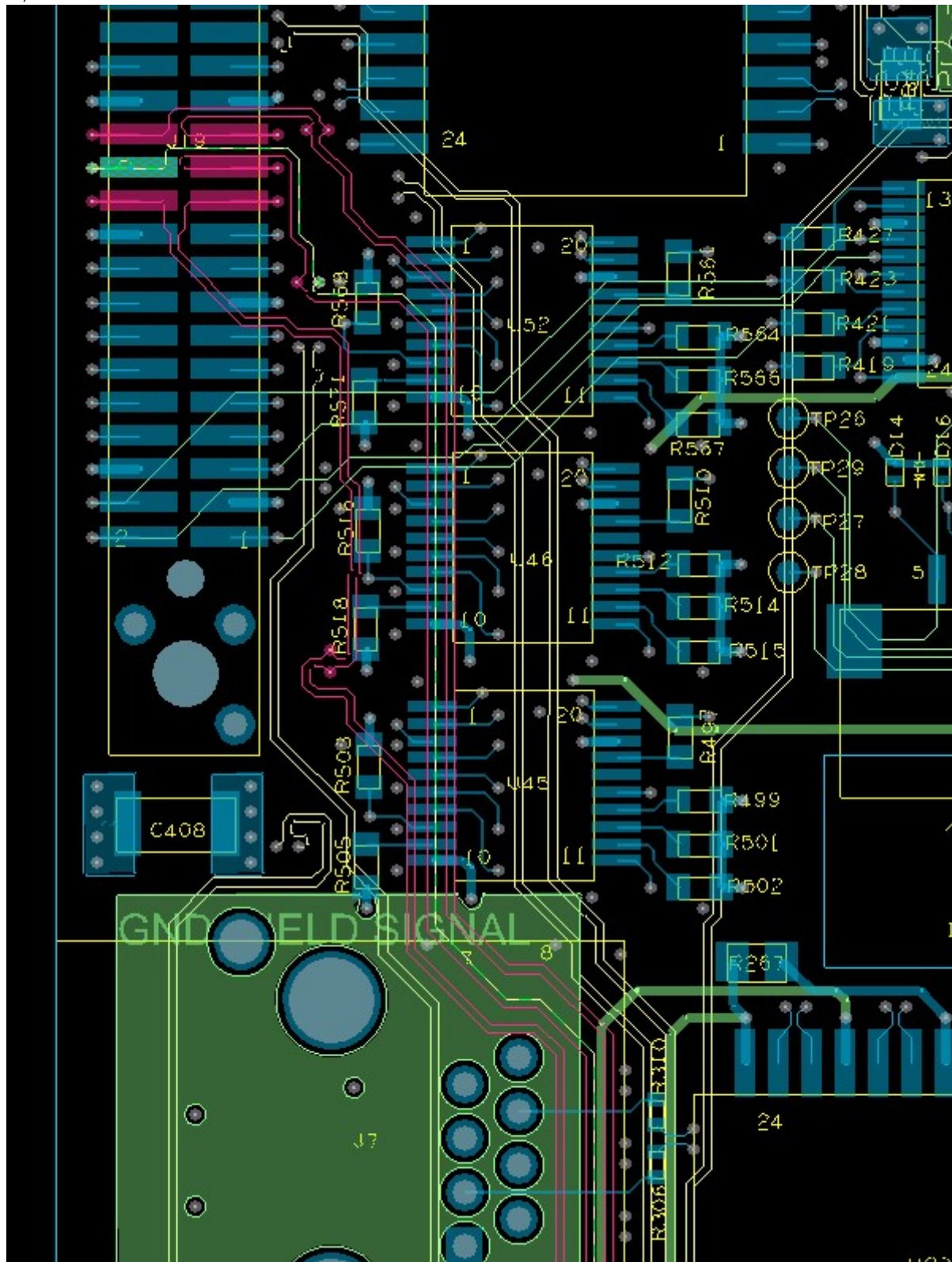
5) Processor to Hub connection



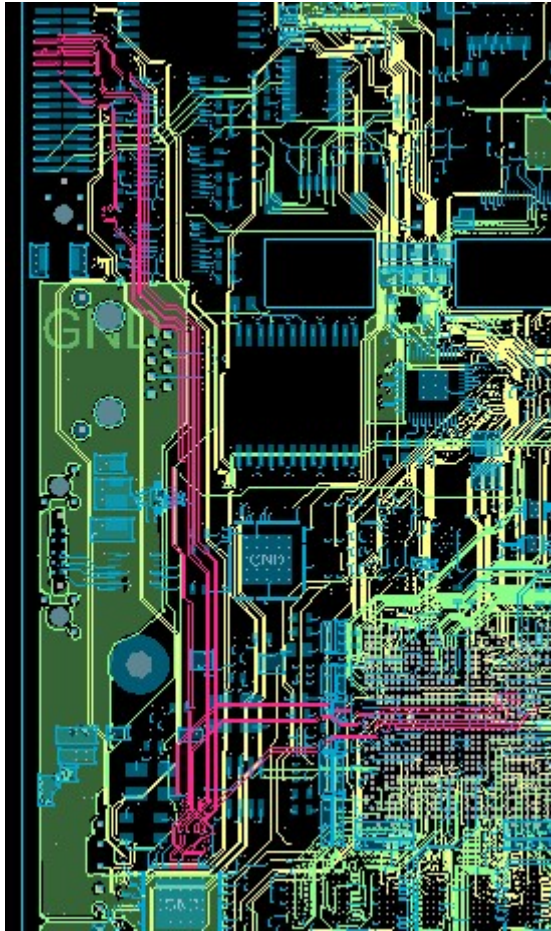
6) Marked signals to processor to connector are on layer7
a)



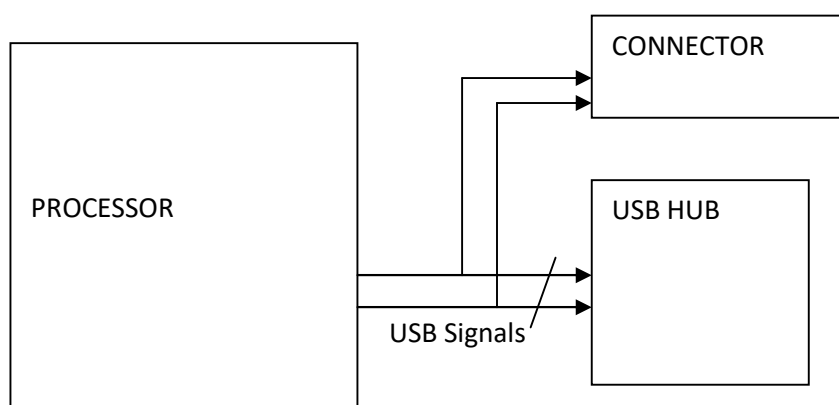
b)



7) All USB signals (HUB, processor and connector)



Block diagram of USB section:



USB Power Section to downstream ports:

