VDDPLL   VDDM_2   AVDD1

VDDM_1   DVDD

VDD_IO

VDD_IO	
3V3	O
1V8	X

U11
TP2855

100-ohm differential pairs

Pin	Ref	Value		Net	Signal	
51	R68	24.9R	CKP_1	CKP_1	MIPI_CKP	[5]
50	R69	24.9R	CKN_1	CKN_1	MIPI_CKN	[5]
53	R71	24.9R	D0P_1	D0P_1	MIPI_D0P	[5]
52	R72	24.9R	D0N_1	D0N_1	MIPI_D0N	[5]
56	R73	24.9R	D1P_1	D1P_1	MIPI_D1P	[5]
55	R75	24.9R	D1N_1	D1N_1	MIPI_D1N	[5]
59	R76	24.9R	D2P_1	D2P_1	MIPI_D2P	[5]
58	R77	24.9R	D2N_1	D2N_1	MIPI_D2N	[5]
62	R79	24.9R	D3P_1	D3P_1	MIPI_D3P	[5]
61	R80	24.9R	D3N_1	D3N_1	MIPI_D3N	[5]

Camera#1 Out =>
Camera#2 Out=>
Camera#3 Out=>
Camera#4 Out=>

NC 68
NC 69
NC 70
NC 71
NC 73
NC 74
NC 76
NC 77
NC 79
NC 80

VDD_IO
R85 4K7/DNI   R86 4K7/DNI

SCL 42 R88 22   SOC_I2C1_SCL [5,9]
SDA 43 R89 22   SOC_I2C1_SDA [5,9]

SAD0 83   SAD0
SAD1 84   SAD1

RSTB 41   V_DECODER_RST#

IRQ 46   TVI_IRQ [5]

TSTOP 2
TSTON 1

TEST 47
EPAD 89

VCC_3V3   AVDD3
L5 WE-74279271
TC1 22uF/6.3V   C39 0.1uF   C40 0.1uF   C55 0.1uF   C41 0.1uF
GND

VCC_3V3   AAD
L7 WE-74279271
TC2 22uF/6.3V
GND

VCC_3V3   VDD_IO
L6 WE-74279271
TC3 22uF/6.3V   C49 0.1uF   C50 0.1uF   C51 0.1uF   C46 0.1uF
GND

VDD_1V2   VDD
L8 WE-74279271
TC4 22uF/6.3V
GND

VDD_1V2   VDDM_1
L9 WE-74279271
TC6 22uF/6.3V   C61 0.1uF   C62 0.01uF   C63 0.1uF   C69 0.01uF
GND

VDD_1V2   VDD
L10 WE-74279271
TC5 22uF/6.3V
GND

VDD_1V2   AVDD1
L11 WE-74279271
TC7 22uF/6.3V   C72 0.1uF   C73 0.1uF   C74 0.1uF   C75 0.1uF
GND

VDD_1V2   DVD
L12 WE-74279271
TC8 22uF/6.3V
GND

VDD_IO
R90 4K7_NI   R91 4K7_NI
SAD0
SAD1

DVD
C80 0.01uF
GND

SAD1	SAD0	ADDR
LOW	LOW	0x88
LOW	HIGH	0x8A
HIGH	LOW	0x8C

R94 4K7   R95 4K7

TP48
TP52
TP55
TP59
TP62

[10] MIPI_D0P
[10] MIPI_D0N

100E

[10] MIPI_D1P
[10] MIPI_D1N

100E

[10] MIPI_CKP
[10] MIPI_CKN

100E

TP70
TP72
TP73

221 219
223 221
225 223
227 225
229 227
231 229
233 231
235 233
237 235
239 237
241 239
243 241
245 243
247 245
249 247
251 249
253 251
255 253
257 255
259 257
259

CSI-2

RESET

220 222
222 224
224 226
226 228
228 230
230 232
232 234
234 236
236 238
238 240
240 242
242 244
244 246
246 248
248 250
250 252
252 254
254 256
256 258
258 260
260

TP49
TP53
TP56
TP60
TP63

100E

MIPI_D2P [10]
MIPI_D2N [10]

100E

MIPI_D3P [10]
MIPI_D3N [10]

100E

TP68
GPIO1_31
CPSW_RGMII_INTn/PRU_IN
PORz_OUT [20,21]
SOC_WARM_RESETz [7]
RESETSTATz [9,10,12,18,
TP74

SODIMM-DDR4

Connector

GND
GND

SOC CSI INTERFACE

U10K
AM62 SoC_EVM

[5] EDGE_CSI0_RXP0   100E   AC15   CSI0_RXP0
[5] EDGE_CSI0_RXN0   AB14   CSI0_RXN0

[5] EDGE_CSI0_RXP1   100E   AE14   CSI0_RXP1
[5] EDGE_CSI0_RXN1   AD14   CSI0_RXN1

[5] EDGE_CSI0_RXP2   100E   AE13   CSI0_RXP2
[5] EDGE_CSI0_RXN2   AD13   CSI0_RXN2

[5] EDGE_CSI0_RXP3   100E   AC13   CSI0_RXP3
[5] EDGE_CSI0_RXN3   AB12   CSI0_RXN3

[5] EDGE_CSI0_RXCLKP   100E   AE15   CSI0_RXCLKP
[5] EDGE_CSI0_RXCLKN   AD15   CSI0_RXCLKN

CSI
PwrGrp:VDDA_1P8_CSIRX
1V8

Reserved Pins   AA12   RESERVED1
                Y15   RESERVED2

                AA14   CSI0_RXRCALIB

R66
499E_0.1%

DGND

AM62x SOC