

P1 : Full Speed code



P2 : Full Speed set

drivers\usb\musb\musb\_core.c

void musb\_start(struct musb \*musb)

{

 void \_\_iomem \*regs = musb->mregs;

 u8 devctl = musb\_readb(regs, MUSB\_DEVCTL);

 dev\_dbg(musb->controller, "<== devctl %02x\n", devctl);

 /\* Set INT enable registers, enable interrupts \*/

 musb->intrtxe = musb->epmask;

 musb\_writew(regs, MUSB\_INTRTXE, musb->intrtxe);

 musb->intrrxe = musb->epmask & 0xfffe;

 musb\_writew(regs, MUSB\_INTRRXE, musb->intrrxe);

 musb\_writeb(regs, MUSB\_INTRUSBE, 0xf7);

 musb\_writeb(regs, MUSB\_TESTMODE, 0);

 /\* put into basic highspeed mode and start session \*/

 musb\_writeb(regs, MUSB\_POWER, MUSB\_POWER\_ISOUPDATE

 /\* | MUSB\_POWER\_HSENAB xls//ed20191108\*/

 /\* ENSUSPEND wedges tusb \*/

 /\* | MUSB\_POWER\_ENSUSPEND \*/

 );

 musb->is\_active = 0;

 devctl = musb\_readb(regs, MUSB\_DEVCTL);

 devctl &= ~MUSB\_DEVCTL\_SESSION;

 /\* session started after:

 \* (a) ID-grounded irq, host mode;

 \* (b) vbus present/connect IRQ, peripheral mode;

 \* (c) peripheral initiates, using SRP

 \*/

 if (musb->port\_mode != MUSB\_PORT\_MODE\_HOST &&

 (devctl & MUSB\_DEVCTL\_VBUS) == MUSB\_DEVCTL\_VBUS) {

 musb->is\_active = 1;

 } else {

 devctl |= MUSB\_DEVCTL\_SESSION;

 }

 musb\_platform\_enable(musb);

 musb\_writeb(regs, MUSB\_DEVCTL, devctl);

}