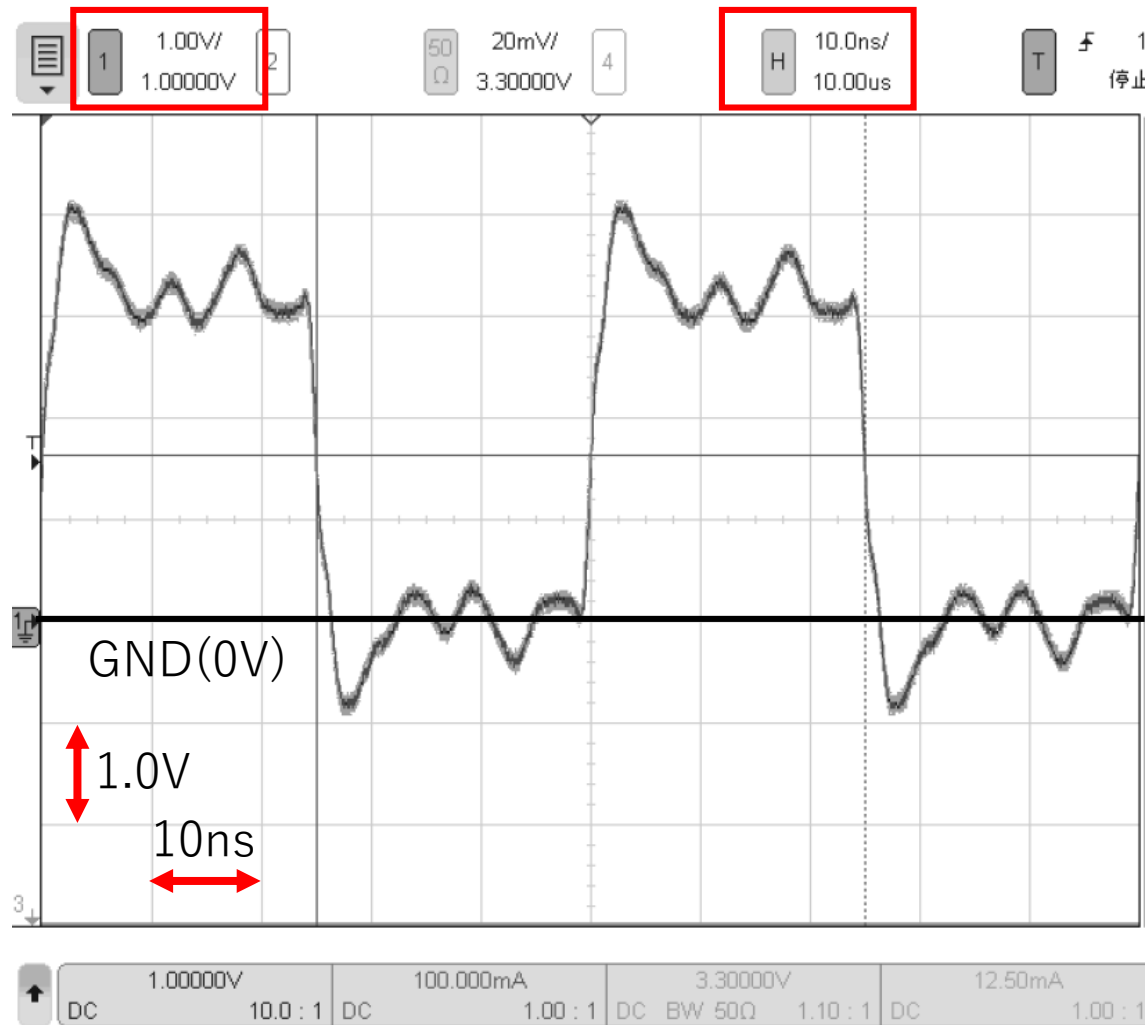
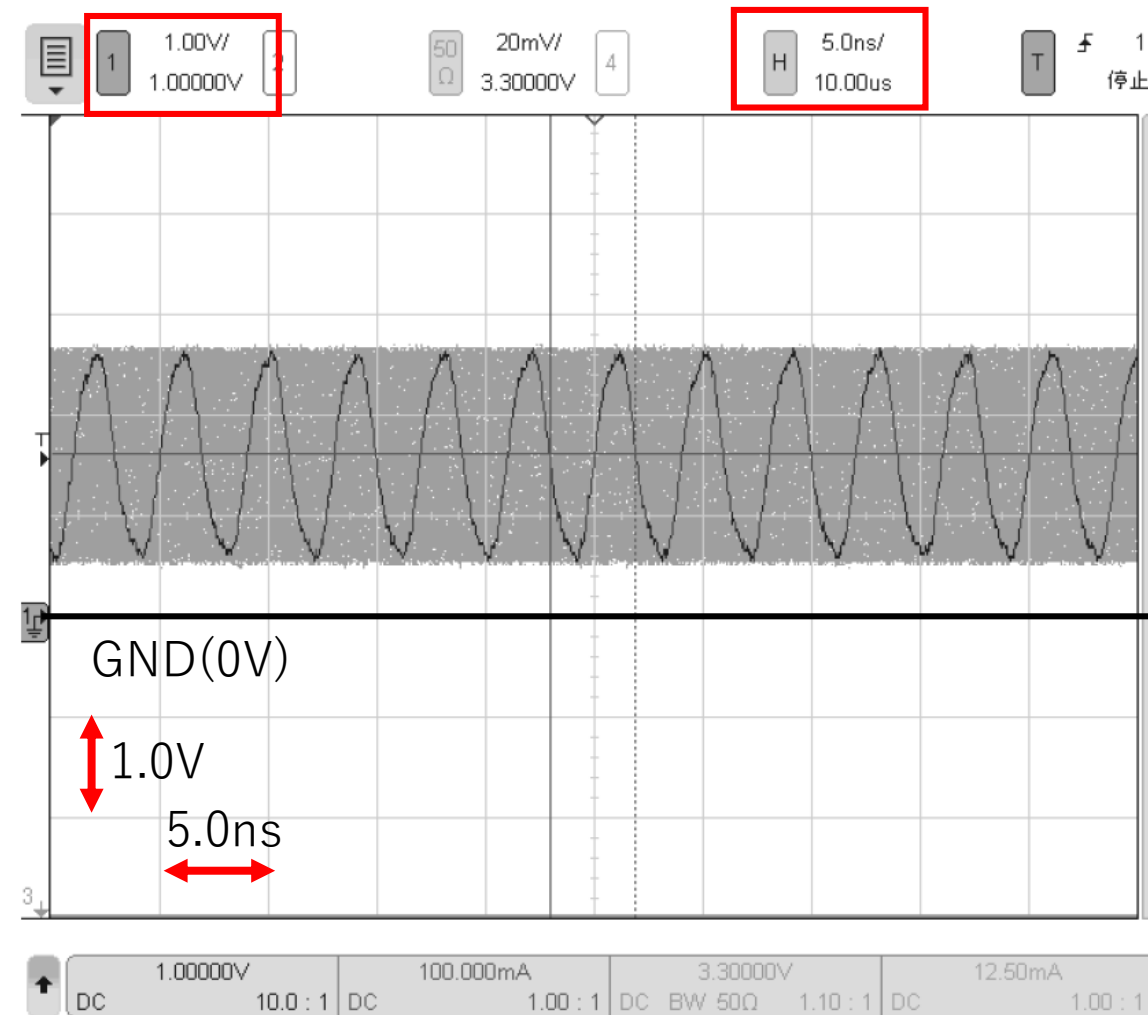


SYS_CLK1_DCLK
(10 μ s after the trigger)

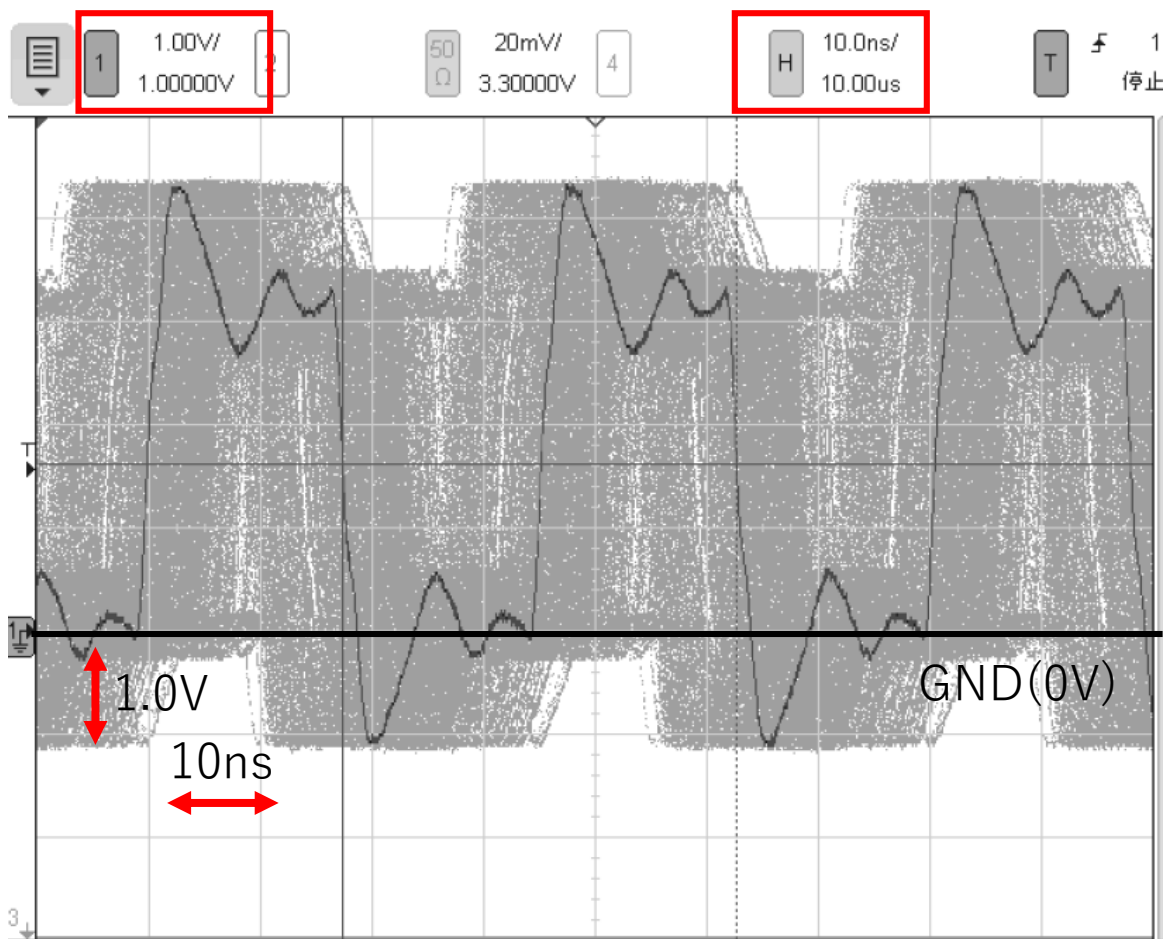


MPU_DCLK
(10 μ s after the trigger)

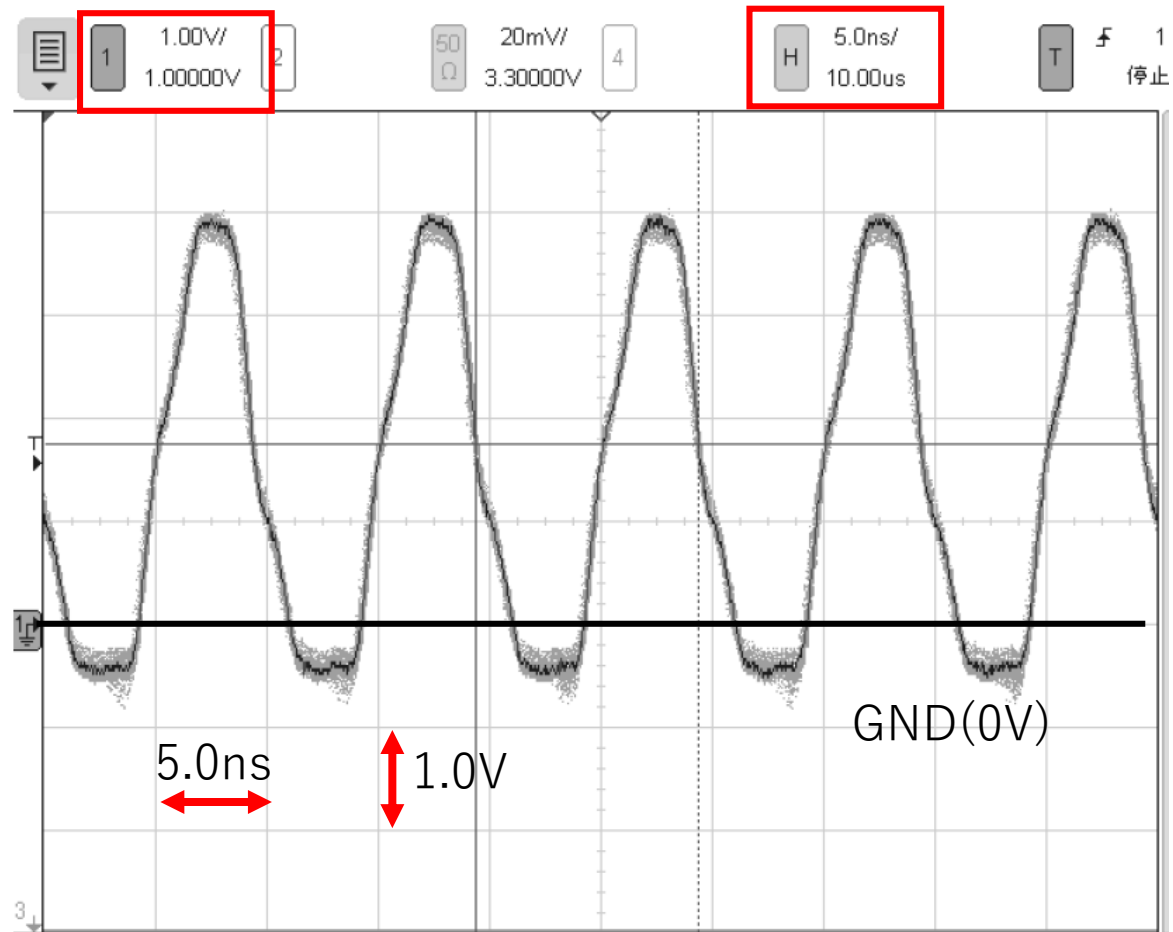


CLKOUT2 output

VIDEO1_DCLK
(10 μ s after the trigger)



PCIE2_DCLK
(10 μ s after the trigger)



CLKOUT2 output