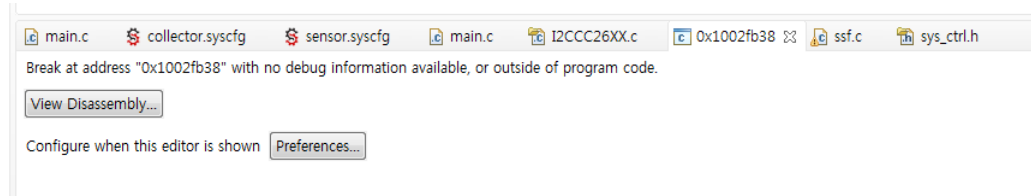


1. Debugging Start

```
459 /*!  
460  * @brief      "main()" function - starting point  
461  */  
462 int main(void)  
463 {  
464     Task_Params taskParams;  
465  
466     #ifndef USE_DEFAULT_USER_CFG  
467     macUser0Cfg[0].pAssertFP = macHalAssertHandler;  
468     #endif  
469  
470     /*  
471     Initialization for board related stuff such as LEDs  
472     following TI-RTOS convention  
473     */  
474     //PIN_init(BoardGpioInitTable);    // Changed by sklee  
475     Board_init();  
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477     //UART_init();  
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1355     //I2C439_Init();  
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1357     //I2C440_Init();  
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1359     //I2C441_Init();  
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1361     //I2C442_Init();  
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1363     //I2C443_Init();  
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1365     //I2C444_Init();  
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1367     //I2C445_Init();  
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1369     //I2C446_Init();  
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1371     //I2C447_Init();  
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1373     //I2C448_Init();  
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1375     //I2C449_Init();  
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1377     //I2C450_Init();  
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1379     //I2C451_Init();  
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1381     //I2C452_Init();  
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1383     //I2C453_Init();  
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1385     //I2C454_Init();  
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1387     //I2C455_Init();  
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1389     //I2C456_Init();  
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1391     //I2C457_Init();  
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1393     //I2C458_Init();  
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1395     //I2C459_Init();  
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1397     //I2C460_Init();  
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1399     //I2C461_Init();  
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1401     //I2C462_Init();  
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1403     //I2C463_Init();  
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1405     //I2C464_Init();  
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1407     //I2C465_Init();  
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1409     //I2C466_Init();  
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1411     //I2C467_Init();  
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1413     //I2C468_Init();  
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1415     //I2C469_Init();  
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1417     //I2C470_Init();  
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1419     //I2C471_Init();  
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1421     //I2C472_Init();  
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1423     //I2C473_Init();  
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1425     //I2C474_Init();  
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1427     //I2C475_Init();  
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1429     //I2C476_Init();  
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1431     //I2C477_Init();  
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1433     //I2C478_Init();  
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1435     //I2C479_Init();  
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1437     //I2C480_Init();  
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1439     //I2C481_Init();  
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1441     //I2C482_Init();  
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1443     //I2C483_Init();  
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1445     //I2C484_Init();  
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1447     //I2C485_Init();  
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1449     //I2C486_Init();  
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1451     //I2C487_Init();  
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1453     //I2C488_Init();  
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1455     //I2C489_Init();  
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1457     //I2C490_Init();  
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1459     //I2C491_Init();  
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1461     //I2C492_Init();  
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1471     //I2C497_Init();  
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1473     //I2C498_Init();  
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1475     //I2C499_Init();  
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1477     //I2C500_Init();  
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1479     //I2C501_Init();  
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1481     //I2C502_Init();  
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1483     //I2C503_Init();  
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1485     //I2C504_Init();  
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1487     //I2C505_Init();  
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1489     //I2C506_Init();  
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1491     //I2C507_Init();  
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1493     //I2C508_Init();  
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1495     //I2C509_Init();  
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1497     //I2C510_Init();  
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1499     //I2C511_Init();  
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1501     //I2C512_Init();  
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1503     //I2C513_Init();  
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1505     //I2C514_Init();  
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1507     //I2C515_Init();  
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1509     //I2C516_Init();  
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1511     //I2C517_Init();  
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1513     //I2C518_Init();  
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1515     //I2C519_Init();  
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1517     //I2C520_Init();  
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1523     //I2C523_Init();  
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1525     //I2C524_Init();  
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1527     //I2C525_Init();  
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1531     //I2C527_Init();  
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1533     //I2C528_Init();  
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1535     //I2C529_Init();  
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1537     //I2C530_Init();  
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1539     //I2C531_Init();  
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1541     //I2C532_Init();  
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1543     //I2C533_Init();  
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1545     //I2C534_Init();  
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1547     //I2C535_Init();  
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1551     //I2C537_Init();  
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1553     //I2C538_Init();  
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1555     //I2C539_Init();  
1556  
1557     //I2C540_Init();  
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1559     //I2C541_Init();  
1560  
1561     //I2C542_Init();  
1562  
1563     //I2C543_Init();  
1564  
1565     //I2C544_Init();  
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1567     //I2C545_Init();  
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1569     //I2C546_Init();  
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1571     //I2C547_Init();  
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1573     //I2C548_Init();  
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1577     //I2C550_Init();  
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1579     //I2C551_Init();  
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1581     //I2C552_Init();  
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1583     //I2C553_Init();  
1584  
1585     //I2C554_Init();  
1586
```

582	object->isOpen = false;		
	I2CCC26XX_init():		
00013d5c:	6840	ldr	r0, [r0, #4]
00013d5e:	2100	movs	r1, #0
00013d60:	F88010DC	strb.w	r1, [r0, #0xdc]
00013d64:	4770	bx	lr

4.Continuous Step Into run



	ti_sysbios_family_arm_m3_Hwi_excHandlerAsm_I:		
1002fb38:	F01E0F04	tst.w	lr, #4
1002fb3c:	BF14	ite	ne
1002fb3e:	F3EF8009	mrs	r0, psp
1002fb42:	4668	mov	r0, sp
1002fb44:	4685	mov	sp, r0

➔ Run until to Address 1002de1a(step into)

->Step Into run

```

426 */
427 xdc_Void Main_excHandler(UInt *excStack, UInt lr)
428 {
429     /* User defined function */
430     Main_assertHandler(MAIN_ASSERT_HWI_TIRTOS);
431 }
432
433 /*!

```

430	Main_assertHandler(MAIN_ASSERT_HWI_TIRTOS);		
	Main_excHandler():		
00013e56:	2004	movs	r0, #4
00013e58:	F7FDB842	b.w	Main_assertHandler

->Step Into run

244	*/		
245	void Main_assertHandler(uint8_t assertReason)		
246	{		
247	#if defined(ASSERT_LEDS)		
248	int toggleCount = 0;		
249	bool toggle = true;		
250			
251	Hwi_disable();		
252	while(1)		
253	{		
254	if(toggleCount == 0)		
255	{		
256	if(toggle == false)		
257	{		
258	{		
259	Board_Led_control(board_led_type_LED1, board_led_state_OFF);		
260	Board_Led_control(board_led_type_LED2, board_led_state_OFF);		
261	#if !defined(DeviceFamily_CC13X0) && !defined(DeviceFamily_CC26X0) && !defined(DeviceFamily_CC13X2) && !defined(DeviceFamily_CC26X2)		
262	Board_Led_control(board_led_type_LED3, board_led_state_OFF);		
263	Board_Led_control(board_led_type_LED4, board_led_state_OFF);		
264	#endif		
265	}		
266	else if(toggle == true)		
267	{		
268	Board_Led_control(board_led_type_LED1, board_led_state_ON);		
269	Board_Led_control(board_led_type_LED2, board_led_state_ON);		
270	#if !defined(DeviceFamily_CC13X0) && !defined(DeviceFamily_CC26X0) && !defined(DeviceFamily_CC13X2) && !defined(DeviceFamily_CC26X2)		

```

Main_assertHandler():
00010ee0: B51F      push    {r0, r1, r2, r3, r4, lr}
1545      {
00010ee2: F88D0000  strb.w   r0, [sp]
1546      if((pNV != NULL) && (pNV->writeItem != NULL))
00010ee6: 480B      ldr     r0, [pc, #0x2c]
00010ee8: 6800      ldr     r0, [r0]

```

->Step Into run

```

1542 Public function defined in ssf.h
1543 */
1544 void Ssf_assertInd(uint8_t reason)
1545 {
1546     if((pNV != NULL) && (pNV->writeItem != NULL))
1547     {
1548         /* Attempt to save reason to read after reset */
1549         (void)pNV->writeItem(nvResetId, 1, &reason);
1550     }
1551 }
1552

```

```

00010ee0: B51F      push    {r0, r1, r2, r3, r4, lr}
1545      {
00010ee2: F88D0000  strb.w   r0, [sp]
1546      if((pNV != NULL) && (pNV->writeItem != NULL))
00010ee6: 480B      ldr     r0, [pc, #0x2c]
00010ee8: 6800      ldr     r0, [r0]

```

->Step Into run

```

1544 void Ssf_assertInd(uint8_t reason)
1545 {
1546     if((pNV != NULL) && (pNV->writeItem != NULL))
1547     {
1548         /* Attempt to save reason to read after reset */
1549         (void)pNV->writeItem(nvResetId, 1, &reason);
1550     }
1551 }
1552

```

```

00010ee2: F88D0000  strb.w   r0, [sp]
1546      if((pNV != NULL) && (pNV->writeItem != NULL))
00010ee6: 480B      ldr     r0, [pc, #0x2c]
00010ee8: 6800      ldr     r0, [r0]
00010eea: B168      cbz     r0, #0x10f08

```

->Step Into run

```

461 __STATIC_INLINE void
462 SysCtrlSystemReset( void )
463 {
464     // Disable CPU interrupts
465     CPUcpsid();
466     // Write reset register
467     HWREGBITW( AON_PMCTL_BASE + AON_PMCTL_O_RESECTL, AON_PMCTL_RESECTL_SYSRESET_BITN ) = 1;
468     // Finally, wait until the above write propagates
469     while ( 1 ) {
470         // Do nothing, just wait for the reset (and never return from here)
471     }
472 }

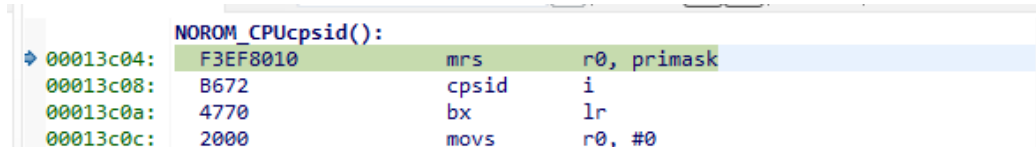
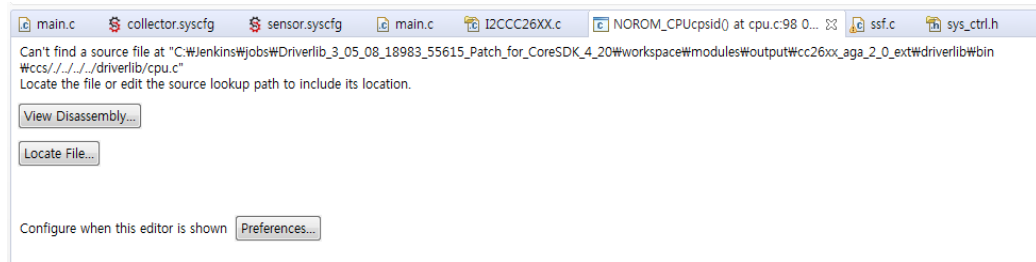
```

```

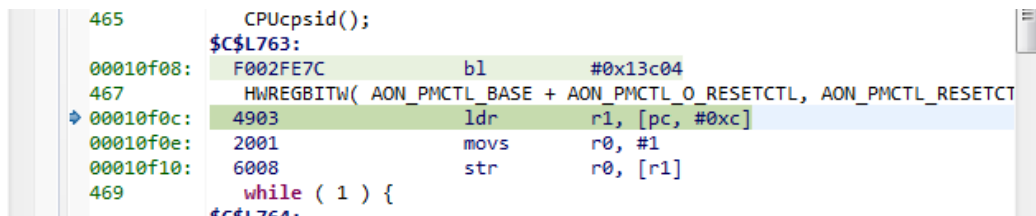
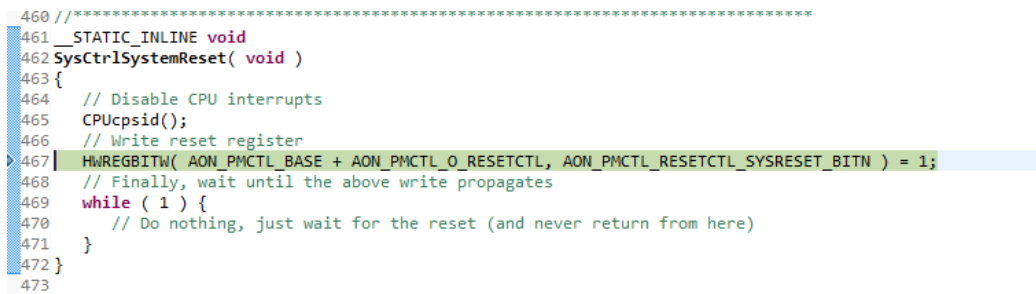
$C$L763:
00010f08: F002FE7C  bl      #0x13c04
467      HWREGBITW( AON_PMCTL_BASE + AON_PMCTL_O_RESECTL, AON_PMCTL_RESECTL
00010f0c: 4903      ldr     r1, [pc, #0xc]

```

->Step Into run



->Step Into run



->Step Into run

