**CASE 1:**

**Application Project cmd file contents**

MEMORY

{

**VECTORS (X) : origin=0x00000000 length=0x00000020**

**FLASH0 (RX) : origin=0x00000020 length=0x0013FFE0**

STACKS (RW) : origin=0x08000000 length=0x00001500

RAM (RW) : origin=0x08001500 length=0x0002EB00

/\* USER CODE BEGIN (2) \*/

/\* USER CODE END \*/

}

sys\_intvecs.asm file contents

.sect ".intvecs"

.arm

;-------------------------------------------------------------------------------

; import reference for interrupt routines

.ref \_c\_int00

.ref \_dabort

.ref phantomInterrupt

.def resetEntry

;-------------------------------------------------------------------------------

; interrupt vectors

resetEntry

b \_c\_int00

undefEntry

b undefEntry

svcEntry

b svcEntry

prefetchEntry

b prefetchEntry

b \_dabort

b phantomInterrupt

ldr pc,[pc,#-0x1b0]

ldr pc,[pc,#-0x1b0]

In the above CASE the application executes properly when debugged standalone without debugger.

/\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*/

**CASE 2 )**

**Application Project cmd file contents**

MEMORY

{

VECTORS (X) : origin=0x00020000 length=0x00000020

FLASH0 (RX) : origin=0x00020020 length=0x0011FFE0

STACKS (RW) : origin=0x08000000 length=0x00001500

RAM (RW) : origin=0x08001500 length=0x0002EB00

/\* USER CODE BEGIN (2) \*/

/\* USER CODE END \*/

}

**sys\_intvecs.asm file contents are same as CASE 1**

**In Application cmd file if the above address are used, the application doest not run, when it is exected standalone without bootloader.**

**the lauchpad turns fulltime ON and program is not debuggable.**