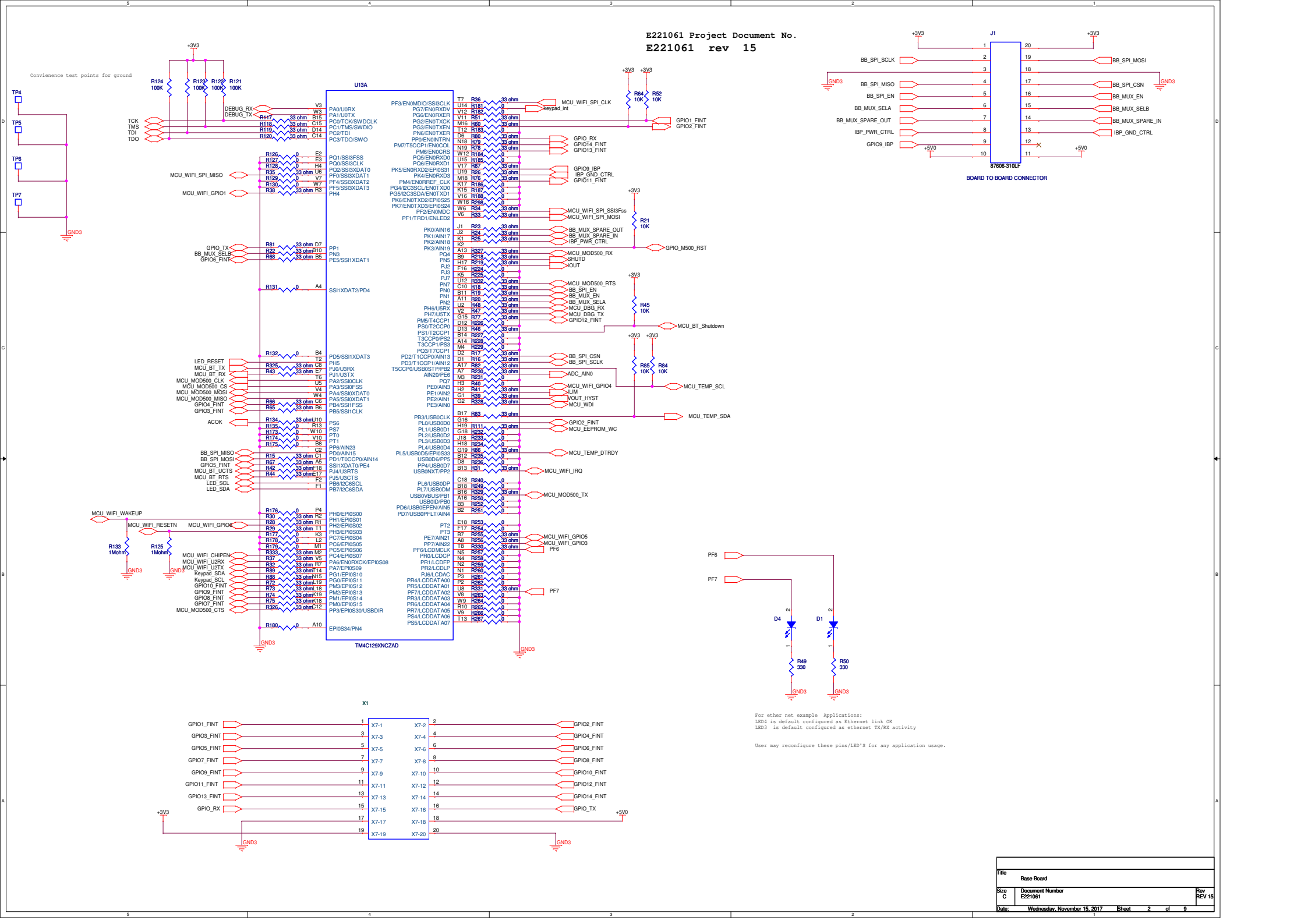


History of Changes

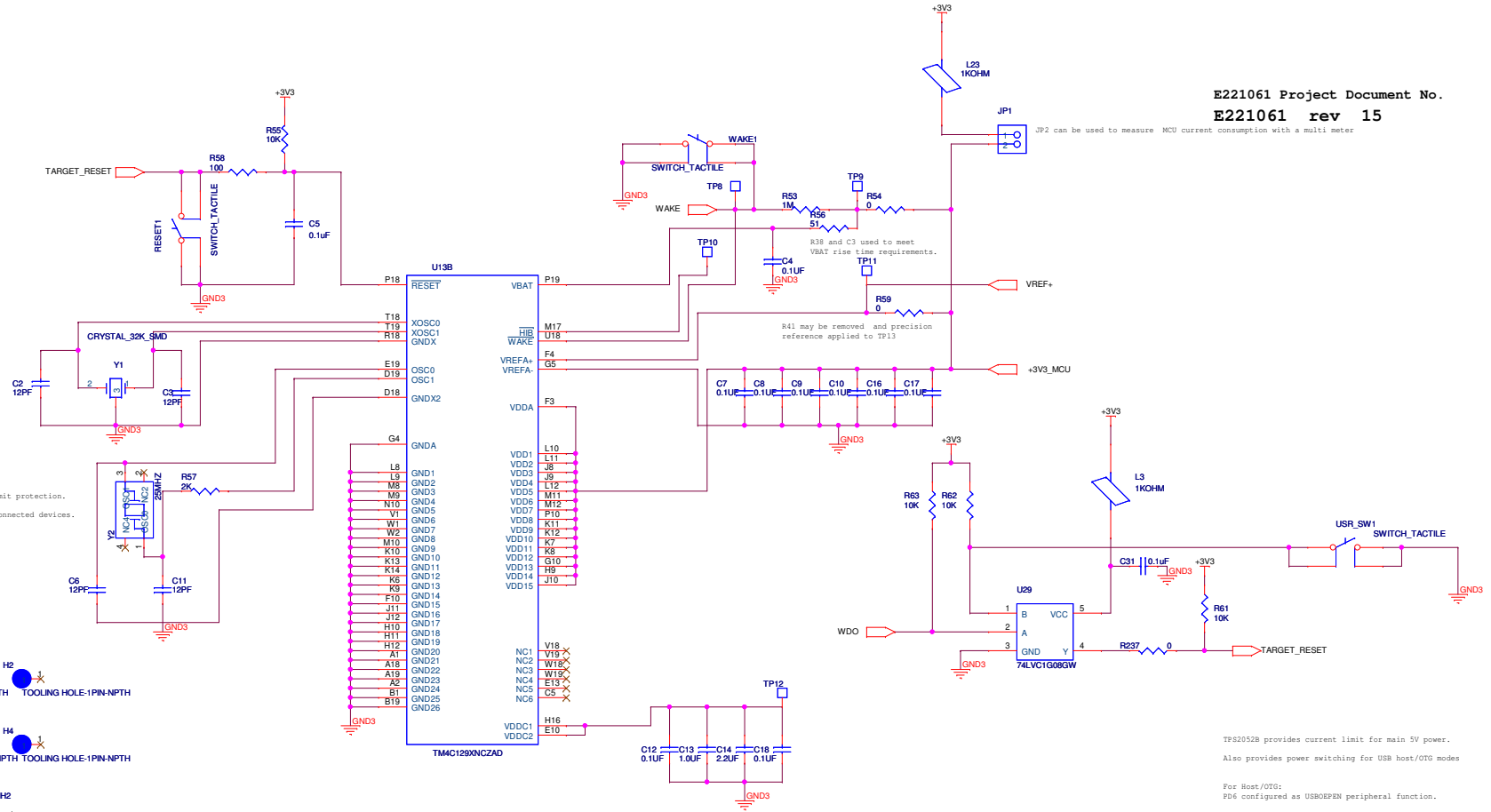
S.NO	DATE	Version	Revision	Remarks
1	03.10.2016	0	Original	
			1. U1G82 removed	
			2. Power management section has been changed	
2	17.10.2016	1	3. Debug interface circuit added	
			4. Model 500 interface circuit added	
			5. Keypad interface added	
			6. LED interface added	
3	18.10.2016	1.1	1. U10[SCL & SDA] made NC	
			2. gnd point added in J26 connector	
			3. Test points connected with keypad and LED section	
			1. X1 connector has been added	
			2. X6 & X7 connectors removed	
			3. JF1, JF4 & JF5 removed	
			4. X11 connector removed	
			5. U6 circuit removed	
			6. VCC_BRD changed to +3V3	
			7. R113 removed	
			8. CC3200 section removed	
			9. J22, J23 removed	
			10. U12 & U11 removed	
			11. U5, U44, U7, U9 Removed	
			12. U10[SCL & SDA] made connection with GPIO's	
			13. F1 fuse has been added in +12V line in power management	
4	10.11.2016	2	14. PH connector part number changed to B4B-PH-SM4-TB	
			15. J26 connector part number changed to BM20B-2PDSS-TF(LF) (SN)	
			16. CC2564MODA added	
			17. SM74AVC4T74RSV added	
			18. LP2985-18DBVR SOT23-5 added	
			19. Model 500 signal interface added	
			20. IBP signal interface added.	
			21. Bluetooth interface added.	
			22. Debug interface added	
			23. Programming interface added.	
			24. Keypad interface added	
			25. GPIO interface added.	
			26. Temperature IC interface added.	
5	14.11.2016	3	1. ATWINC1500-MR circuit added.	
			2. WiFi interface added	
			1. M24512-A125 added	
			2. MAX6369 added	
			3. SW1, SW2, SW3, R73, R74, R75, R76, R77, R78, R79, R80, R86, R82, R84, R85, R87, R88, R89, Q1, Q2, Q3, D5, D6, D7 removed	
6	15.11.2016	4	4. ESP Diode part number changed to ESDA5V1BC5	
7	29.11.2016	4.1	1. J8 connector part number updated	
			2. signal mapping updated	
8	05.12.2016	4.2	1. Symbols verified and updated	
9	03.01.2017	5.0	1. Baseboard Power filter circuit Added	
			1. Power Management circuit removed	
10	06.01.2017	6.0	2. PCA9522 driver has been replaced instead of TCA9535PW	
			3. J5 connector connections modified	
			1. TPS2052B_DRB_8 section removed	
			2. Ferrite Beads symbols commonized	
11	09.01.2017	7.0	3. VDDIO 1.8V 100mA circuit removed	
			4. Gnd and Power reference name updated	
			1. I/O Port Symbols added	
12	16.01.2017	7.1	2. Symbols verified and updated	
			1. Global symbols updated	
13	17.01.2017	7.2	2. Symbols verified and updated	
			1. 74LVC1G08GW IC added	
14	19.01.2017	7.3	2. Ferrite Beads symbols added	
			3. Resistors added	

History of Changes

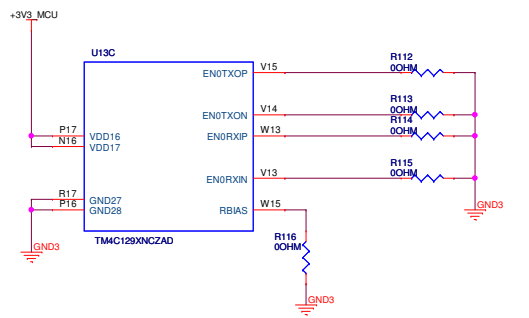
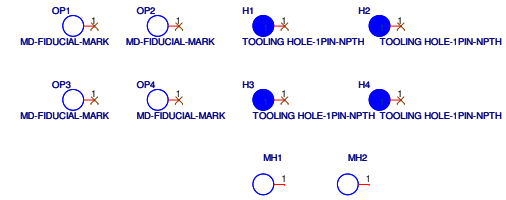
S.NO	DATE	Version	Revision	Remarks
15	25.01.2017	7.4	1. D2, D3, R51, R52, JF2 Removed	
			2. 10k Pull up resistor included for WDO	
			3. Duplicate net names removed	
			4. Pull up resistor added at enable pin of U84D9 IC	
			5. 10K pull up resistor added at KEY_SPARE1&KEY_SPARE2	
			6. Ferrite bead added at J5 connector	
			7. Signal routed at O/P CONNECTOR	
			8. TMP006 has been replaced with TMP112	
16	27.01.2017	7.5	1. Ground symbols updated	
17	7.02.2017	7.6	1. 87606-303LF has been replaced with DF1BZ-6DP-2.5DSA	
			2. Output connector has been replaced with DF1BZ-12DP-2.5DSA	
18	20.02.2017	8	1. TM4C1294NCPDT has been replaced by TM4C129KNCZAD	
19	21.02.2017	9	1. U3 & U1 Removed	
			2. U6 IC, J24&J25 Connectors included	
			3. Global symbols updated	
20	23.02.2017	10	1. J31 & T1, D9 Included	
21	27.02.2017	11	1. OTG Section Removed.	
			2. JTAG/DEBUG lines added.	
			3. Included for TM4C123G IC	
			4. Pin mapping updated as per pinlist 07	
22	02.03.2017	12	1. J29 Connector removed.	
			2. Global symbols updated	
23	7.03.2017	13	1. Global symbols updated	
24	31.10.2017	14	1. Power sections removed	
			2. Wifi section removed	
			3. Resistors added	
			4. J31 connector 20 pins replaced by 14 pin connector	
			5. U9 Ic removed	
			6. U19 & U20 partnumber changed	
24	6.11.2017	15	1. J10 connector 14 pins replaced by 20 pin connector	
			2. Capacitors & Resistors added	
			3. U9 Ic & J3 connector added	
			4. Diode symbols added	
			5. J5 connector net names updated	



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Power control Jumper:  
 1) To power from debug install jumper on pins 5-6  
 2) To power from Target USB install jumper on pins 3-4  
 3) To power from boosterpack 5V install jumper on pins 1-2  
 this is also off position if booster pack does not supply power4.  
 when powered from booster pack TP2052B does not provide current limit protection.  
 when power by booster pack, USB host mode does not supply power to connected devices.

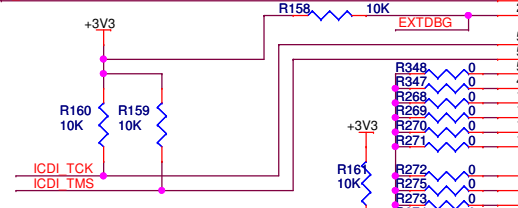


TP2052B provides current limit for main 5V power.  
 Also provides power switching for USB host/OTG modes

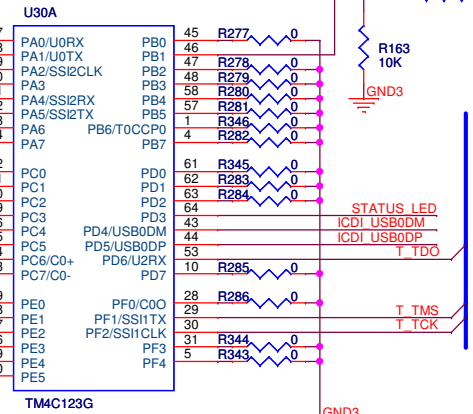
For Host/OTG:  
 PD6 configured as USBOPEN peripheral function.  
 PD4 configured as individual pin interrupt. Indicates power fault on USB bus. USBOPEN peripheral pin not available due to pin mux and use on Booster packs.  
 USB Host mode does not supply power to devices when powered from a boosterpack.  
 For application that do not use USB.  
 Configure PD6 as input with internal pull-down enabled. turn off power to TARGET\_VBUS

TARGET\_DEBUG/COMM

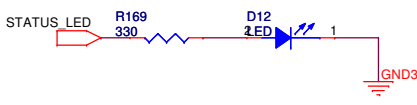
- T\_UTX
- T\_URX
- T\_TCK
- T\_TMS
- T\_TDO
- T\_TDI
- T\_RESET



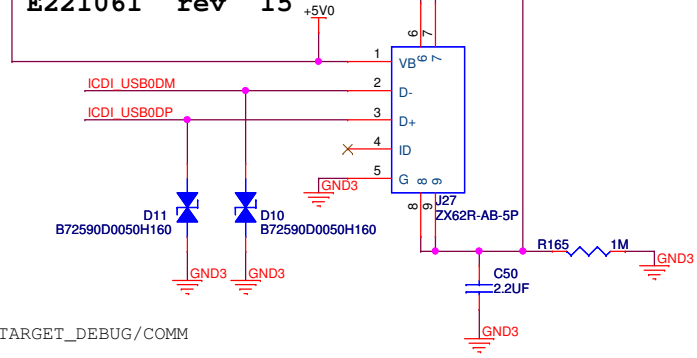
ICDI JTAG



ICDI STATUS LED

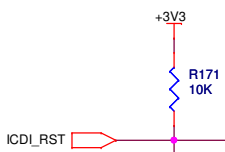
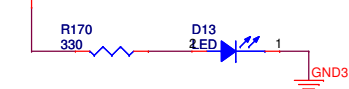


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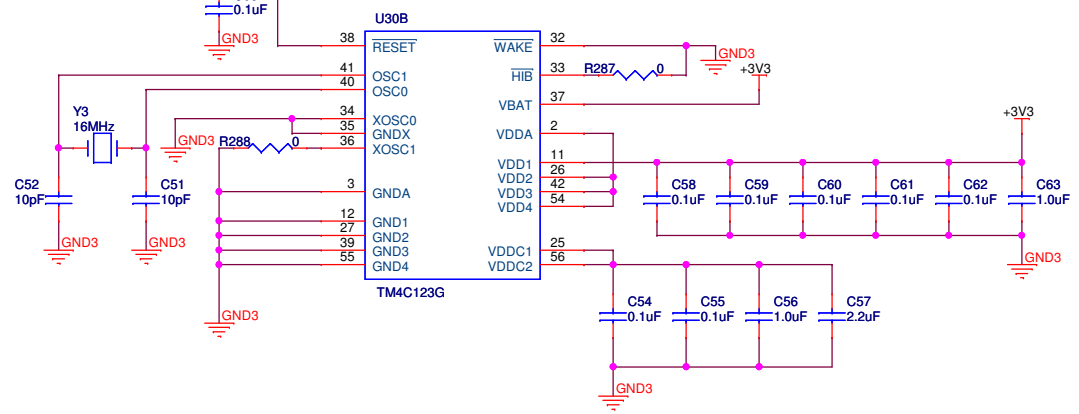
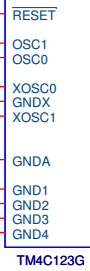


TARGET\_DEBUG/COMM

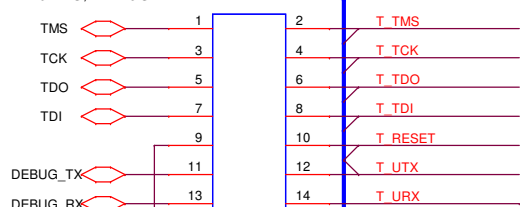
POWER LED



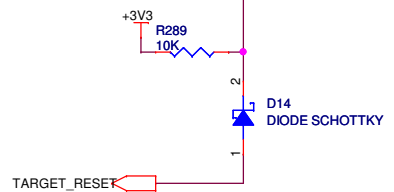
U30B



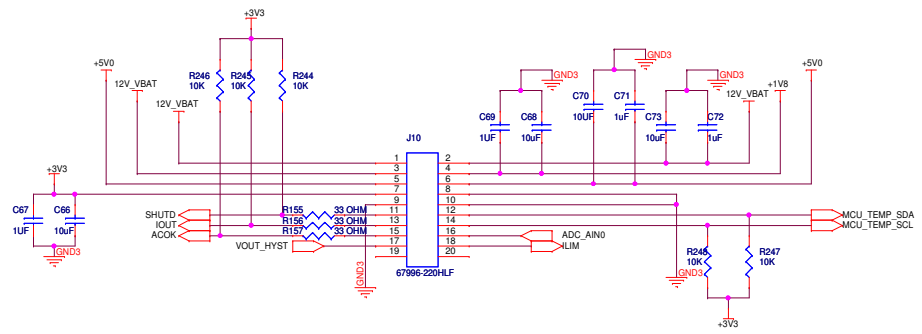
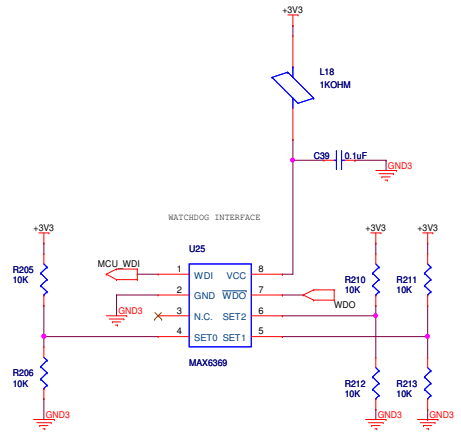
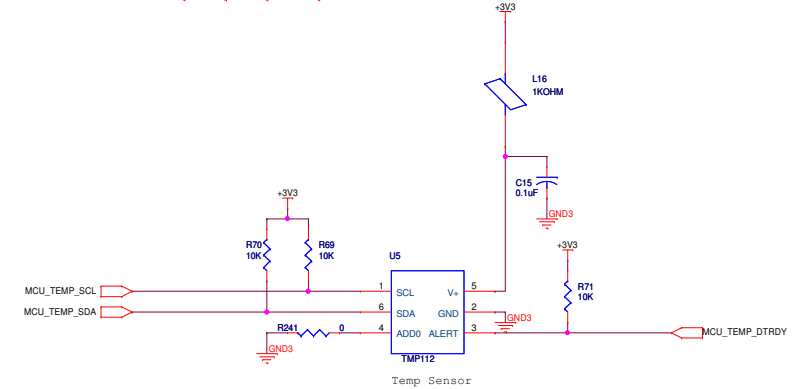
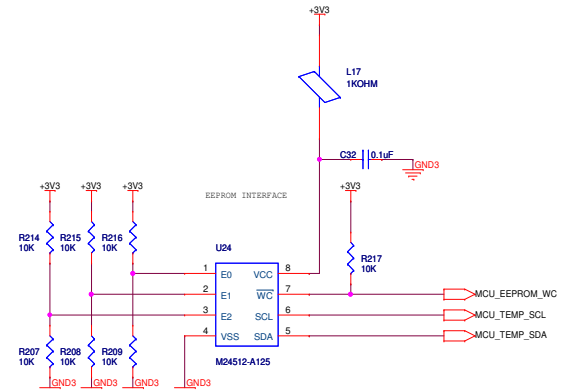
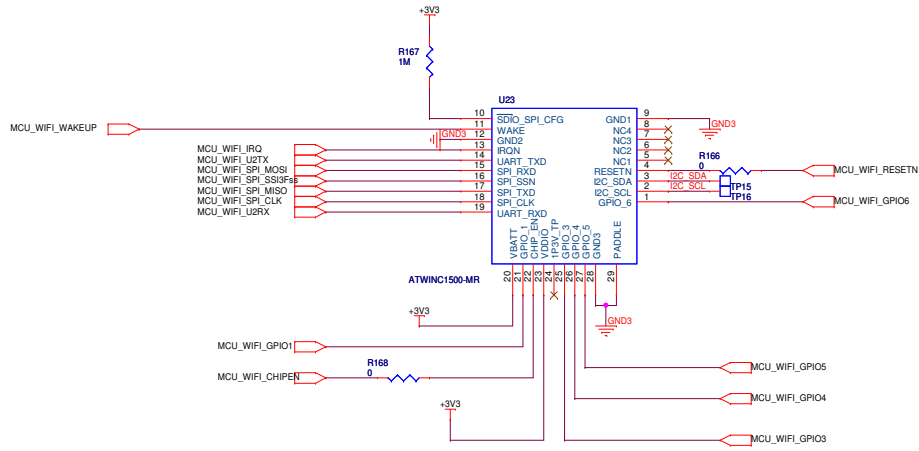
JTAG/DEBUG

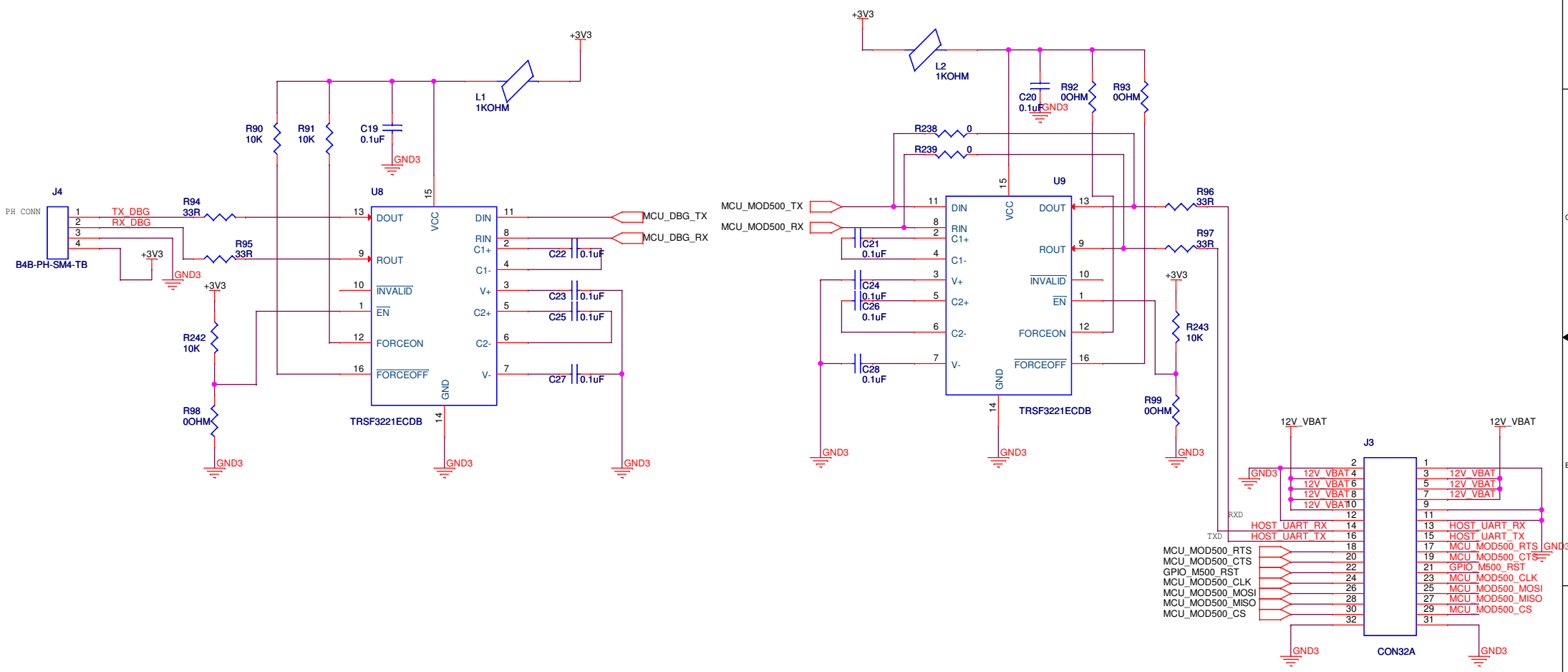


TARGET\_DEBUG/COMM

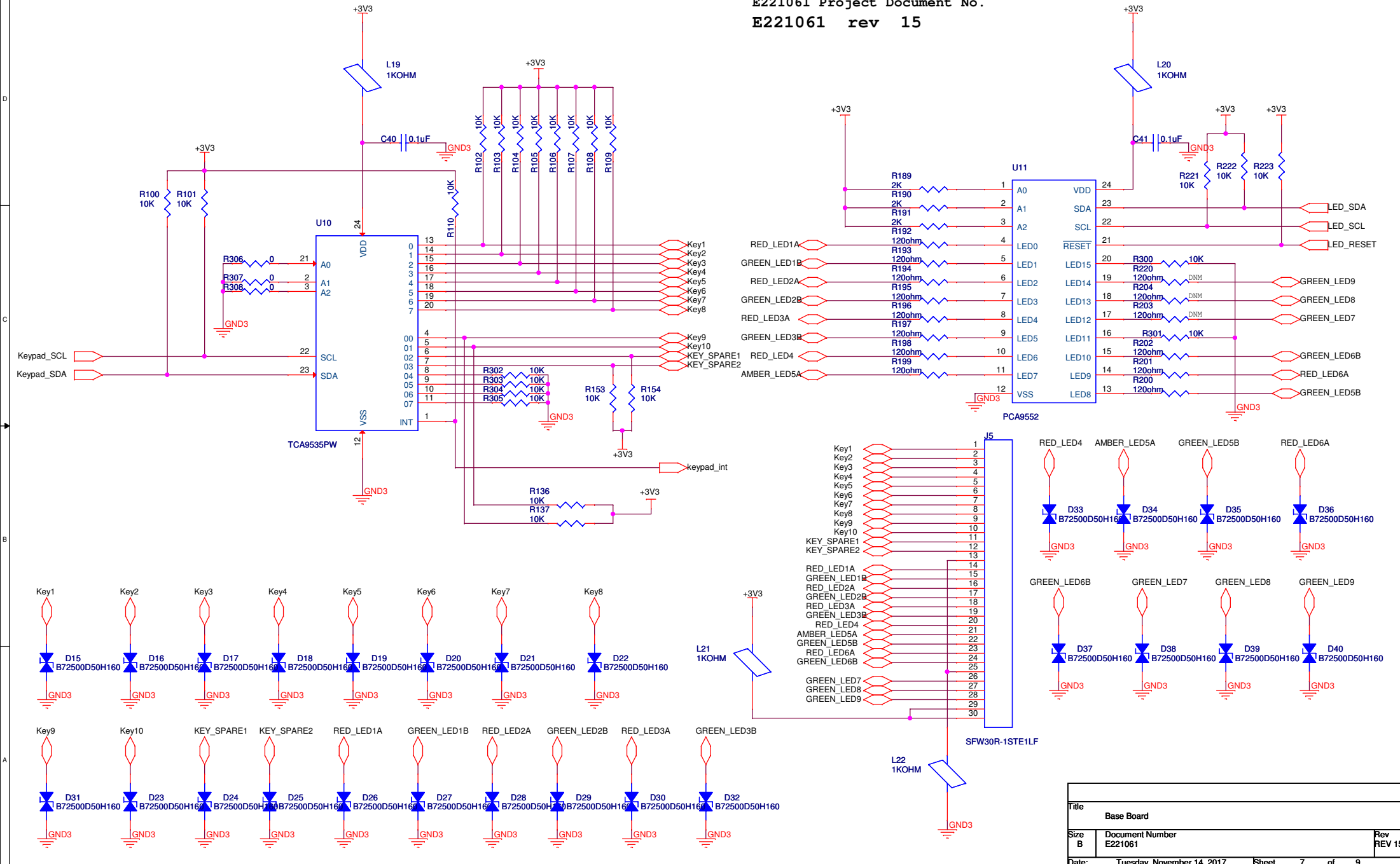


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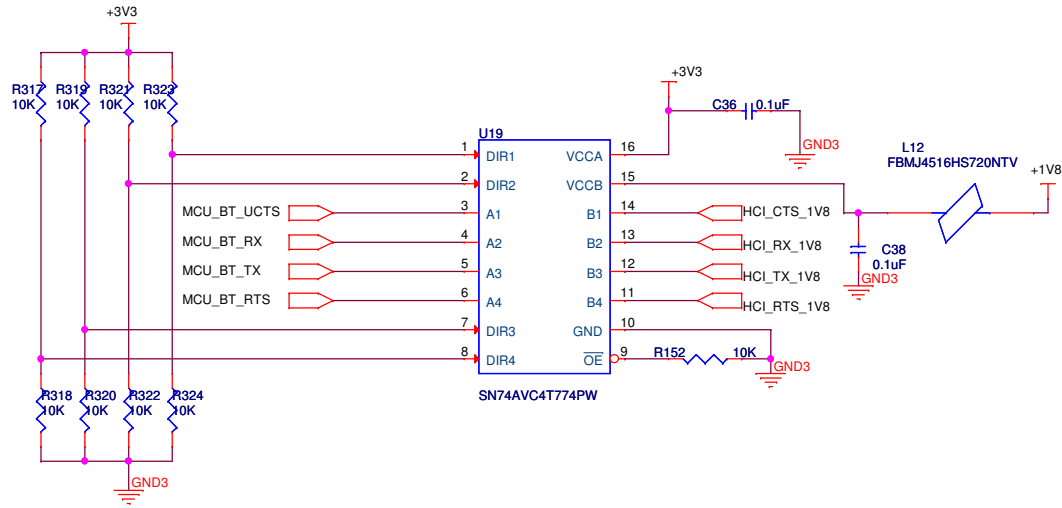




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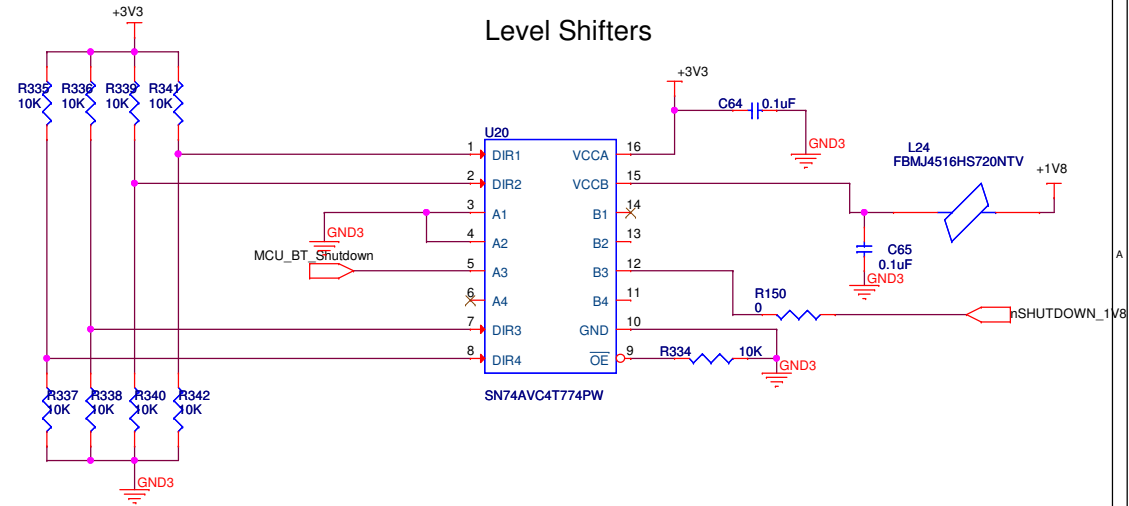


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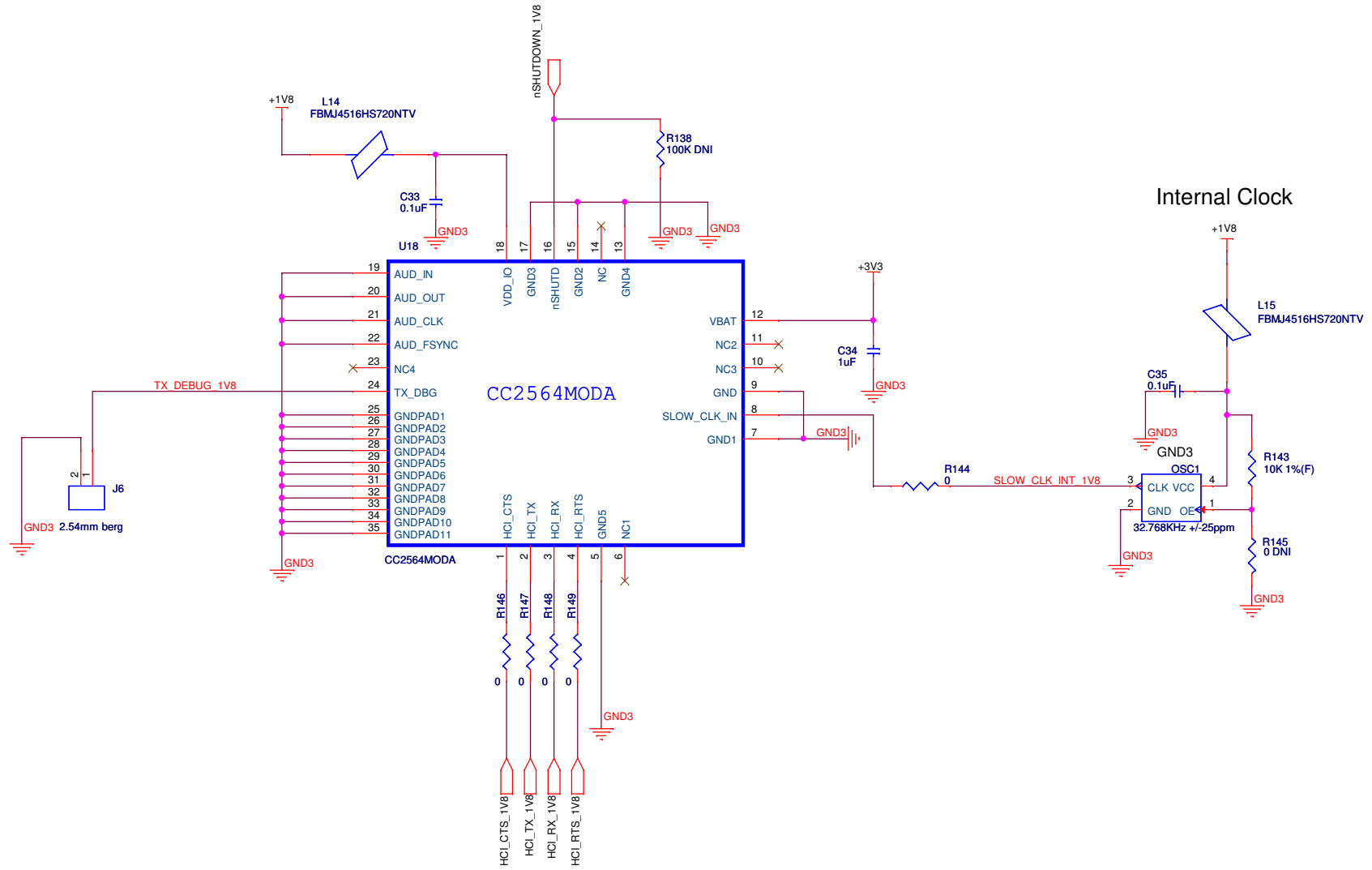
"H" A->B  
"L" B->A

Level Shifters



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