Hello Amit

I have attached a snapshot of the board. I am using a EK-TM4C123GXL to debug a EK-TM4C1294XL. The changes that you should note

1. The header on the EK-TM4C123GXL close to R17 has been removed to power down the main MCU

We also do not use this header.

2. The TCK, TMS, TDI, TDO, RST and GND from the EK-TM4C123GXL has been taken to the other board

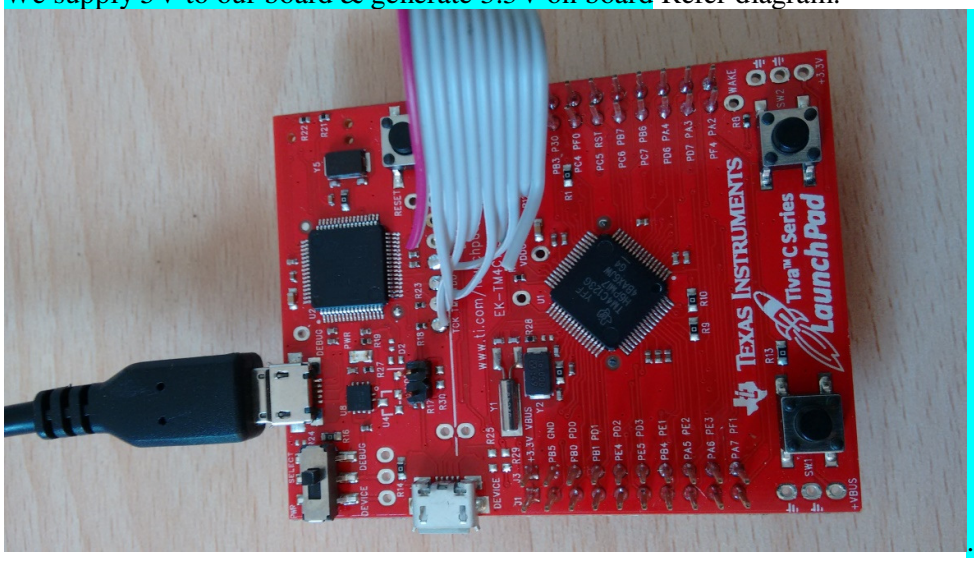
As I explained you earlier, we have not used RST connection.

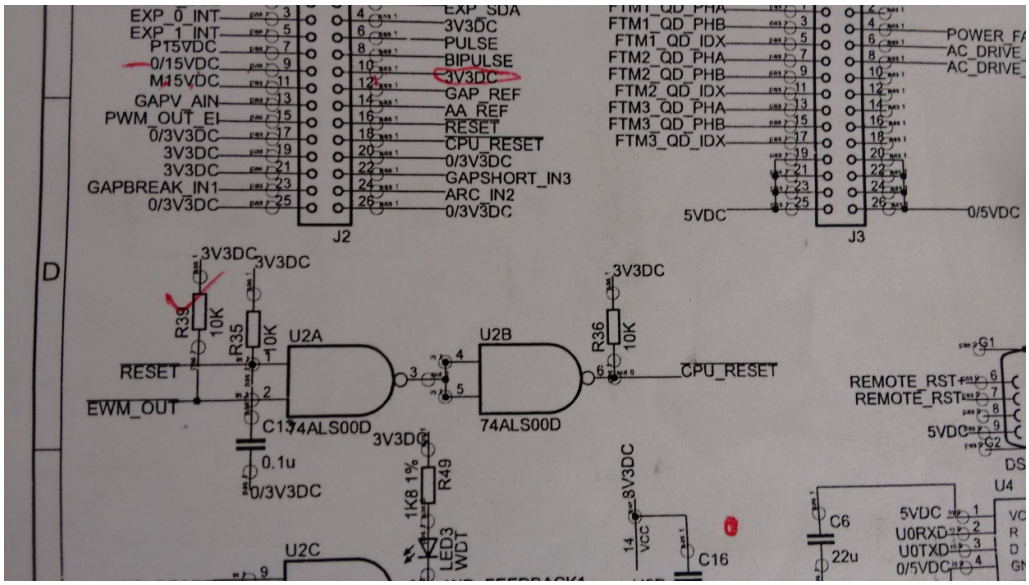
I have attached snapshot of circuit. Shall I connect RST to output of U2 pin 6 ?. This pin is directly connected to Pin 63 of TM4C123GH6PZ.

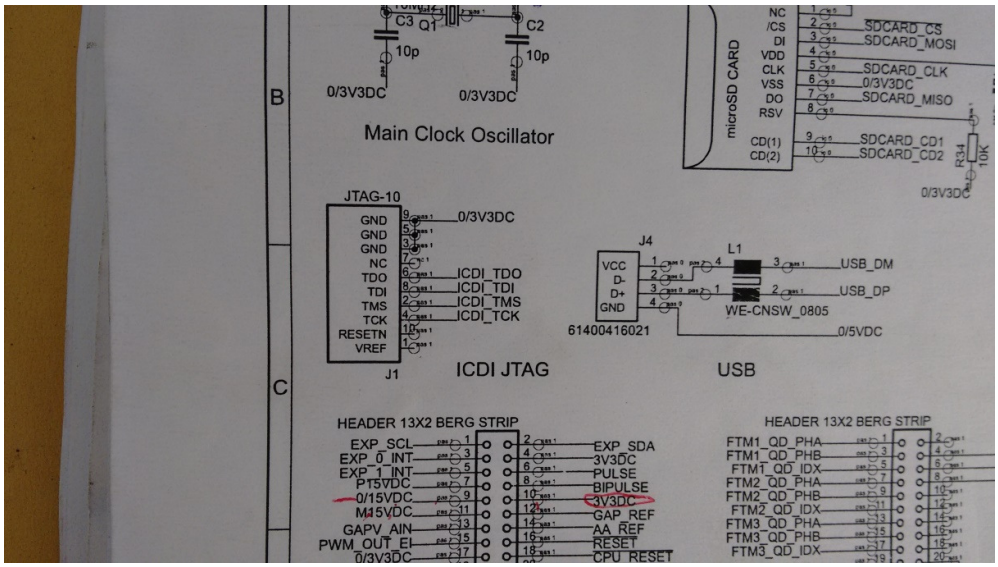
3. While TCK, TMS, RST and GND are connected as is on both the boards, TDI from the EK-TM4C123GXL is to be connected to TDI Pin on the TM4C129 Board's Main TM4C129 MCU and same for TDO.

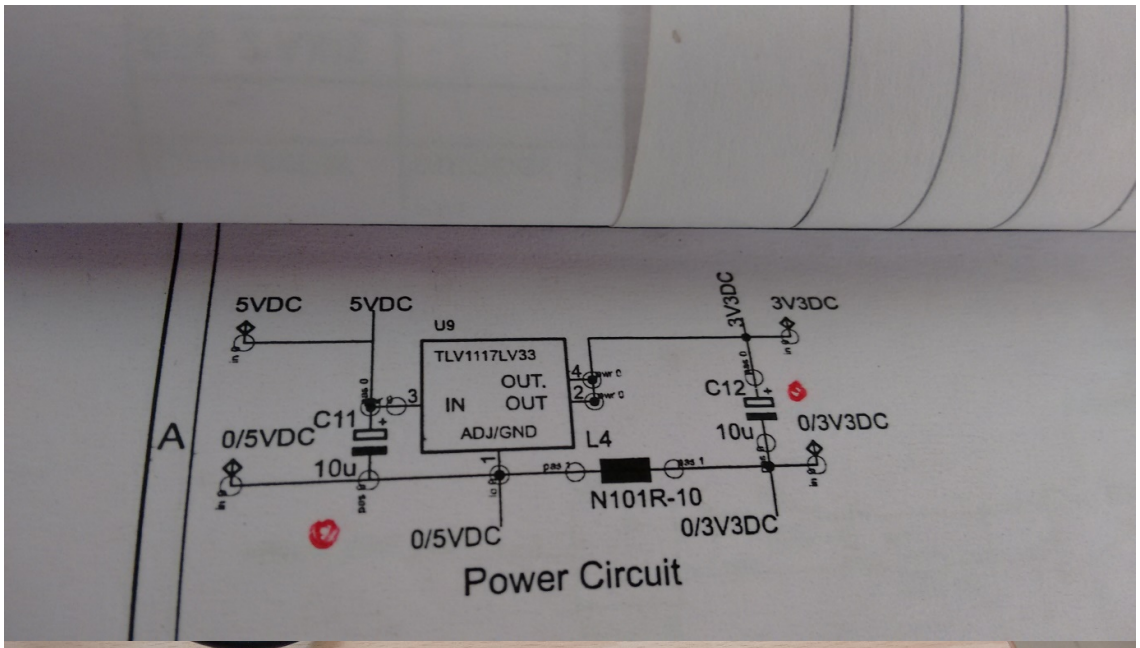
One thing I have noted – You have taken 3.3V & GND from J3 header. We are not using 3.3V from EK-TM4C123GXL & only taking GND from J3.

We supply 5V to our board & generate 3.3V on board Refer diagram.




EXP_0_INT
EXP_1_INT
PT5VDC
0/15VDC
M15VDC
GAPV_AIN
PWM_OUT_EI
0/3V3DC
3V3DC
3V3DC
GAPBREAK_IN1
0/3V3DC
EXP_SDA
3V3DC
PULSE
BIPULSE
3V3DC
GAP_REF
AA_REF
RESET
CPU_RESET
0/3V3DC
GAPSHORT_IN3
ARC_IN2
0/3V3DC
J2
FTM1_QD_PHA
FTM1_QD_PHB
FTM1_QD_IDX
FTM2_QD_PHA
FTM2_QD_PHB
FTM2_QD_IDX
FTM3_QD_PHA
FTM3_QD_PHB
FTM3_QD_IDX
5VDC
POWER_FA
AC_DRIVE_
AC_DRIVE_
0/5VDC
J3
D
3V3DC
3V3DC
R39
10K
R35
10K
U2A
RESET
EWM_OUT
C13
74ALS00D
0.1u
0/3V3DC
U2B
74ALS00D
3V3DC
R36
10K
CPU_RESET
3V3DC
1K8 1%
R49
LED3
WDT
U2C
8V3DC
C16
G1
REMOTE_RST
REMOTE_RST
5VDC
G2
DS
U4
C6
22u
5VDC
U0RXD
U0TXD
0/5VDC


B
C
C3
Q1
C2
10p
10p
0/3V3DC
0/3V3DC
Main Clock Oscillator
JTAG-10
GND
GND
GND
NC
TDO
TDI
TMS
TCK
RESETN
VREF
0/3V3DC
ICDI_TDO
ICDI_TDI
ICDI_TMS
ICDI_TCK
J1
ICDI JTAG
J4
VCC
D-
D+
GND
61400416021
L1
WE-CNSW_0805
USB_DM
USB_DP
0/5VDC
USB
microSD CARD
NC
/CS
DI
VDD
CLK
VSS
DO
RSV
CD(1)
CD(2)
SDCARD_CS
SDCARD_MOSI
SDCARD_CLK
0/3V3DC
SDCARD_MISO
SDCARD_CD1
SDCARD_CD2
R34
10K
0/3V3DC
HEADER 13X2 BERG STRIP
EXP_SCL
EXP_0_INT
EXP_1_INT
PT5VDC
0/15VDC
M15VDC
GAPV_AIN
PWM_OUT_EI
0/3V3DC
EXP_SDA
3V3DC
PULSE
BIPULSE
3V3DC
GAP_REF
AA_REF
RESET
CPU_RESET
HEADER 13X2 BERG STRIP
FTM1_QD_PHA
FTM1_QD_PHB
FTM1_QD_IDX
FTM2_QD_PHA
FTM2_QD_PHB
FTM2_QD_IDX
FTM3_QD_PHA
FTM3_QD_PHB
FTM3_QD_IDX


5VDC
5VDC
U9
TLV1117LV33
OUT.
IN
OUT
ADJ/GND
3V3DC
3V3DC
C12
10u
0/3V3DC
C11
0/5VDC
10u
L4
N101R-10
0/3V3DC
0/5VDC
A
Power Circuit

er Pull up
_0_INT
_1_INT
U1
PD7
PD6
PD5
PD4
PE5
PE4
GND
VDD
PB4
PB5
PE7
PE6
PG7
PG6
VDDC
CK/SWCLK
MS/SWDIO
PC2/TDI
/TDO/SWO
GND
VDD
PH4
PH5
PH6
PH7
100 FTM3_QD_D7
99 FTM3_QD_D6
98 FTM3_QD_D5
97 FTM3_QD_D4
96 AC_DRIVE_M
95 AC_DRIVE_P
94 0/3V3DC
93 3V3DC
92
91
90
89
88 WD_RESET
87 EWM_OUT
86 VDDC
85 ICDI_TCK
84 ICDI_TMS
83 ICDI_TDI
82 ICDI_TDO
81 0/3V3DC
80 3V3DC
79
78
77
76
75