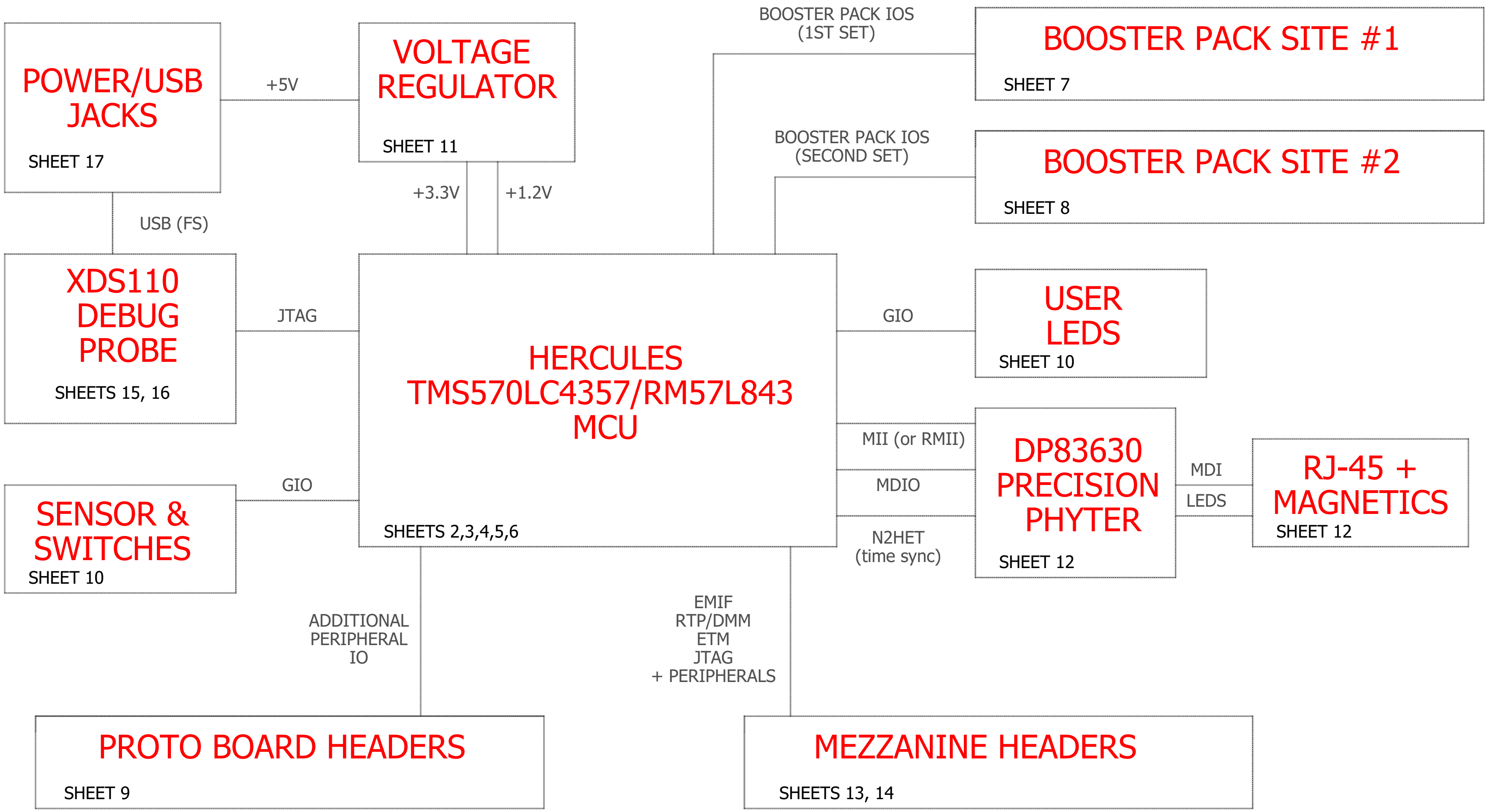
LAUNCHXL2-570LC43 / LAUNCHXL2-RM57L
337 ZWT LAUNCHPAD XL2



RevA->RevB only Silkscreen Changes. No Schematic/Circuit Changes.

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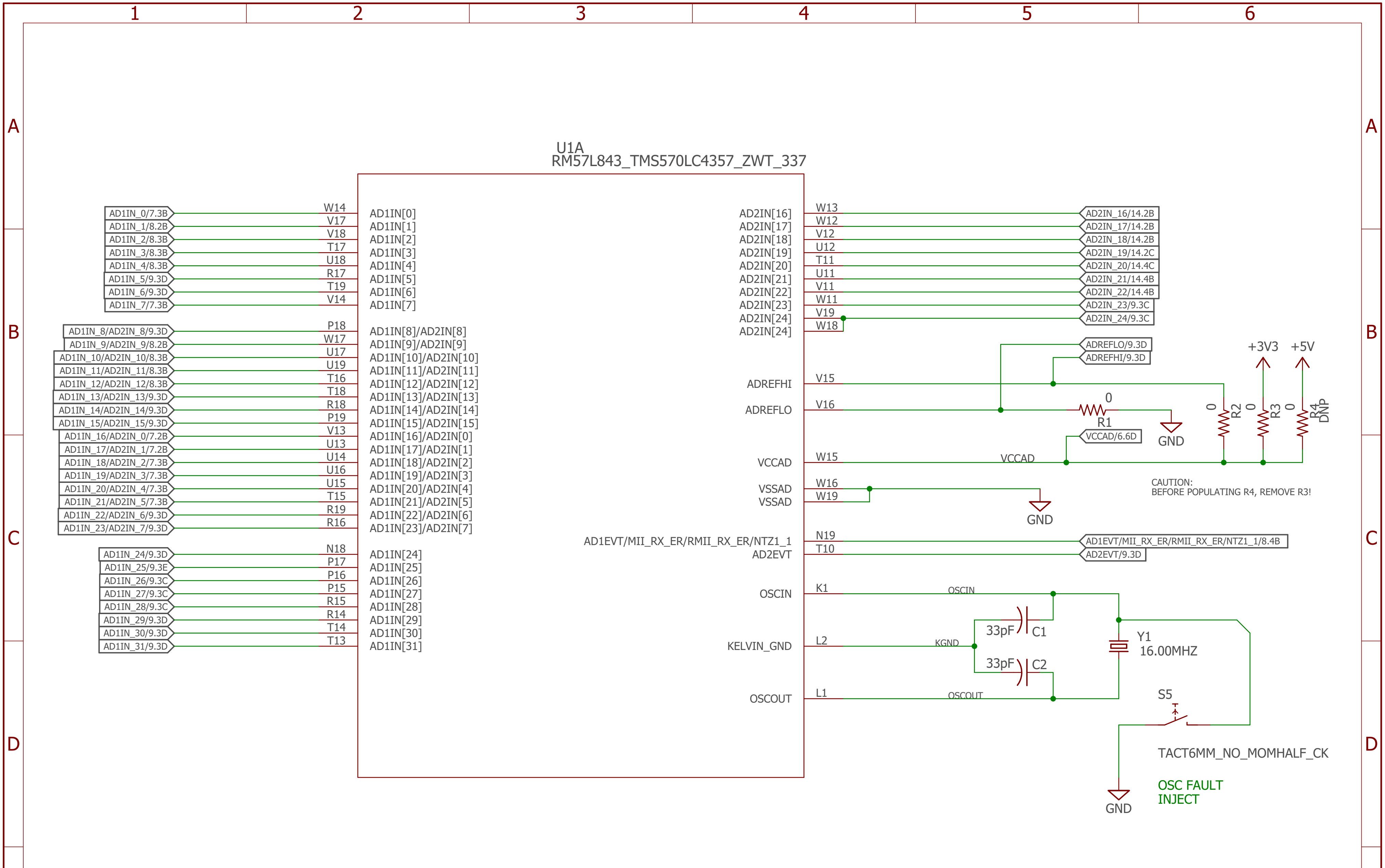

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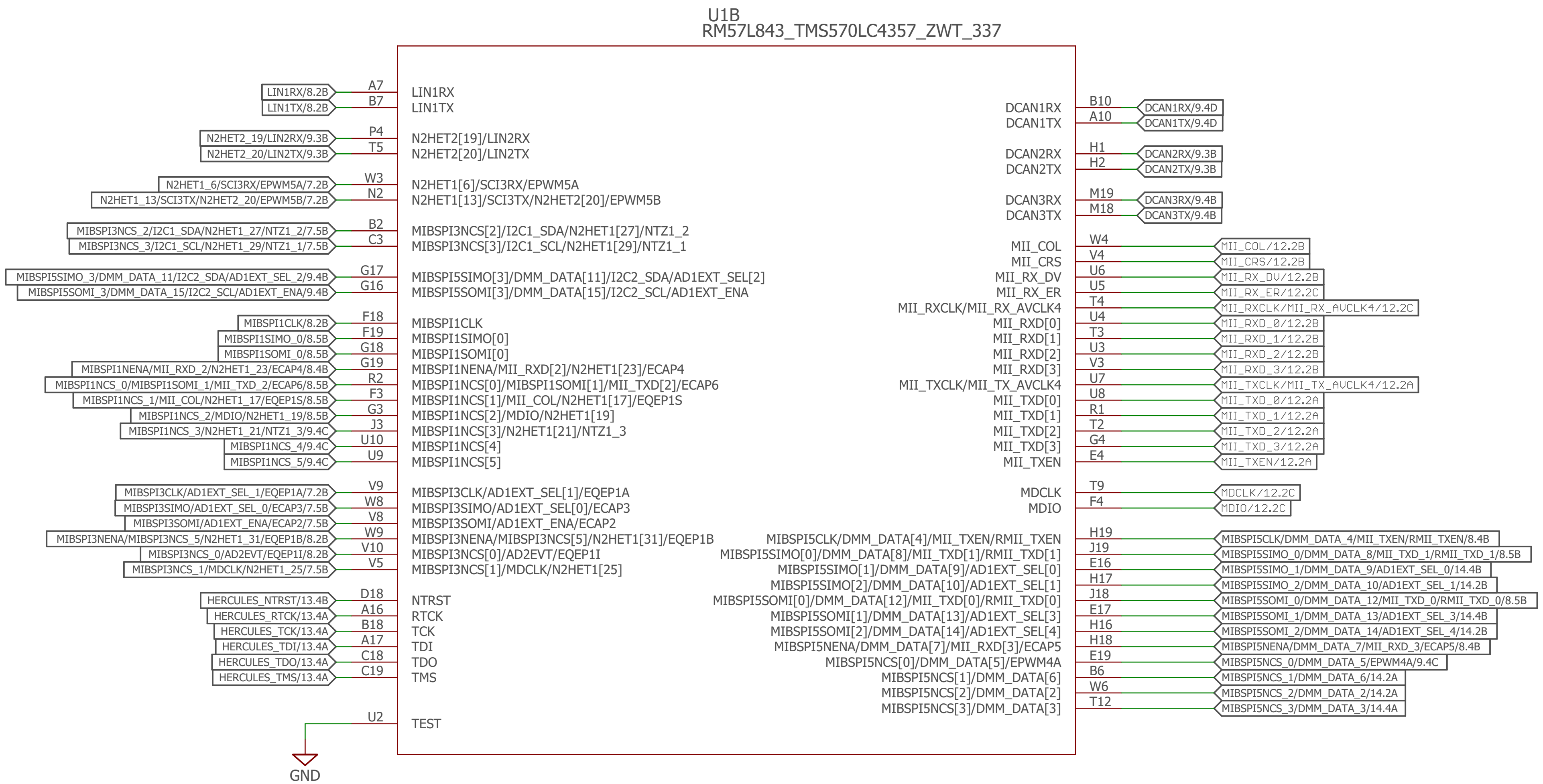

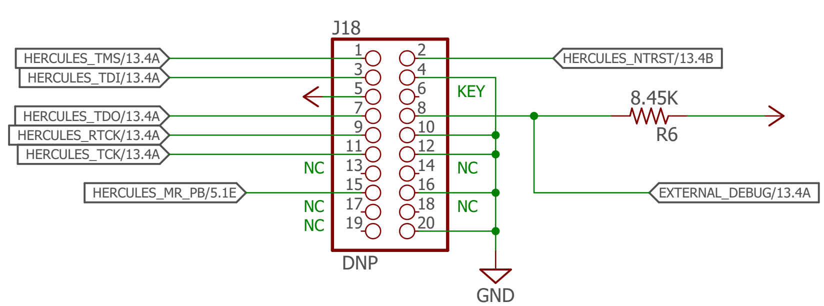

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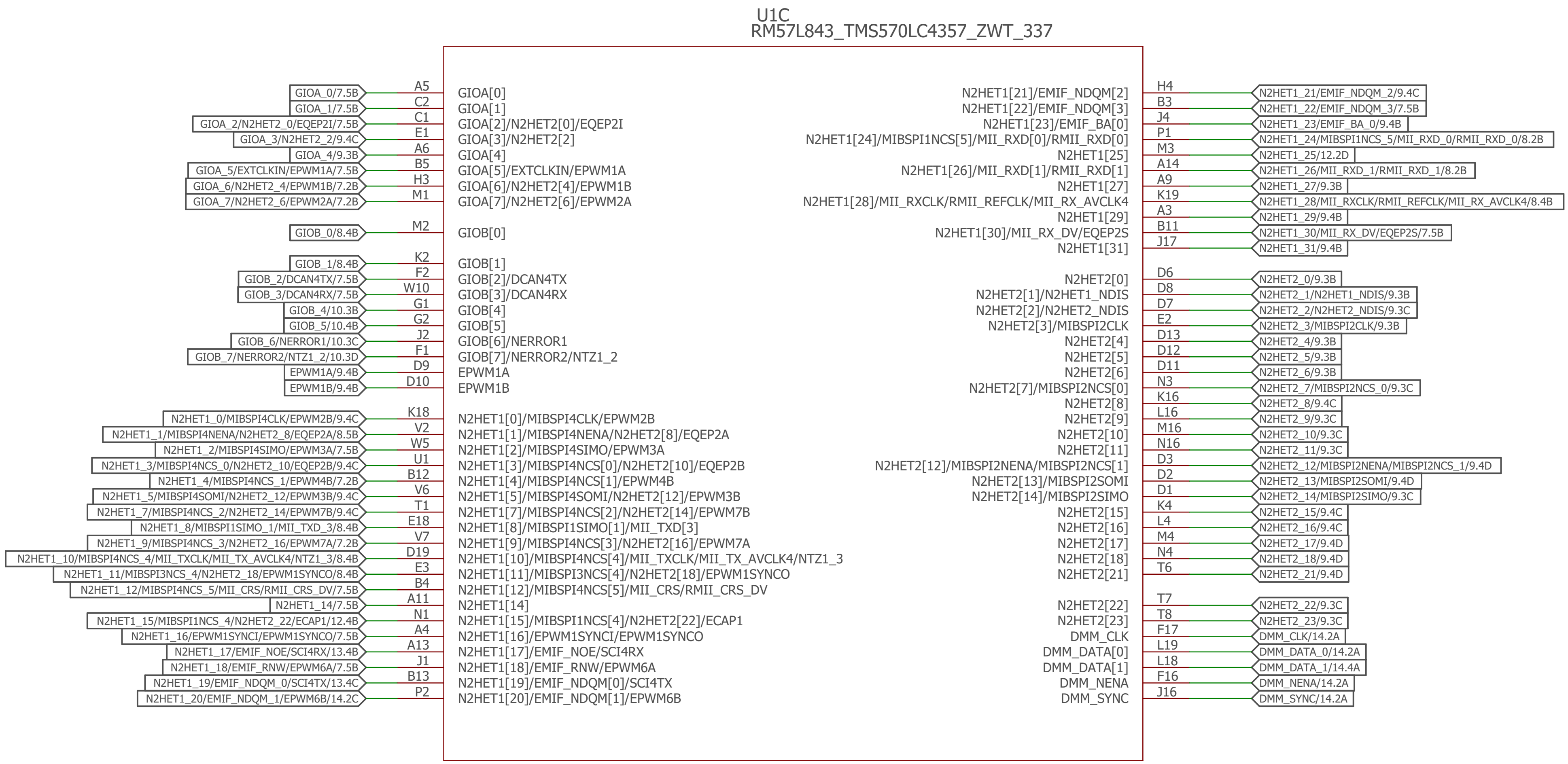
1 2 3 4 5 6

A B C D E



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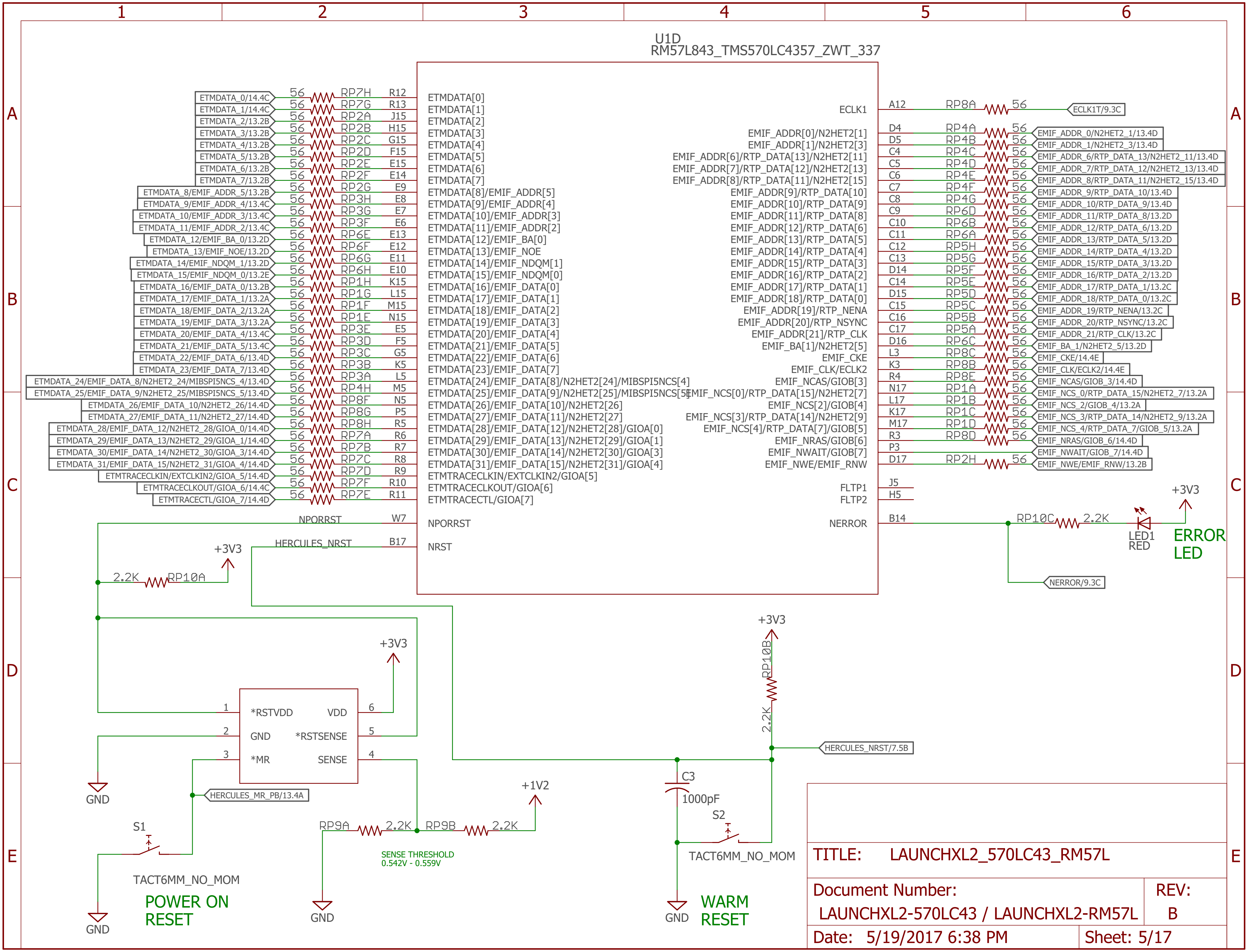

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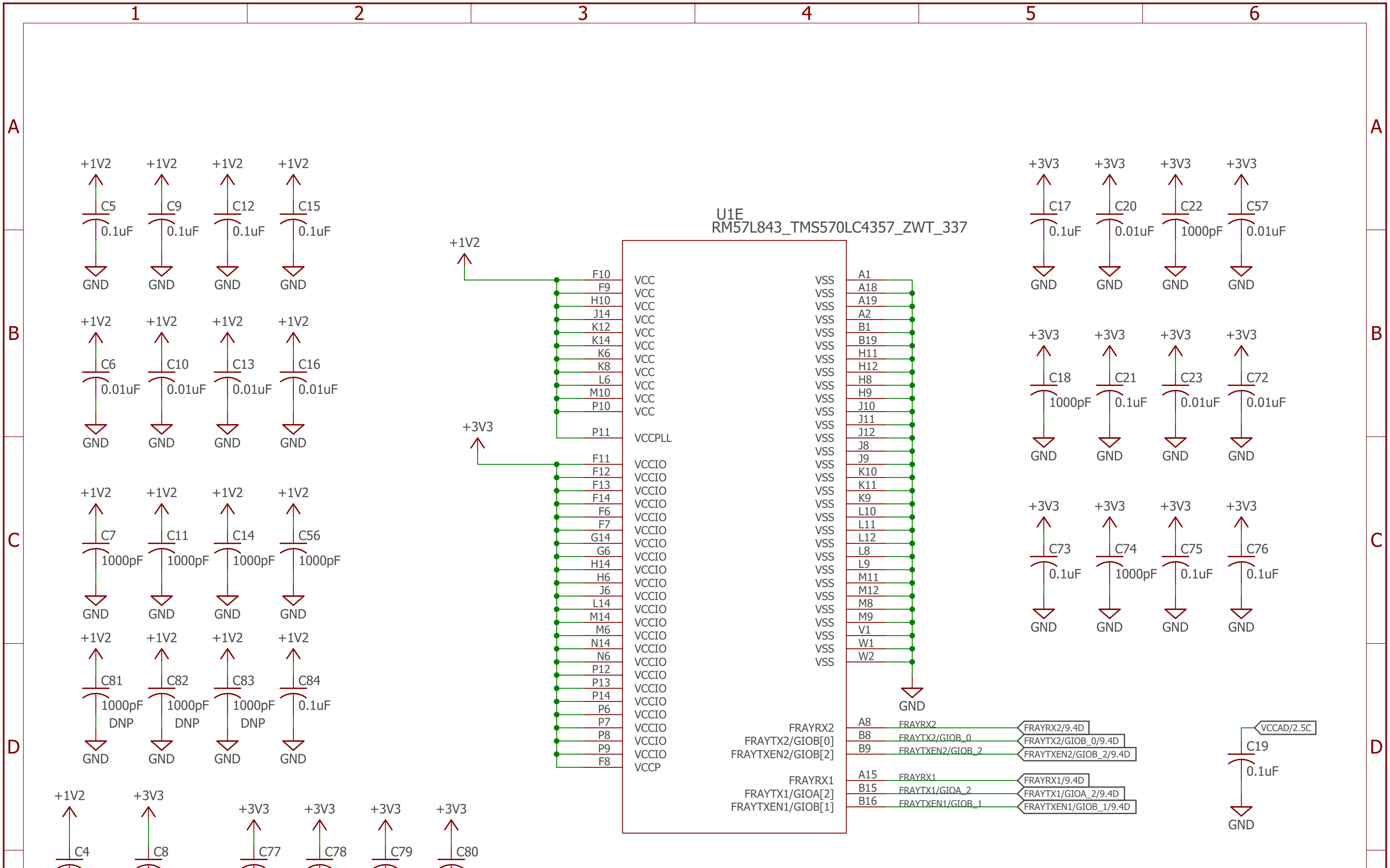

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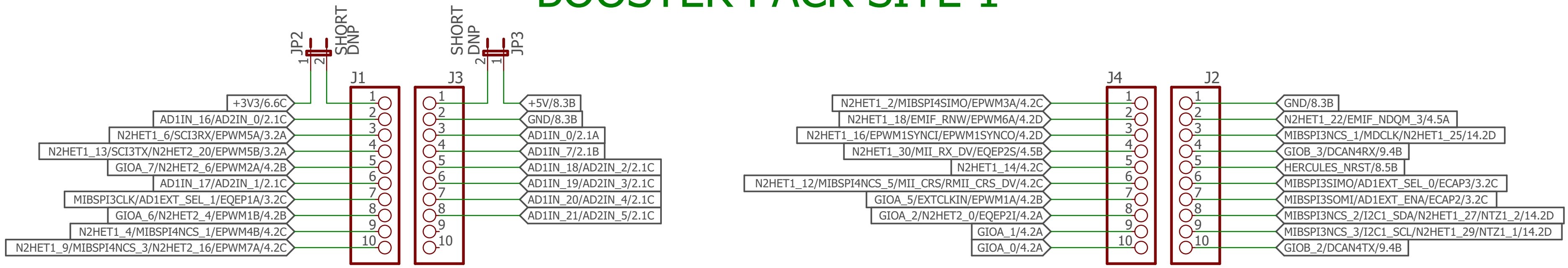
BOOSTER PACK SITE 1



Headers/Receptacles may be ordered from http://launchpad.mlelectronics.com/

NOTES:

JP2 & JP3 are normally shorted on the PCB - the footprint consists of an (unpopulated) 2 pin SMT header and a solder-bridging structure.
Most users can leave these jumpers as-is.

Certain booster packs may require that the position is opened - these are booster packs that would otherwise supply power to the launchpad through these pins.
Providing +3V3 to the launchpad is a problem because there would be a conflict with the launchpad's on-board LM26420 regulator.
Providing +5V to the launchpad through the booster pack could be ok, but the barrel jack is preferred as it is protected with a PTC.
Also be careful to avoid back powering the USB connection if you do this.

In some cases you may find the need to make/break the connections JP2,JP3 frequently.
If you do, then you can remove the solder bridge and mount a 2 pin SMT header on the footprint location which can then be opened/closed by using a Jumper or Shunt.

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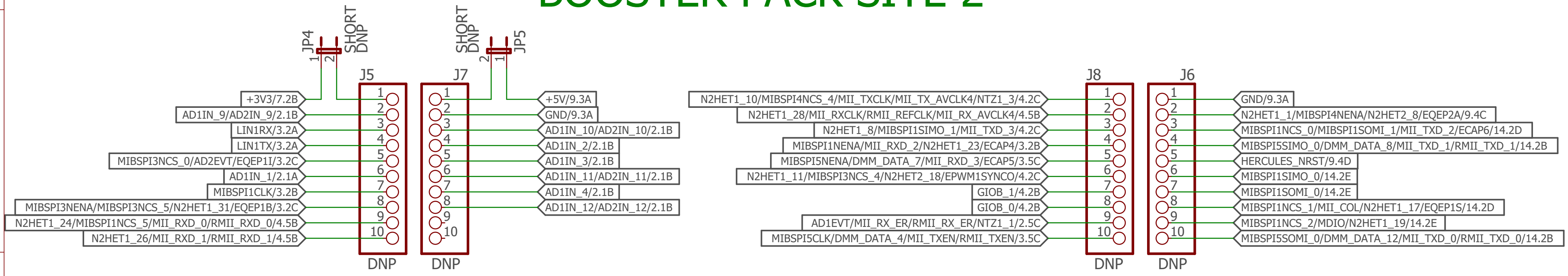
BOOSTER PACK SITE 2



Headers/Receptacles may be ordered from http://launchpad.mlelectronics.com/

NOTES:

JP4 & JP5 are normally shorted on the PCB - the footprint consists of an (unpopulated) 2 pin SMT header and a solder-bridging structure.
Most users can leave these jumpers as-is.

Certain booster packs may require that the position is opened - these are booster packs that would otherwise supply power to the launchpad through these pins.
Providing +3V3 to the launchpad is a problem because there would be a conflict with the launchpad's on-board LM26420 regulator.
Providing +5V to the launchpad through the booster pack could be ok, but the barrel jack is preferred as it is protected with a PTC.
Also be careful to avoid back powering the USB connection if you do this.

In some cases you may find the need to make/break the connections JP4,JP5 frequently.
If you do, then you can remove the solder bridge and mount a 2 pin SMT header on the footprint location which can then be opened/closed by using a Jumper or Shunt.

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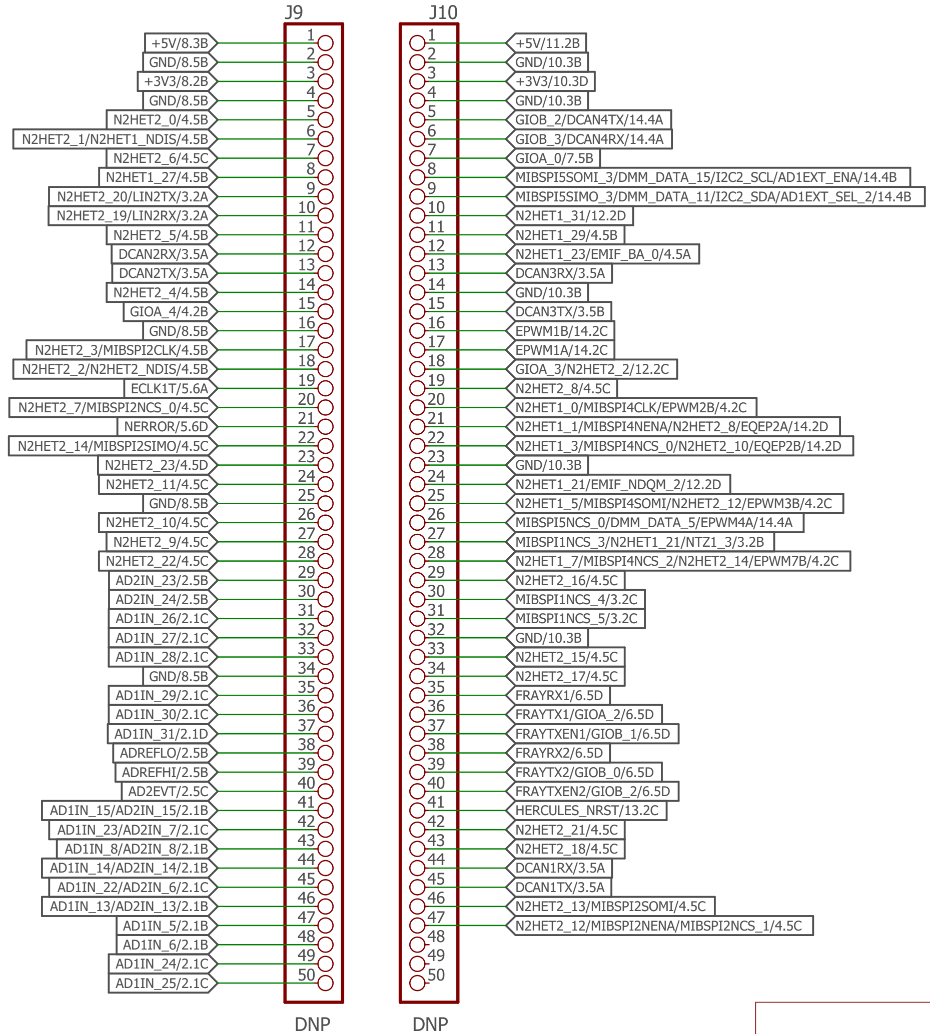
PROTO BOARD HEADERS



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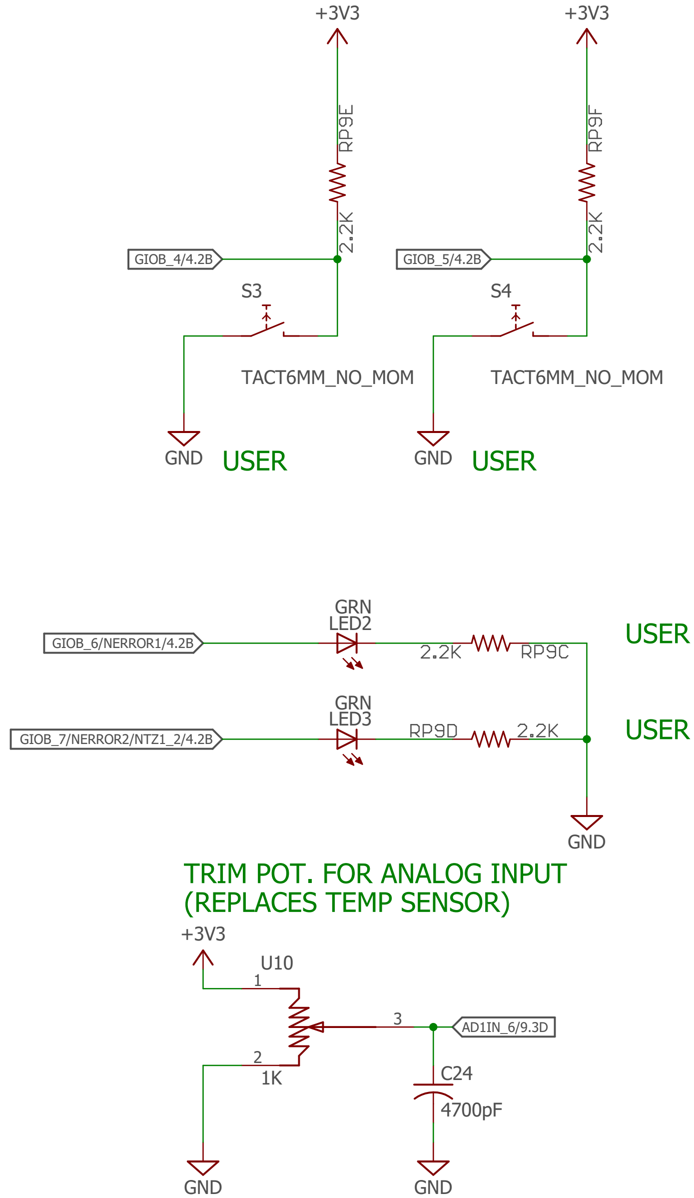

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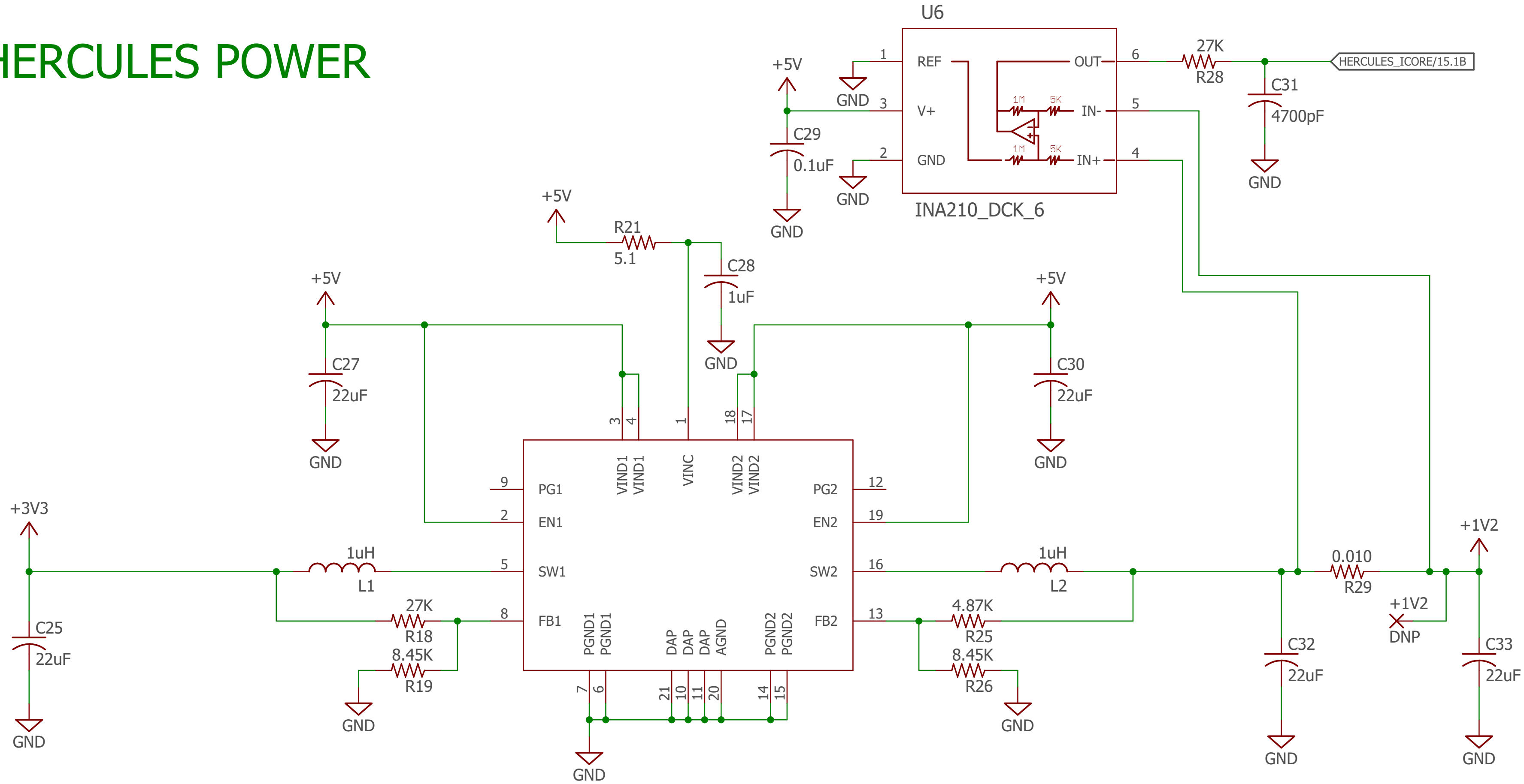
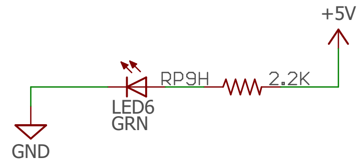
HERCULES POWER



POWER INDICATOR LED



+5V

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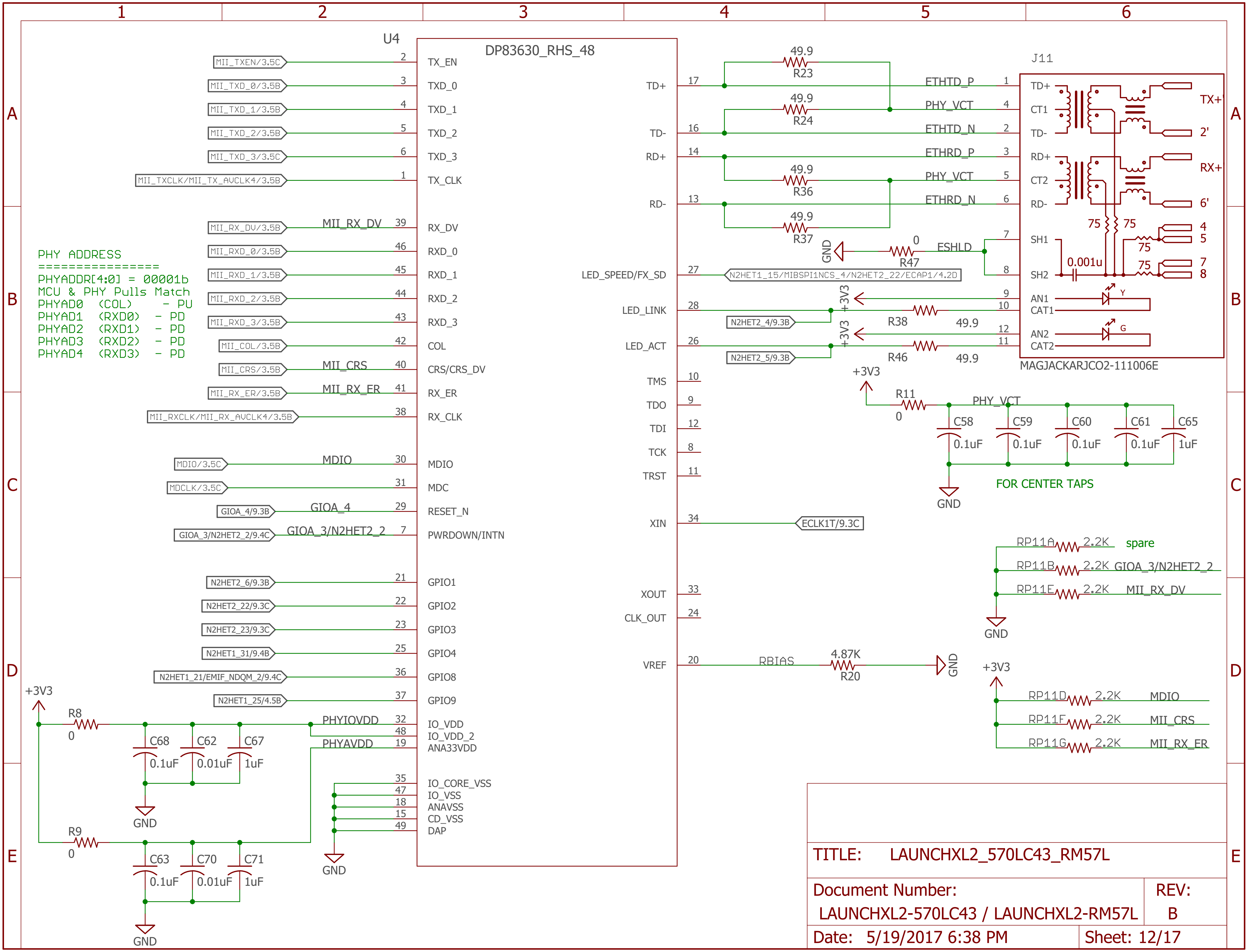
U4
DP83630_RHS_48
TX_EN
TXD_0
TXD_1
TXD_2
TXD_3
TX_CLK
RX_DV
RXD_0
RXD_1
RXD_2
RXD_3
COL
CRS/CRS_DV
RX_ER
RX_CLK
MDIO
MDC
RESET_N
PWRDOWN/INTN
GPIO1
GPIO2
GPIO3
GPIO4
GPIO8
GPIO9
IO_VDD
IO_VDD_2
ANA33VDD
IO_CORE_VSS
IO_VSS
ANAVSS
CD_VSS
DAP
TD+
TD-
RD+
RD-
LED_SPEED/FX_SD
LED_LINK
LED_ACT
TMS
TDO
TDI
TCK
TRST
XIN
XOUT
CLK_OUT
VREF
MII_TXEN/3.5C
MII_TXD_0/3.5B
MII_TXD_1/3.5B
MII_TXD_2/3.5B
MII_TXD_3/3.5C
MII_TXCLK/MII_TX_AVCLK4/3.5B
MII_RX_DV/3.5B
MII_RXD_0/3.5B
MII_RXD_1/3.5B
MII_RXD_2/3.5B
MII_RXD_3/3.5B
MII_COL/3.5B
MII_CRS/3.5B
MII_RX_ER/3.5B
MII_RXCLK/MII_RX_AVCLK4/3.5B
MDIO/3.5C
MDCLK/3.5C
GIOA_4/9.3B
GIOA_3/N2HET2_2/9.4C
N2HET2_6/9.3B
N2HET2_22/9.3C
N2HET2_23/9.3C
N2HET1_31/9.4B
N2HET1_21/EMIF_NDQM_2/9.4C
N2HET1_25/4.5B
MII_RX_DV
MII_CRS
MII_RX_ER
MDIO
GIOA_4
GIOA_3/N2HET2_2
PHYIOVDD
PHYAVDD
PHY ADDRESS
================
PHYADDR[4:0] = 00001b
MCU & PHY Pulls Match
PHYAD0 (COL) - PU
PHYAD1 (RXD0) - PD
PHYAD2 (RXD1) - PD
PHYAD3 (RXD2) - PD
PHYAD4 (RXD3) - PD
+3V3
R8
0
R9
0
C68 0.1uF
C62 0.01uF
C67 1uF
C63 0.1uF
C70 0.01uF
C71 1uF
GND
J11
MAGJACKARJCO2-111006E
ETHTD_P
PHY_VCT
ETHTD_N
ETHRD_P
ETHRD_N
R23 49.9
R24 49.9
R36 49.9
R37 49.9
R47 0
ESHLD
N2HET1_15/MIBSPI1NCS_4/N2HET2_22/ECAP1/4.2D
N2HET2_4/9.3B
N2HET2_5/9.3B
R38 49.9
R46 49.9
TD+
CT1
TD-
RD+
CT2
RD-
SH1
SH2
AN1
CAT1
AN2
CAT2
TX+
2'
RX+
6'
75
0.001u
Y
G
R11
0
C58 0.1uF
C59 0.1uF
C60 0.1uF
C61 0.1uF
C65 1uF
FOR CENTER TAPS
ECLK1T/9.3C
RBIAS
R20 4.87K
RP11A 2.2K spare
RP11B 2.2K GIOA_3/N2HET2_2
RP11E 2.2K MII_RX_DV
RP11D 2.2K MDIO
RP11F 2.2K MII_CRS
RP11G 2.2K MII_RX_ER
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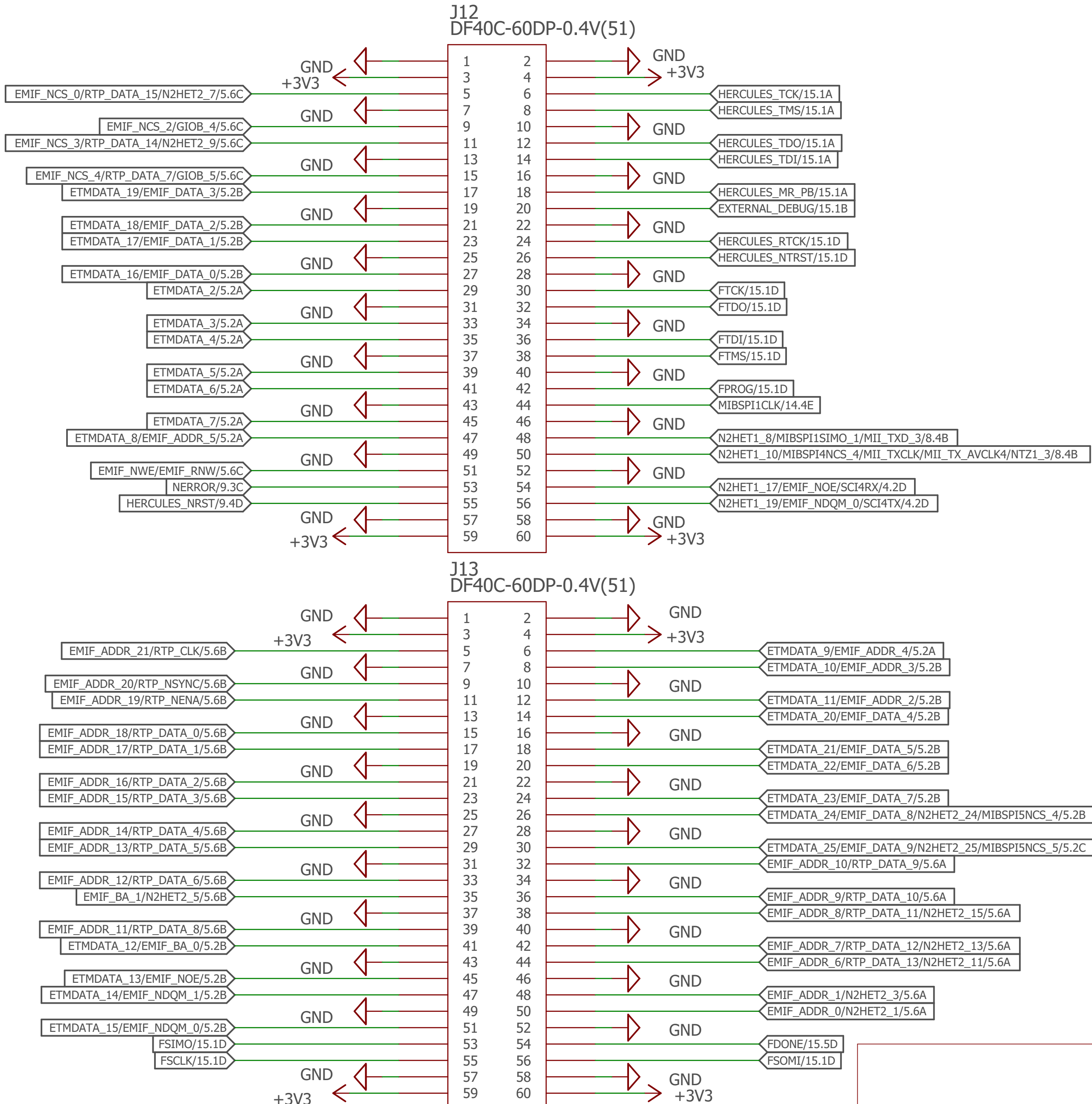

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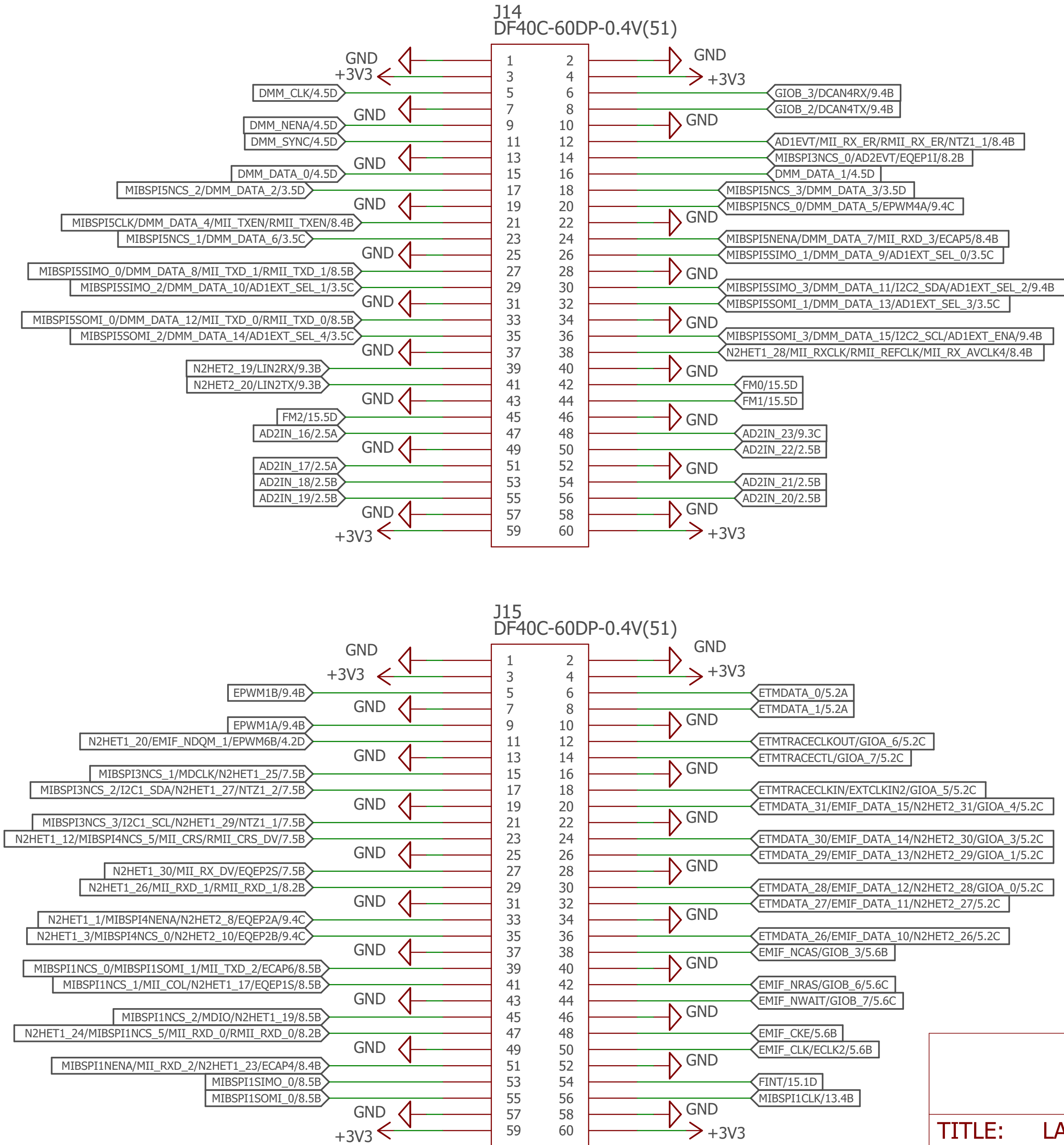

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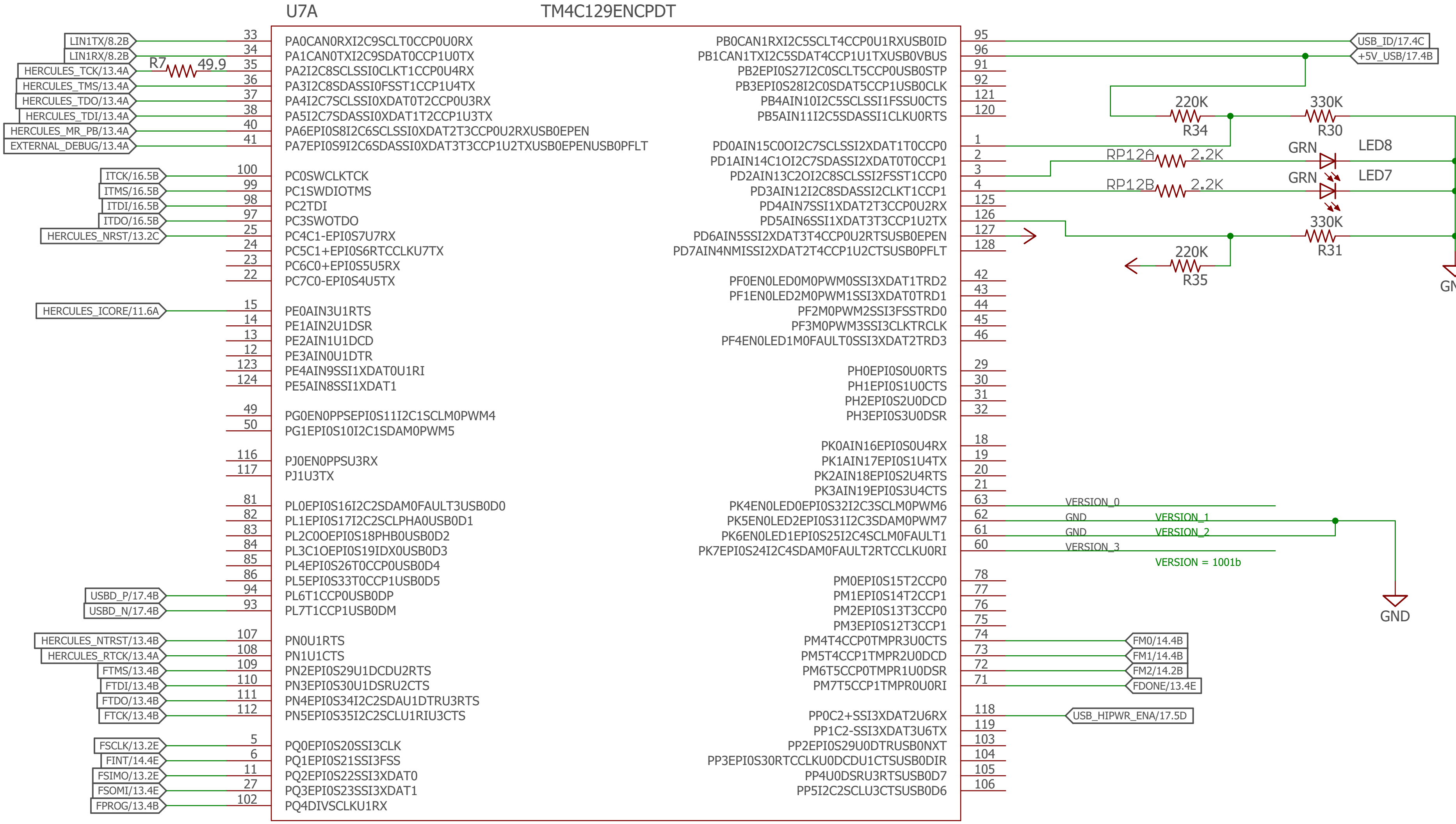
XDS110 DEBUG PROBE



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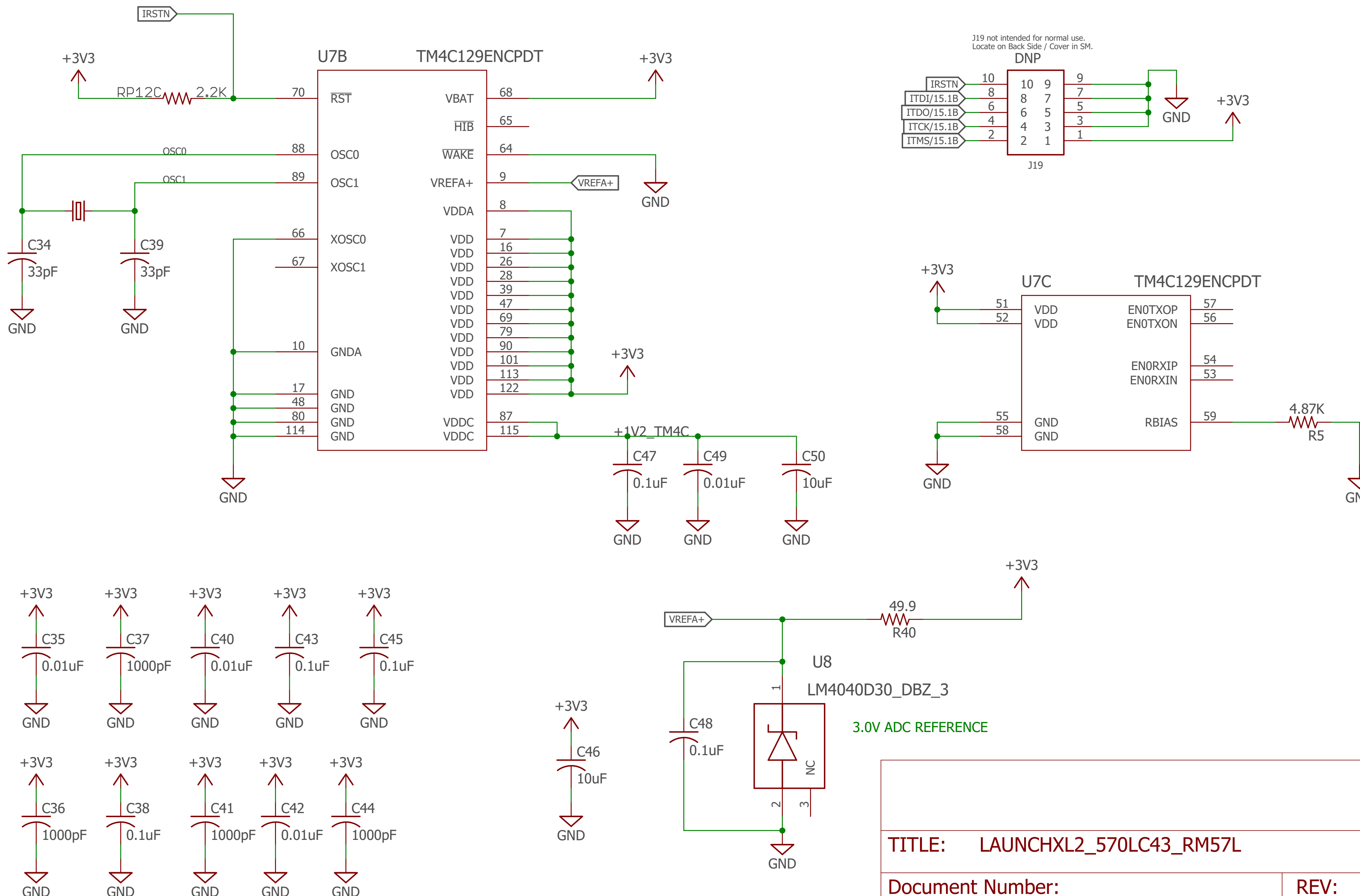
XDS110 DEBUG PROBE



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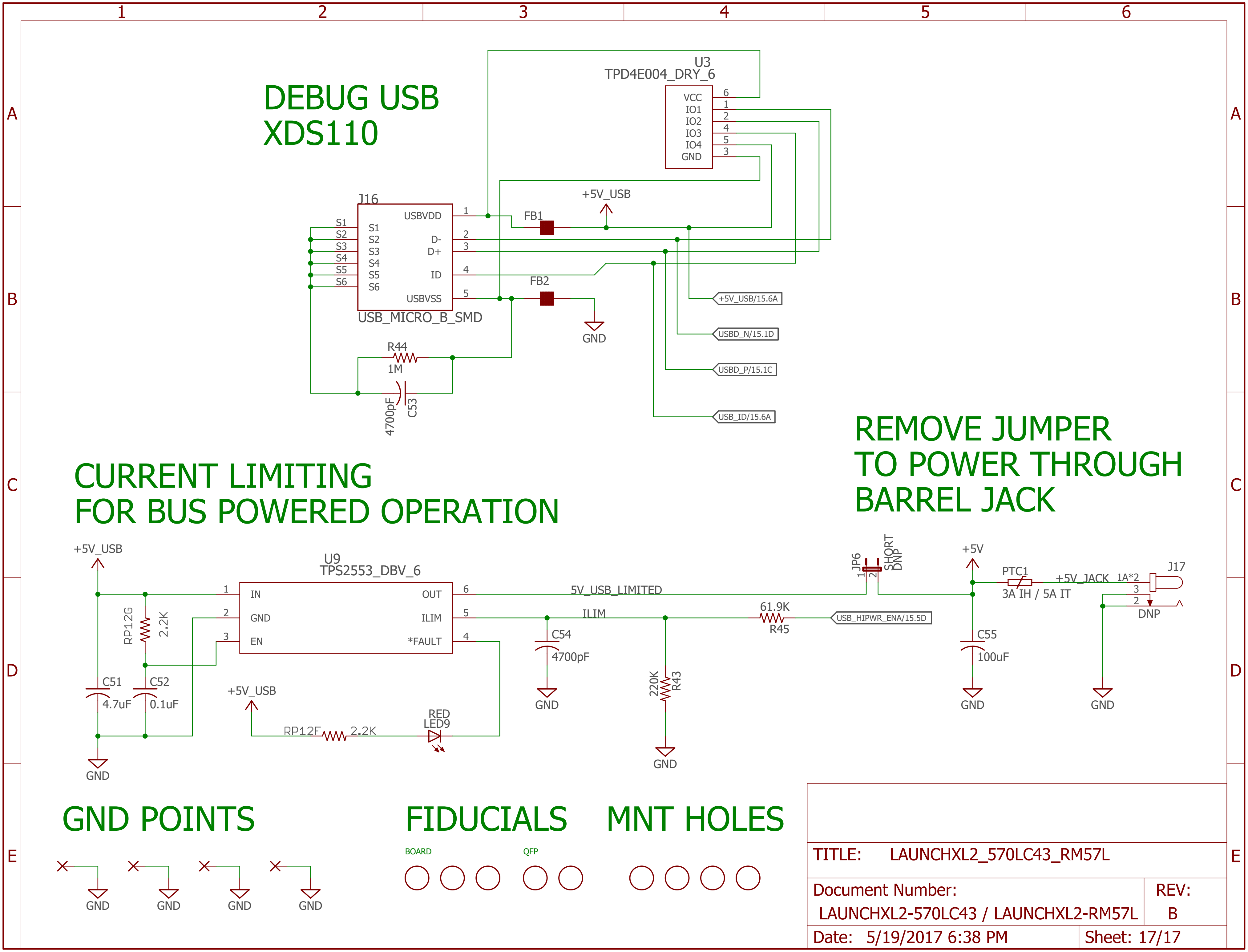

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