

222 ■ These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

| Layer | Name | Material | Thickness | Constant | Board Layer | Back |
|-------|----------------|---------------|-----------|----------|-------------|------|
| 1 | Top Overlay | | | | | |
| 2 | Top Solder | Solder Resist | 0.40mil | 3.8 | | |
| 3 | Top Layer | Copper | 1.40mil | | | |
| 4 | Dielectric 2 | FR-4 | 8.40mil | 4.2 | | |
| 5 | VCC Layer | Copper | 1.40mil | | | |
| 6 | Dielectric 3 | FR-4 | 6.00mil | 4.2 | | |
| 7 | Brd Layer | Copper | 1.40mil | | | |
| 8 | Dielectric 3 | FR-4 | 8.40mil | 4.2 | | |
| 9 | Bottom Layer | Copper | 1.40mil | | | |
| 10 | Bottom Solder | Solder Resist | 0.40mil | 3.8 | | |
| 11 | Bottom Overlay | | | | | |

| | | | |
|---|---|--|--|
| DESIGN INFORMATION | | | |
| MIN. TRACK WIDTH: _____ <u>8</u> MIL MIN. CLEARED: _____ <u>0.2</u> mm MIN. VIA PAD SIZE: _____ <u>24</u> MIL | 0.05mm (2MIL) _____ EXTERNAL _____ | | |
| MINIMUM ANNUAL RING _____ PCB PC-D-275 CLASS 2 LEVEL C _____ | | | |
| REGISTRATION TOLERANCES _____ HOLE SIZE TOLERANCE (AUNLESS OTHERWISE SPECIFIED) _____ | METAL <u>+</u> / <u>-</u> _____ <u>5</u> MIL HOLES <u>+</u> / <u>-</u> _____ <u>3</u> MIL <u>+</u> / <u>-</u> _____ <u>3</u> MIL | | |

| | | | |
|---|---|--|---|
| MATERIAL: _____ | | | |
| <input type="checkbox"/> FR-408 THICKNESS: <input type="checkbox"/> <u>02</u> MIL (0.05mm) <u>+</u> / <u>-</u> 10% TOLERANCE: <input type="checkbox"/> _____ <input type="checkbox"/> _____ <input type="checkbox"/> _____ BOB & TRIST: <input type="checkbox"/> _____ <input type="checkbox"/> _____ <input type="checkbox"/> _____ | <input type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER _____ ANIS PC-8012 TYPE 3 CLASS 2 OTHER <u>+</u> / <u>-</u> _____ ANIS PC-8012 TYPE 3 CLASS 2 OTHER <u>+</u> / <u>-</u> _____ | <input type="checkbox"/> AS SHOWN <input type="checkbox"/> NO DRILL FLUX PTH COPPER THICKNESS: <input type="checkbox"/> <u>20-30</u> um <input type="checkbox"/> OTHER _____ | <input type="checkbox"/> BOARD FINISH: _____ SLU/SCREEN: <input type="checkbox"/> TOP <input type="checkbox"/> BOTTOM SLU/SCREEN COLOR: <input type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____ SOLDER RESIST COLOR: <input type="checkbox"/> GREEN <input type="checkbox"/> OTHER <u>Red</u> <input type="checkbox"/> WHITE <input type="checkbox"/> SEM-GLOSS |
| SURFACE FINISH: <input type="checkbox"/> _____ <input type="checkbox"/> BAL. TRU/SILVER OR TOLUV <input type="checkbox"/> OTHER _____ | <input type="checkbox"/> MARRISON GOLD (BING) <input type="checkbox"/> ENIG | | |
| APPEARANCE: <input type="checkbox"/> _____ <input type="checkbox"/> CUT AND TRIM PER MI BOARD OUTLINE <input type="checkbox"/> N.C. ROUTE <input type="checkbox"/> V. SCORE | | | |

| | | | |
|---|--|---|-----------------|
| CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF: | | | |
| <input type="checkbox"/> ANIS PC-A-600F CLASS -> | <input type="checkbox"/> 1 <input type="checkbox"/> 2 <input type="checkbox"/> 3 | <input type="checkbox"/> REVIS <input type="checkbox"/> OTHER _____ | PER ORDER _____ |

ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.

PCB MUST BEAR THE UL94-V0 UL REEVIEWED MATERIAL D NUMBER

ADDITIONAL REQUIREMENTS: _____

MISDIRECTION: ☐ YES ☐ NO ☐ NONE ☐ REQUIRED ☐ PER ORDER

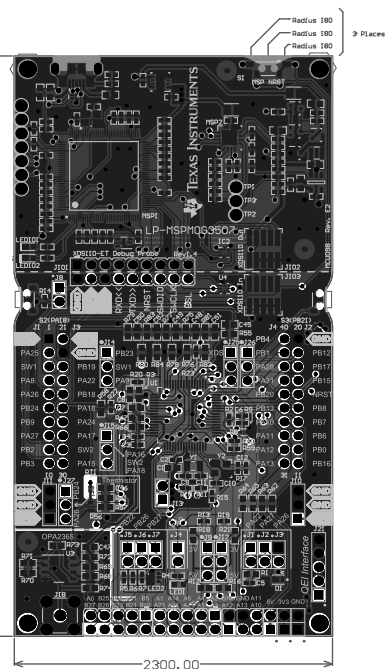
DATE BOARD ELEC. TEST: _____



FILE 14962

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ACTUM DESIGNER VERSION



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.

ASSEMBLY VARIANT: [No Variations]

COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.

[illegible]

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