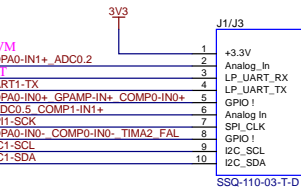
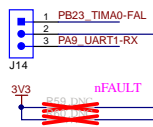


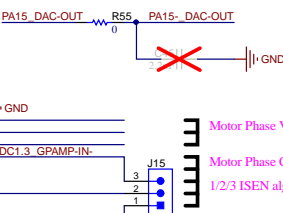
BoosterPack Connectors

Pin Selection for J1, pin3



SSQ-110-03-T-D

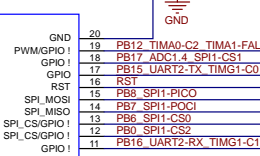
RC Filter for DAC Output



Motor Phase Voltage
Motor Phase Current
1/2/3 ISEN algorithm

Pin Selection for J3, pin29

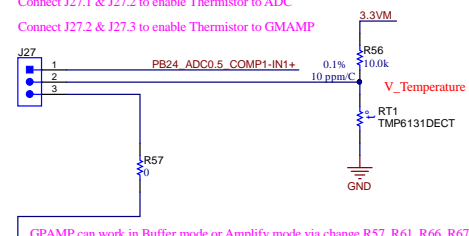
J15 is to flexibly adopt 1 ISEN algorithm, 2 ISENs algorithm and 3 ISENs algorithm
Some DRV BP the ISEN A/B/C is 26 27 28, some is 27 28 29



SSQ-110-03-T-D

Thermistor Circuit

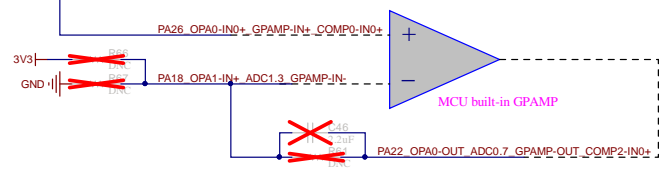
Connect J27.1 & J27.2 to enable Thermistor to ADC
Connect J27.2 & J27.3 to enable Thermistor to GMAMP



GPAMP can work in Buffer mode or Amplify mode via change R57, R61, R66, R67, C46

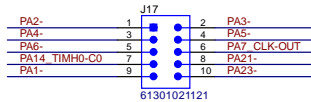
GPAMP Test Circuit

This GPAMP can be used in Thermistor or Motor Control (3 ISEN algorithm)

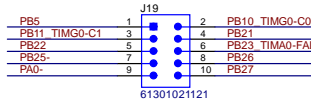


R66, R67 is voltage bias for GPAMP, 100-300mV

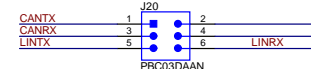
Pin headers at the Low Side of the Board



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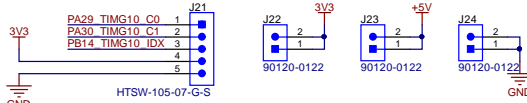


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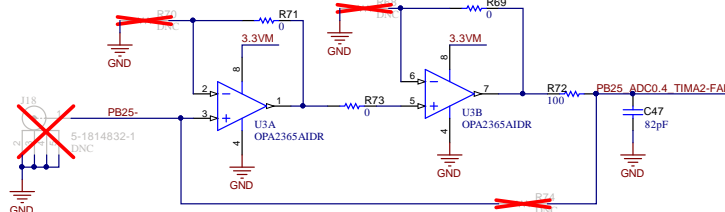
PBC03DAAN

QEI Interface



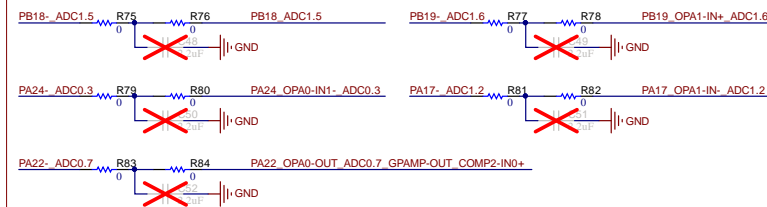
PA19, PA20 are SWDIO, SWDCLK for debug/programming interface. They are on J101, pin14, pin16

ADC Input with Active Buffer



Default OPA2365 is populated for the ADC input test;
If OPA2365 is NOT used, R72 need to be taken off and R74 0-ohm to be populated;

RC Filter for ADC Input

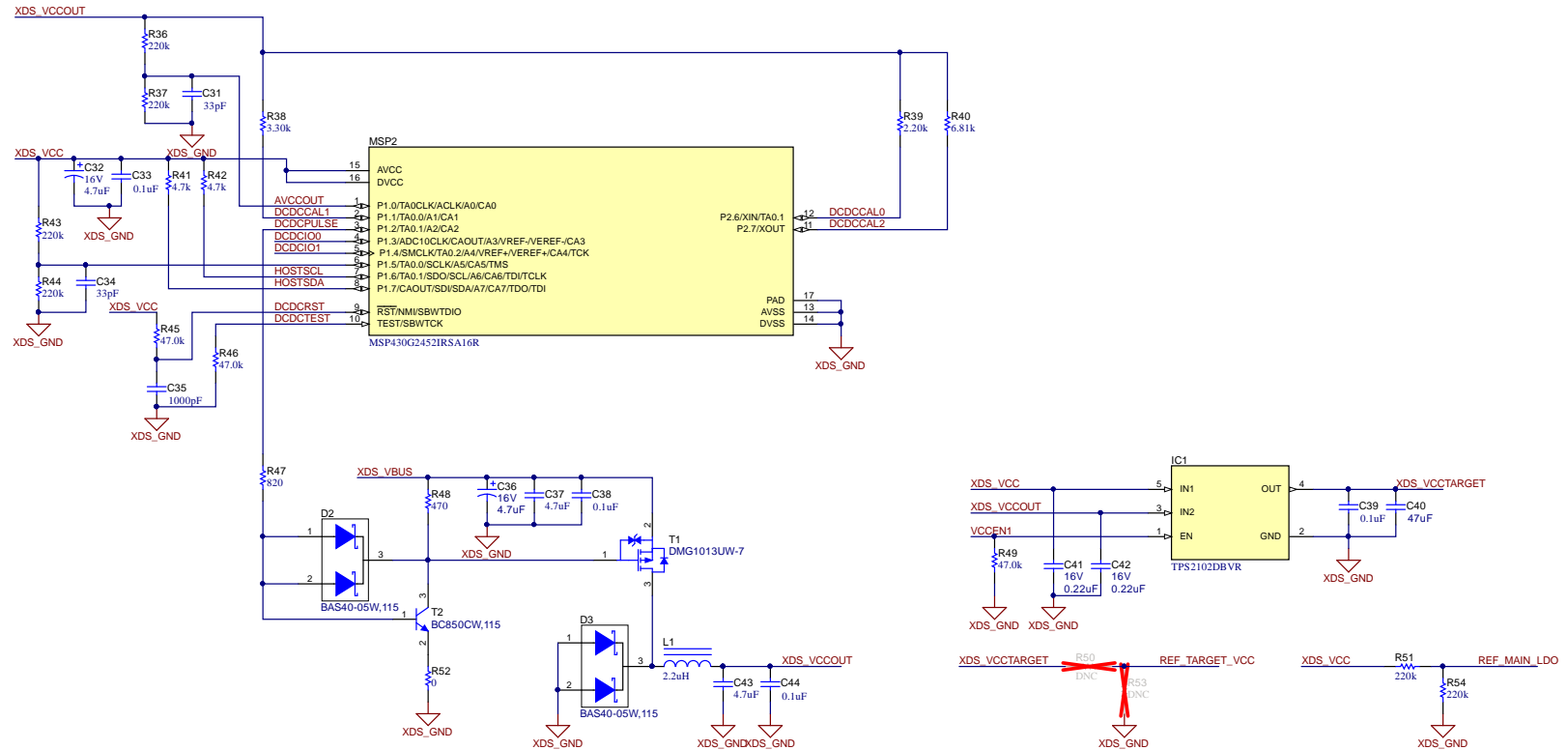


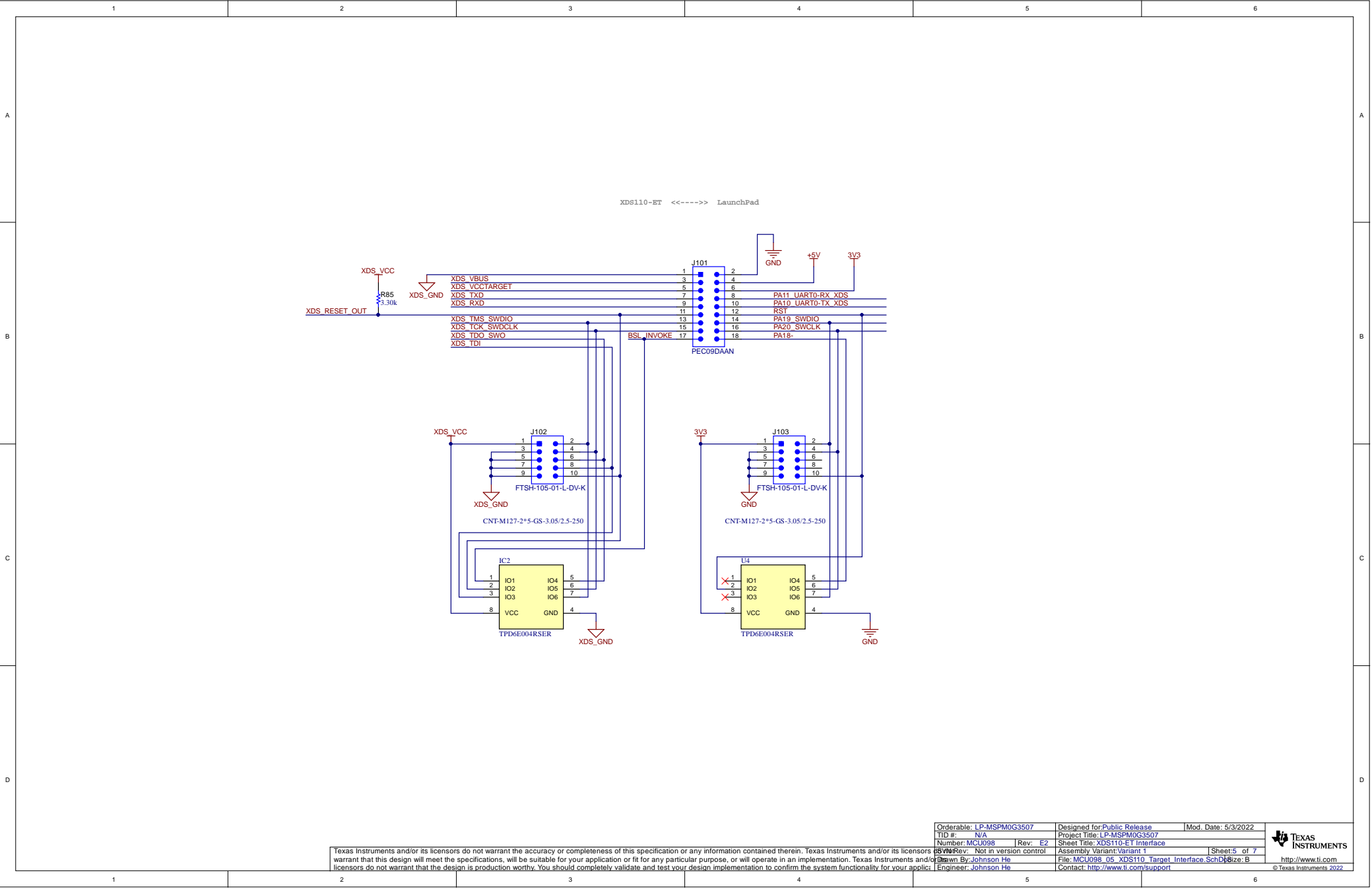
Orderable: LP-MSPM0G3507	Designed for: Public Release	Mod. Date: 5/3/2022
TID #: N/A	Project Title: LP-MSPM0G3507	
Number: MCU098	Rev: E2	Sheet Title: BoosterPack Connectors
Rev: Not in version control	Assembly Variant: Variant 1	Sheet 2 of 7
Drawn By: Johnson He	File: MCU098_02_BoosterPack_Connectors_Sch.Dwg	Size: B
Engineer: Johnson He	Contact: http://www.ti.com/support	

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Software-controlled DCDC converter

Energy measurement method protected under U.S. Patent
Application 13/329,073 and subsequent patent applications





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