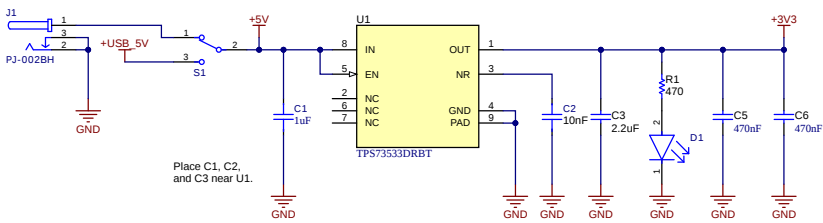
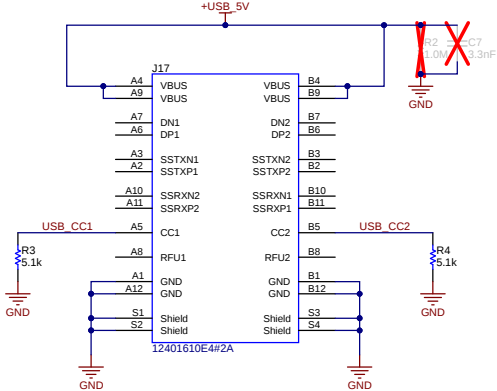


Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
R4.0	N/A	6/12/12	SBL	Redesign for HSEC8 connector
R4.1	N/A	7/23/12	SBL	PCB Change Only
R4.1	N/A	6/3/14	SBL	Fixed naming issues in the HSEC symbol (the netlist was not affected)
R5.0	N/A	2/26/18	SBL	Fixed naming issues in the HSEC symbol and Pwr and Gnd Connections
R6.0	N/A	4/24/18	CW	PCB Change Only
F	N/A	5/12/21	GM	Added FSI connector, brought additional HSEC pins to headers, switched to USB Type-C connector, added 20-pin JTAG header, changed to new LDO, and changed to letter revision. Updated PCB design.

Input Power Switch and 3.3V Supply



USB Type-C Connector



FSI Connector

