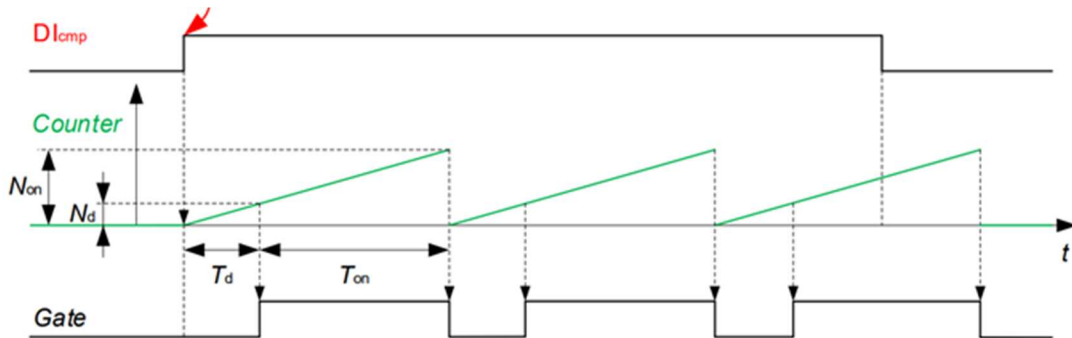# Henri, Guillaume

Attachments: syscfg.7z

We have here an eval board (MSPM0C1104) and we managed to generate a pwm on a output pin, by triggering it via a input pin.
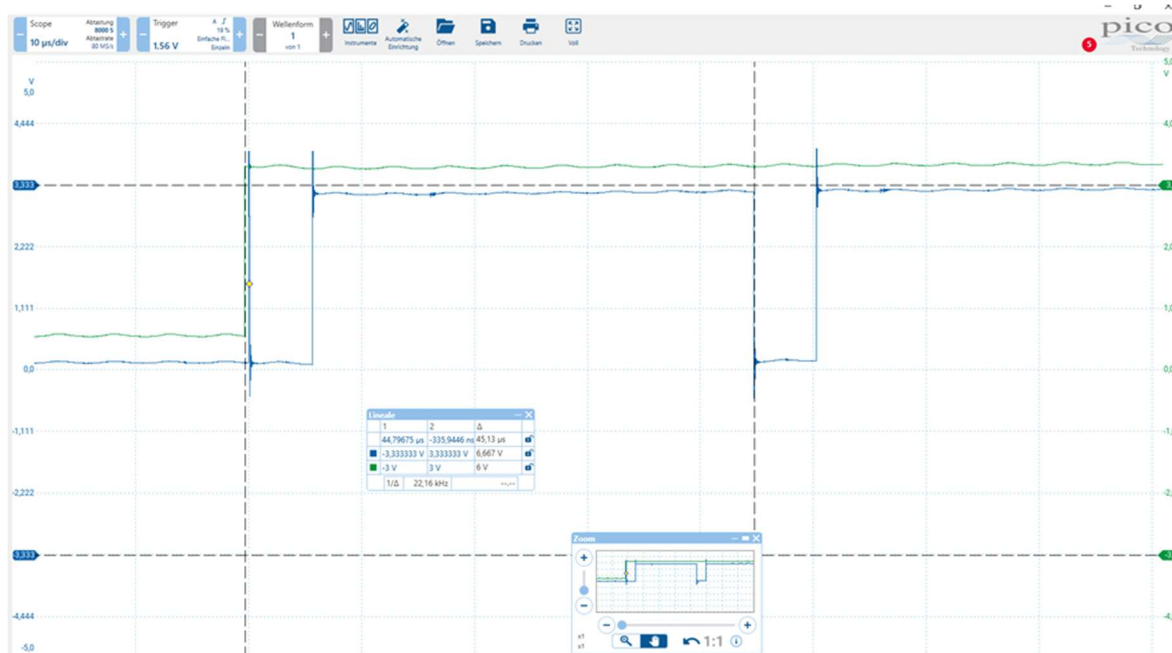The goal would be to have the following behavior :
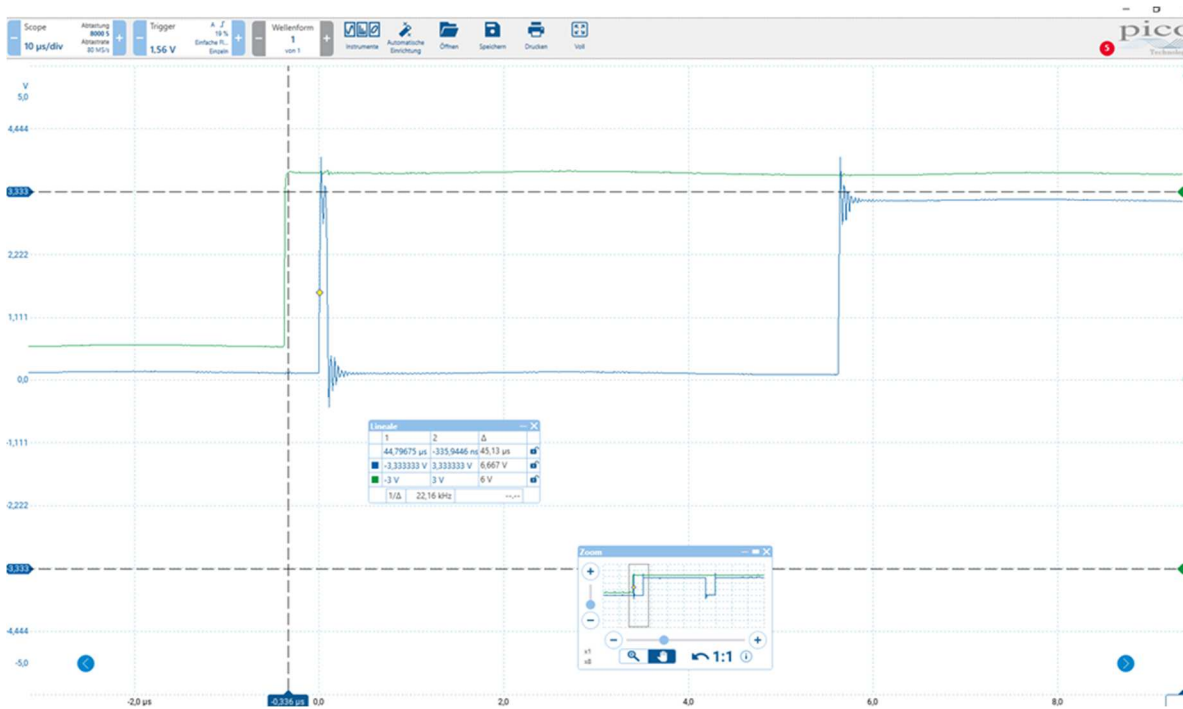

We would have two questions here

Q1/ By inverting the PWM Channel we get the right output but there is a spike in the beginning/ ~at the trigger time. Why is this and can this be removed?

Blue- PWM Channel output.
Green- Input.

When PWM Channel not inverted, there are not glitches like this.

Q2/ How can this PWM be stopped, when there is a falling edge of the input, as shown in the first figure? Is it via Fault Pin?

The Sysconfig file is attached, if there is something to inspect from your side.