LP-AM261 - AM261x Launchpad EVM

SYSTEM BLOCK DIAGRAM

TI Launchpad XL Form-Factor
- AM261x MCU
- Dual R5F ARM Safety Cores
- 5 Mbyte SRAM
- 8 Mbyte OSPI Flash Memory
- xMbyte OSPI RAM Memory (expansion)

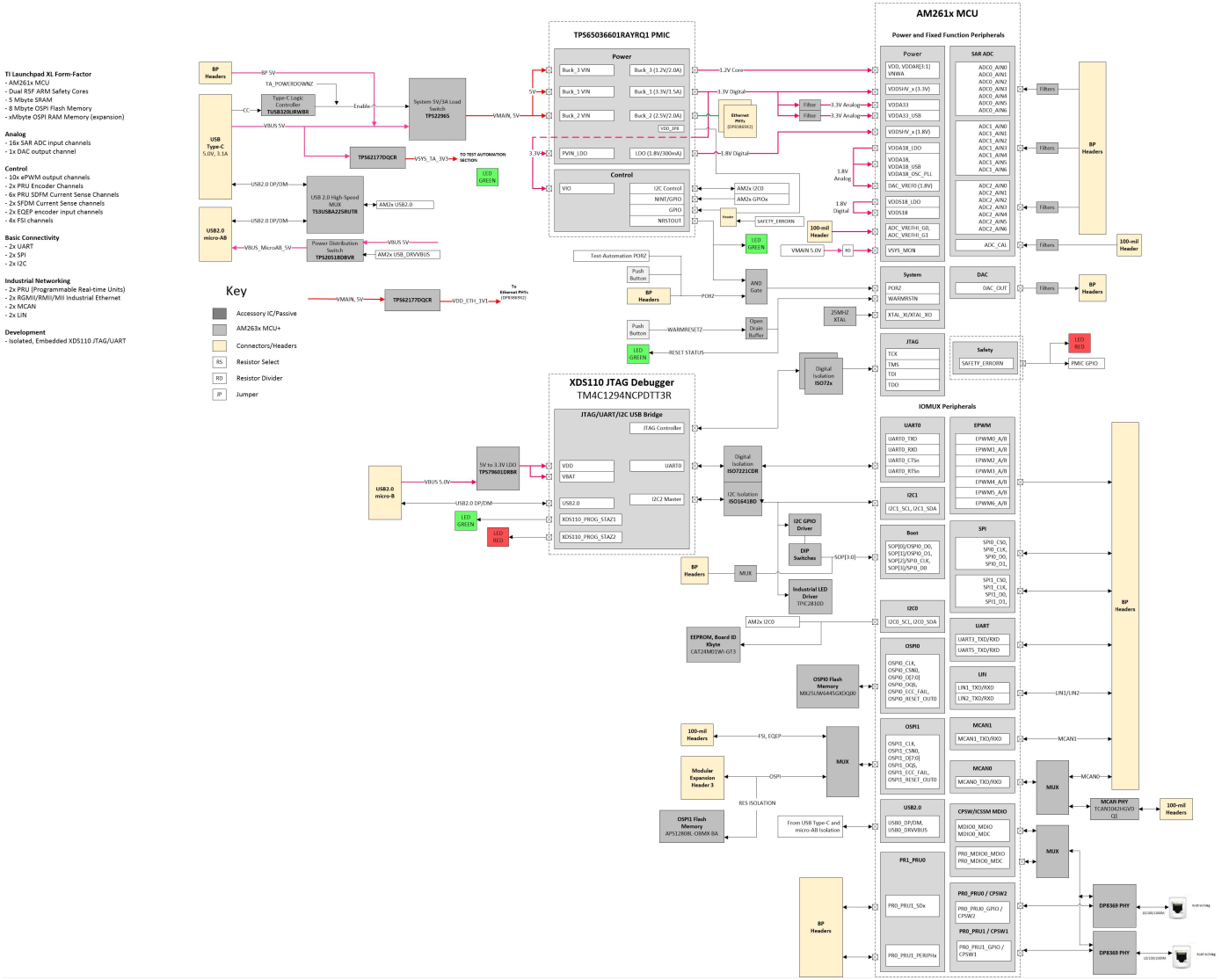
Analog
- 16x SAR ADC input channels
- 1x DAC output channel

Control
- 10x ePWM output channels
- 2x PRU Encoder Channels
- 6x PRU SDFM Current Sense Channels
- 2x SFDM Current Sense channels
- 2x EQEP encoder input channels
- 4x FSI channels

Basic Connectivity
- 2x UART
- 2x SPI
- 2x I2C

Industrial Networking
- 2x PRU (Programmable Real-time Units)
- 2x RGMII/RMII/MII Industrial Ethernet
- 2x MCAN
- 2x LIN

Development
- Isolated, Embedded XDS110 JTAG/UART



Revision History

Rev	ECN #	Approved Date	Approved by	Notes
A	N/A	2025-06-24	Sridhar Potluri	Drafted from E2 - Removed Ethernet add-on board scheme and replaced with 2x DP83869 PHYs and RJ-45 connectors - Added U81 Mux - Swapped GPIO5 and GPIO124 assignments. - Updated IO Expanders reset control with porz - Rearranged ADC channels across U17, U19 - Added 1uF R323 and R325 cap, for ETH power VDD_1V1 - Updated System Block Diagram

Orderable: LP-AM261		Designed for:	Mod. Date: 7/9/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: Cover sheet	
SVN Rev: 558 [Locally Modified]		Assembly Variant: 001	Sheet: 1 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_01_CoverSheet.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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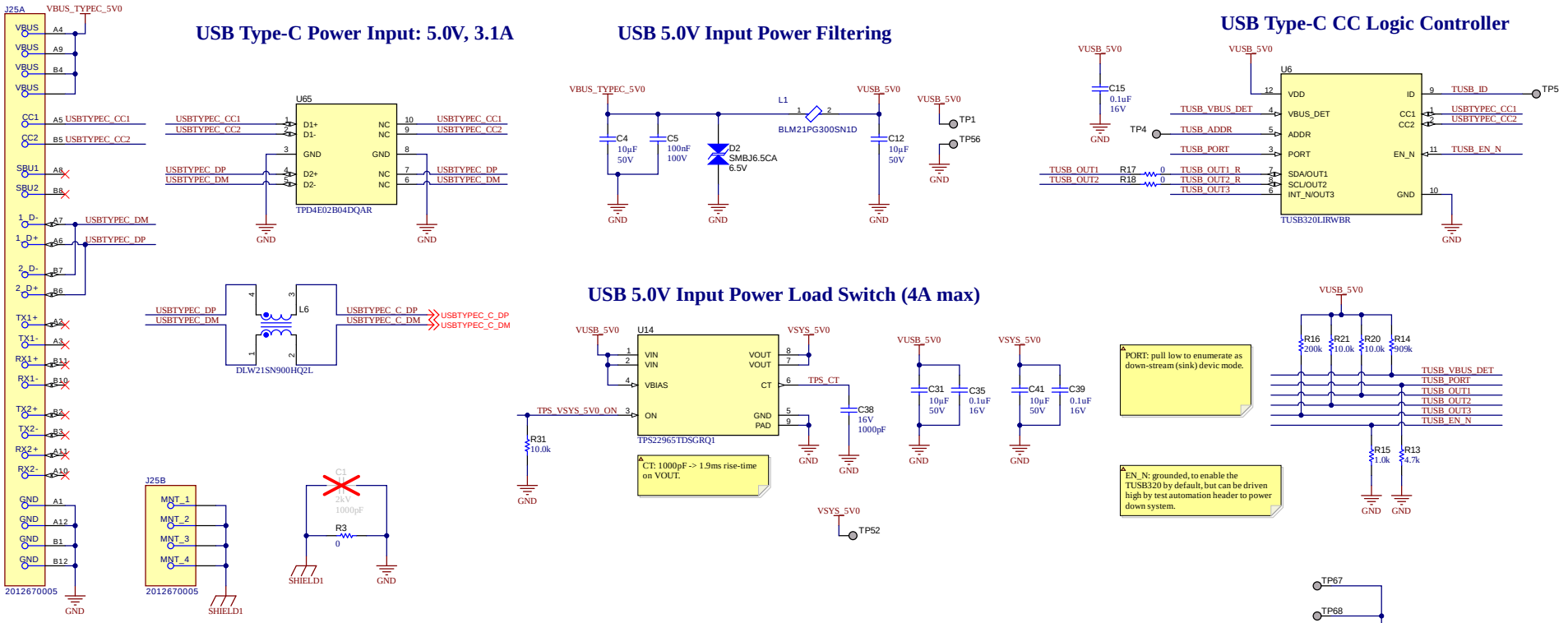
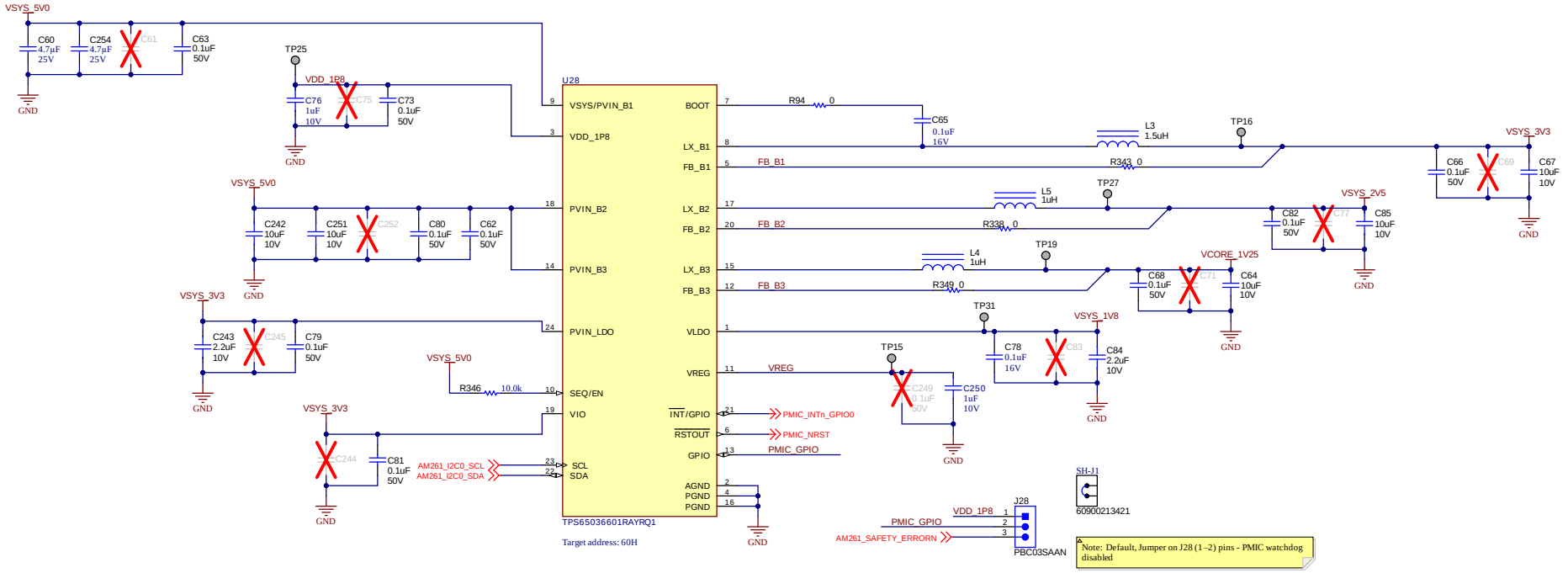
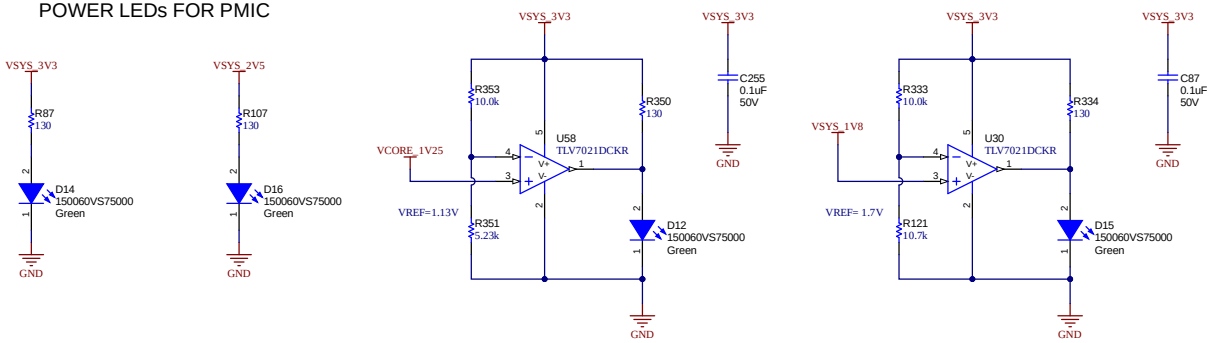


Orderable: LP-AM261		Designed for:	Mod. Date: 6/3/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: USB TYPEC INPUT PWR	
SVN Rev: 473		Assembly Variant: 001	Sheet: 2 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_02_USB_TYPEC_IN_PWR.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

PMIC



POWER LEDs FOR PMIC



Orderable: LP-AM261		Designed for:	Mod. Date: 6/24/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: PMIC	
SVN Rev: N/A		Assembly Variant: 001	Sheet: 3 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_03_PMIC_PWR.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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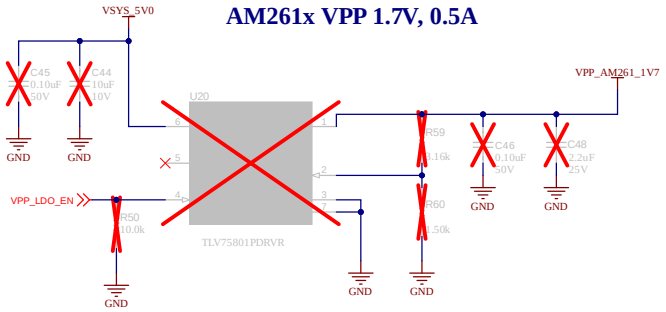
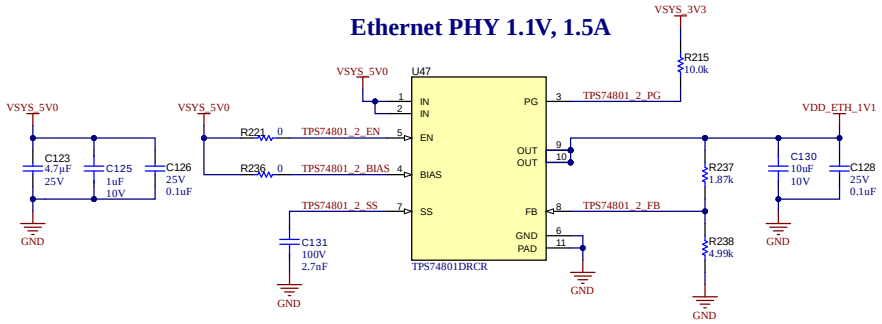
AM261x VPP 1.7V, 0.5A



Ethernet PHY 1.1V, 1.5A



Orderable: LP-AM261		Designed for:	Mod. Date: 6/3/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: VPP 1.7V LDO	
SVN Rev: 473		Assembly Variant: 001	Sheet: 4 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_04_VPP1V7_PWR.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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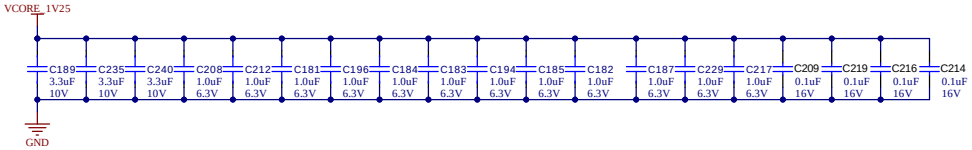
VDD 1V25 Core Digital



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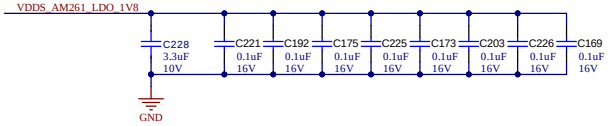

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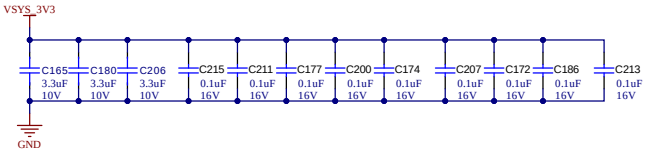

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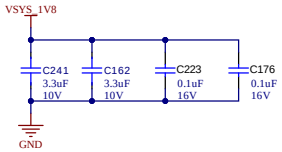
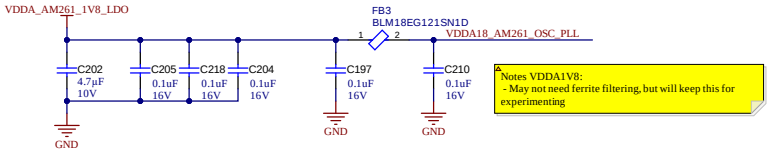
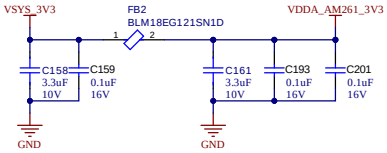
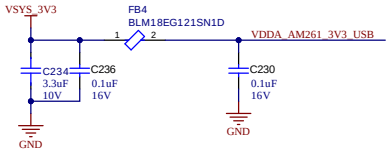


VDDA 1V8 Analog

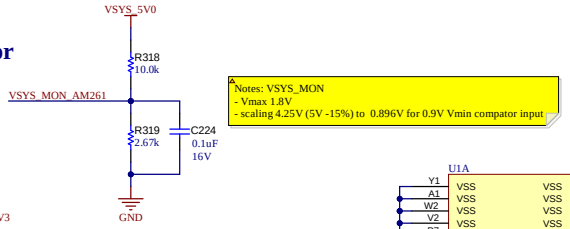


VDDA 3V3 Analog

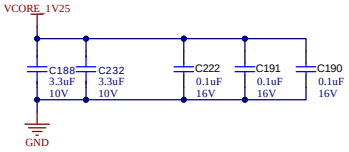




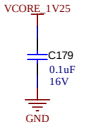
VSYS Voltage Monitor



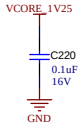
VDDAR[3:2] 1V25 SRAM Array



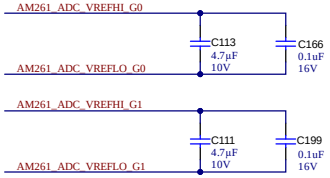
VNWA 1V2

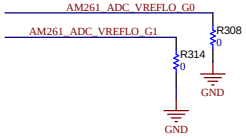


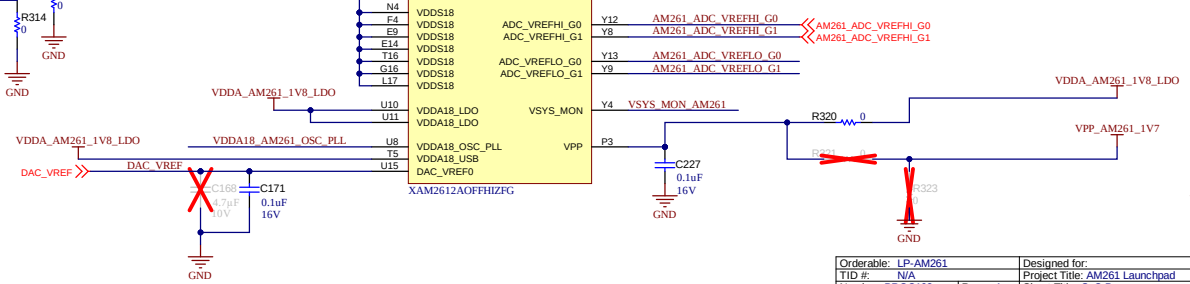
VDD 1V2 Temperature

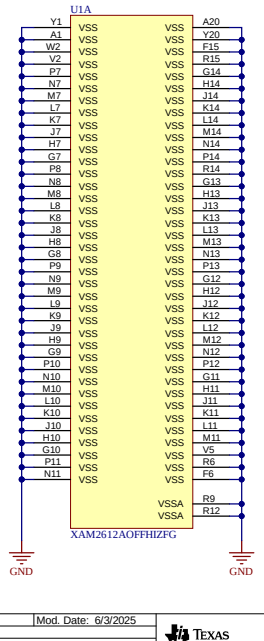


ADC VREF Decoupling









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Orderable: LP-AM261		Designed for:	Mod. Date: 6/3/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: SoC Power	
SVN Rev: 473		Assembly Variant: 001	Sheet: 5 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_05_SoC_Power.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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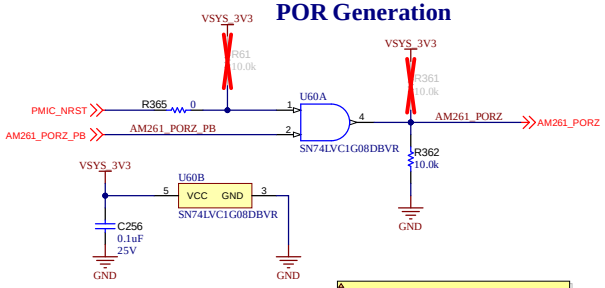
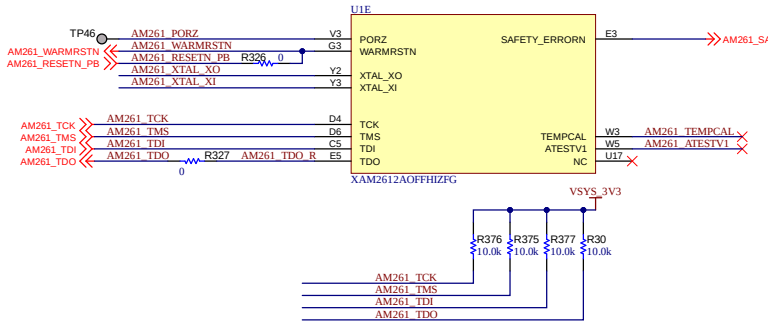
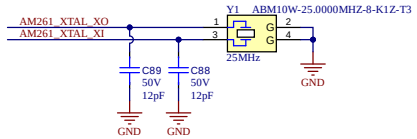
AM261x Clock, Reset, Boot, JTAG

POR Generation

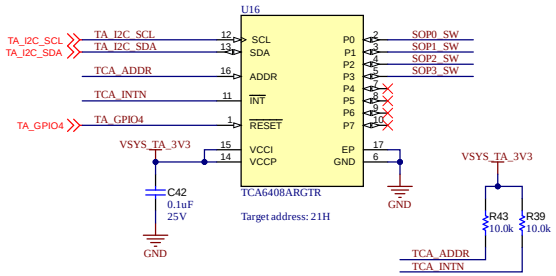




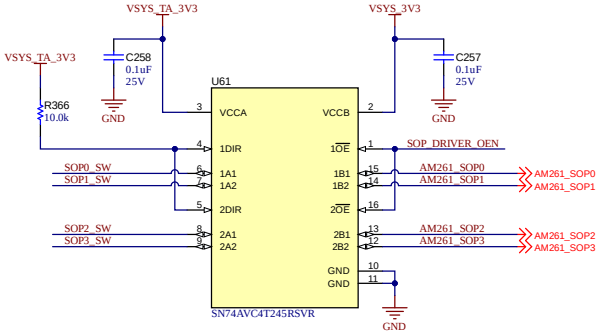
25 MHz Crystal



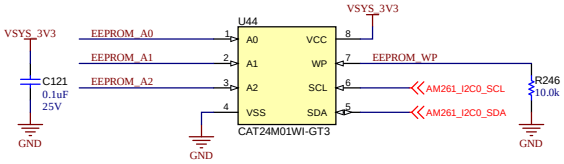
Test Automation SOP Select



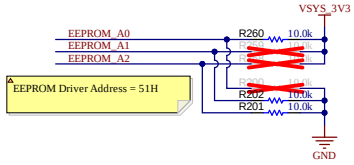
SOP State Driver



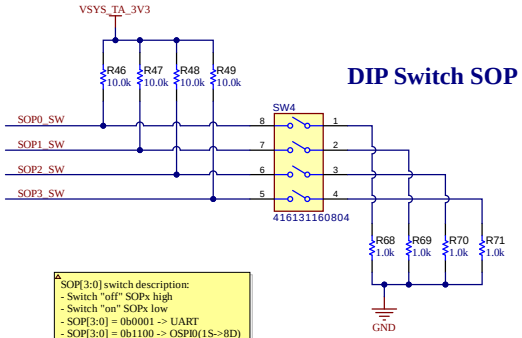
Board ID EEPROM

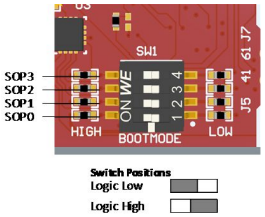


EEPROM Address

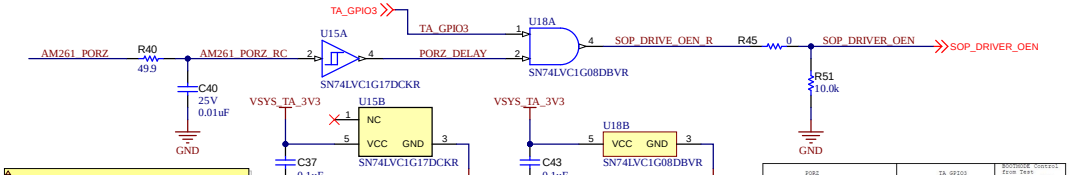


DIP Switch SOP Select





PORZ SOP Driver RC Delay



PORZ	TA_GPIO3	BOOTMODE Control from Test Automation MUX
HIGH	LOW	Enabled
HIGH	HIGH	Disabled

Orderable: LP-AM261	Designed for:	Mod. Date: 6/3/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193 Rev: A	Sheet Title: Clock_Reset_Boot_JTAG	
SVN Rev: 473	Assembly Variant: 001	Sheet: 6 of 26
Drawn By: Vijetha J, Kiran	File: PROC193A_06_Clock_Reset_Boot_JTAG.SchDoc	Size: B
Engineer: Vijetha J, Kiran	Contact:	

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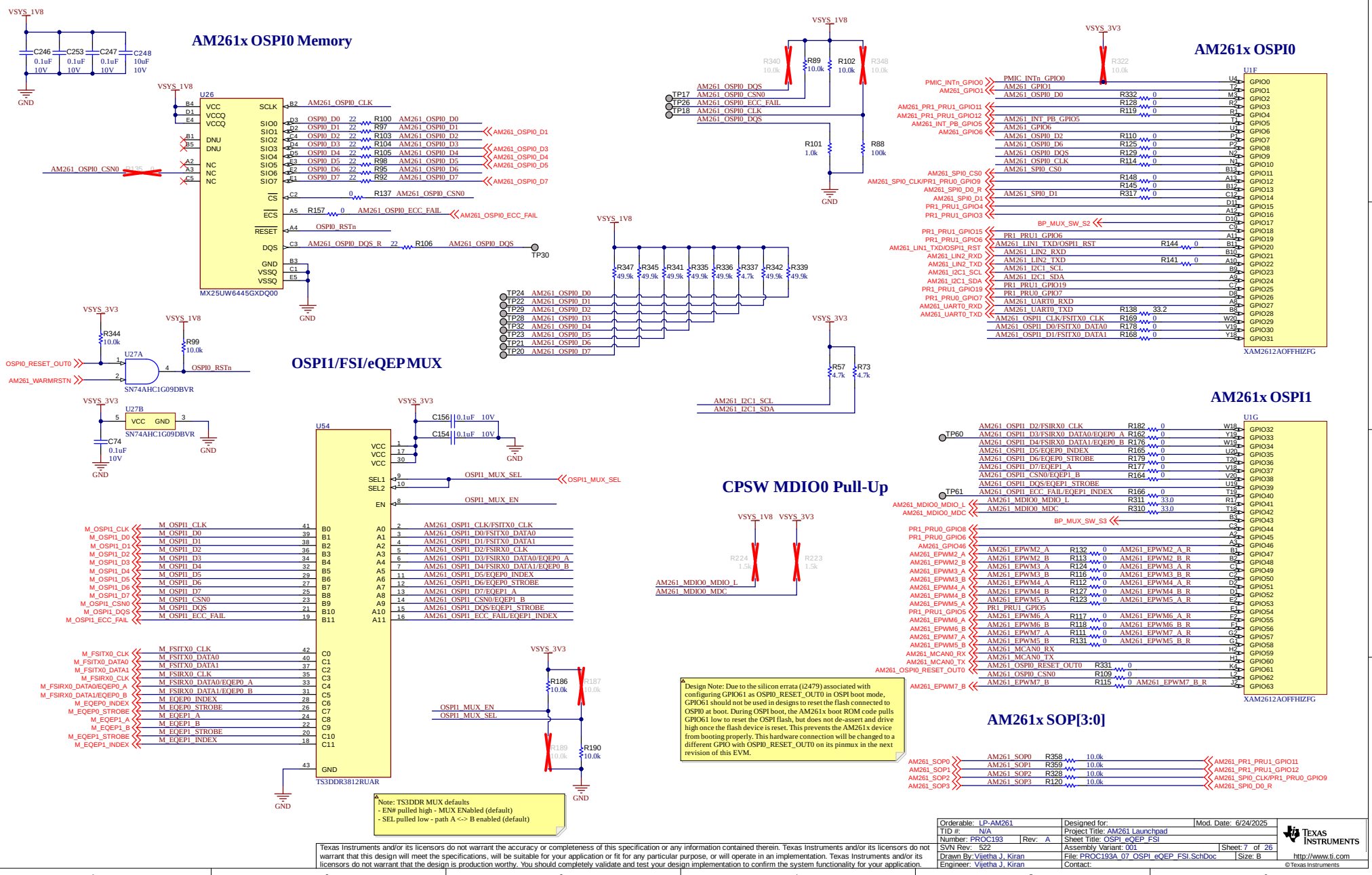
AM261x OSPI0 Memory
MX25UW6445GXDQ00
AM261x OSPI0
XAM2612AOFFHIZFG
OSPI1/FSI/eQEP MUX
SN74AHC1G09DBVR
TS3DDR3812RUAR
CPSW MDIO0 Pull-Up
AM261x OSPI1
AM261x SOP[3:0]
Design Note: Due to the silicon errata (i2479) associated with configuring GPIO61 as OSPI0_RESET_OUT0 in OSPI boot mode, GPIO61 should not be used in designs to reset the flash connected to OSPI0 at boot. During OSPI boot, the AM261x boot ROM code pulls GPIO61 low to reset the OSPI flash, but does not de-assert and drive high once the flash device is reset. This prevents the AM261x device from booting properly. This hardware connection will be changed to a different GPIO with OSPI0_RESET_OUT0 on its pinmux in the next revision of this EVM.
Note: TS3DDR MUX defaults
- EN# pulled high - MUX ENabled (default)
- SEL pulled low - path A <-> B enabled (default)
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Orderable: LP-AM261
TID #: N/A
Number: PROC193
Rev: A
SVN Rev: 522
Drawn By: Vijetha J, Kiran
Engineer: Vijetha J, Kiran
Designed for:
Mod. Date: 6/24/2025
Project Title: AM261 Launchpad
Sheet Title: OSPI_eQEP_FSI
Assembly Variant: 001
Sheet: 7 of 26
File: PROC193A_07_OSPI_eQEP_FSI.SchDoc
Size: B
Contact:
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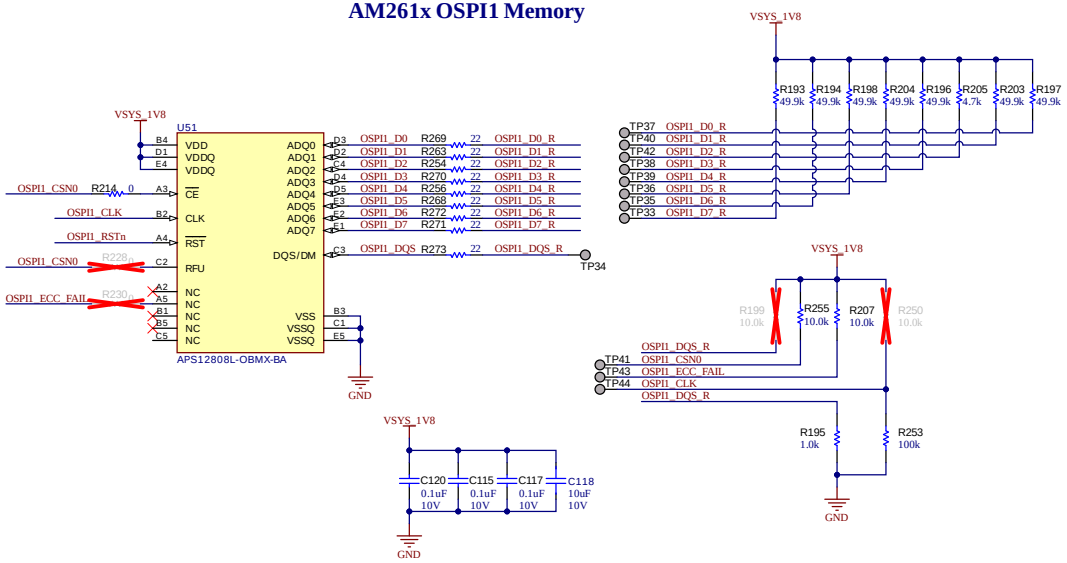
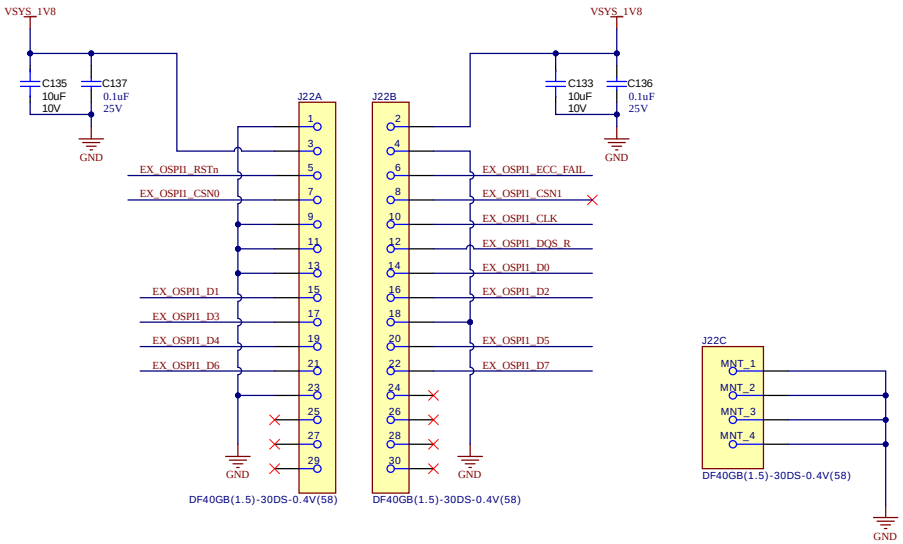
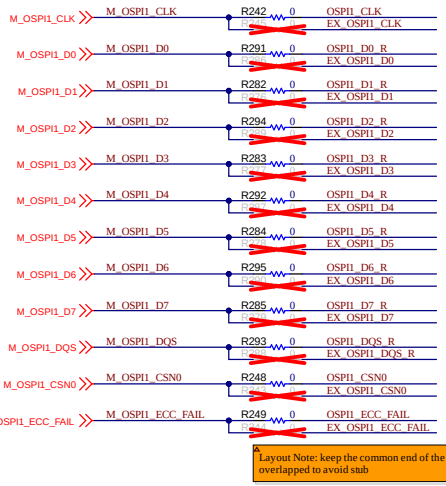
AM261x OSPI1 Memory

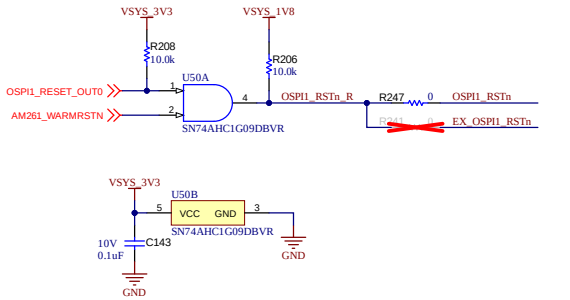


OSPI1 EXPANSION CONNECTOR





Layout Note: keep the common end of the resistors pads overlapped to avoid stub



Note:
- RES R242, R291, R282, R294, R283, R292, R284, R295, R285, R293, R248, R249 and R247 will be mounted and R245, R286, R276, R289, R277, R287, R278, R290, R279, R288, R243, R244 and R241 will be DNI for the on-board OPSI1 usage
- RES R242, R291, R282, R294, R283, R292, R284, R295, R285, R293, R248, R249 and R247 will be DNI and R245, R286, R276, R289, R277, R287, R278, R290, R279, R288, R243, R244 and R241 will be mounted for the External OPSI1 usage

Orderable: LP-AM261		Designed for:	Mod. Date: 6/3/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: OSPI1 memory and connector	
SVN Rev: 473		Assembly Variant: 001	Sheet: 8 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_08_OSPI1_Conn.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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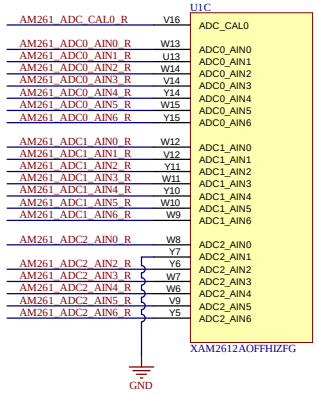
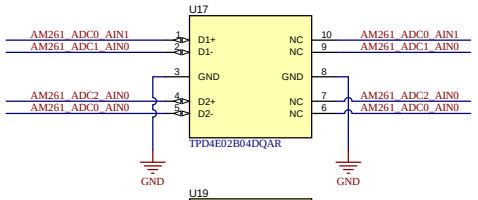
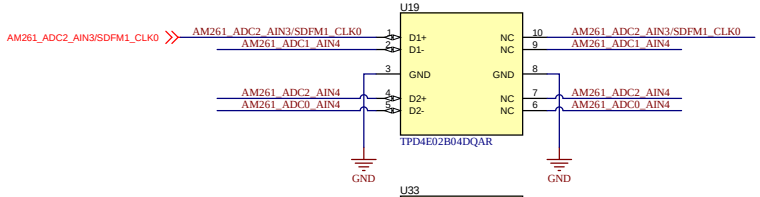
AM261x ADC and DAC

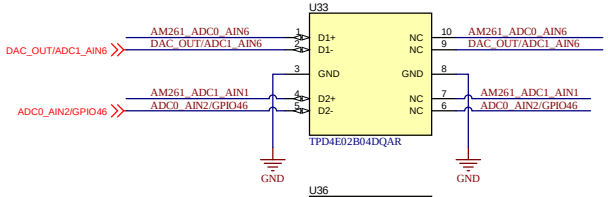
SAR ADC RC Filtering

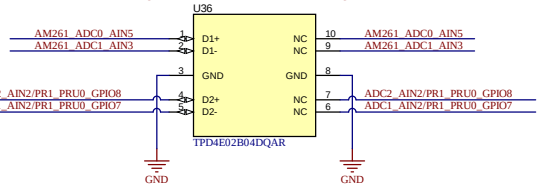


Layout Note: Place external ESD near connectors

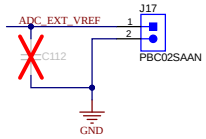




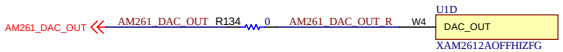




ADC External VREF Header



DAC Output



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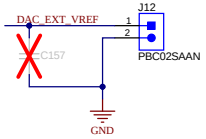
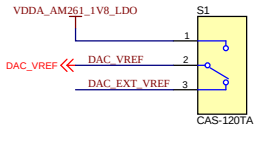
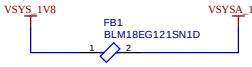
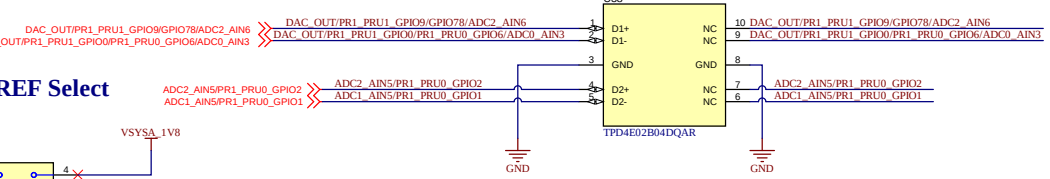


DAC VREF Select

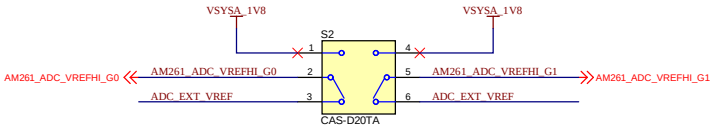


DAC VREF Switch Select - 1.8V VREF must be provided for AM261x comparators to function
- Select pins 1-2 select AM261x 1.8V analog LDO output as DAC VREF (default)
- Select pins 2-3 select external 1.8V VREF (if any provided)



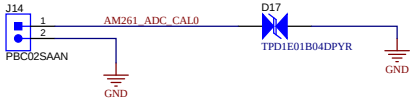


ADC VREF Select



DAC VREF Switch Select
- Switch in 1-2 position allows AM261x on-board 1.8 VREF (VSYSA_1V8) for VREFG0
- Switch in 2-3 position allows on-die ADC_VREF (default) or external 1.8V VREF (if any provided) for VREFG0

- Switch in 4-5 position allows AM261x on-board 1.8 VREF (VSYSA_1V8) for VREFG0/G1
- Switch in 5-6 position allows on-die ADC_VREF (default) or external 1.8V VREF (if any provided) for VREFG1



Orderable: LP-AM261	Designed for:	Mod. Date: 6/30/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193 Rev: A	Sheet Title: ADC_DAC	
SVN Rev: 546	Assembly Variant: 001	Sheet: 9 of 26
Drawn By: Vijetha J, Kiran	File: PROC193A_09_ADC_DAC.SchDoc	Size: B
Engineer: Vijetha J, Kiran	Contact:	

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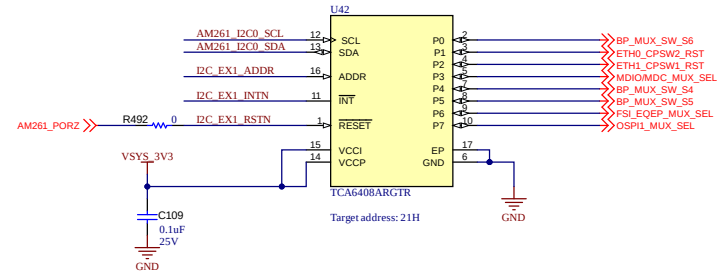
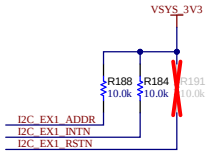
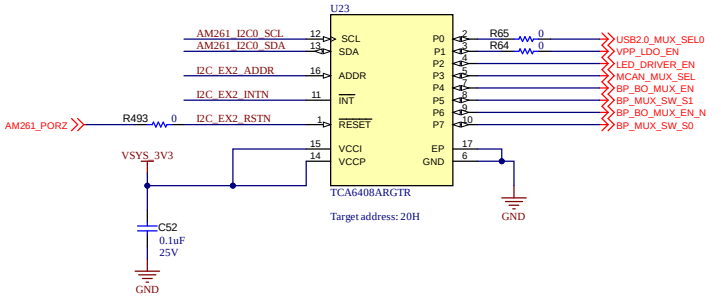
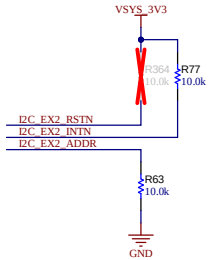
AM261x_USB2.0, I2C0 and IO expander

I2C IO Expanders

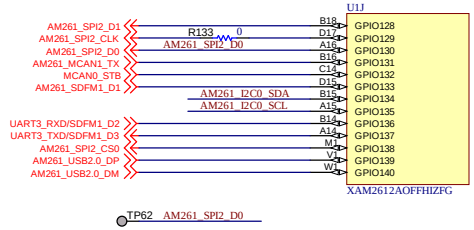




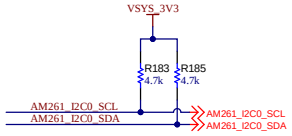




AM261x_USB2.0, I2C0



I2C0 Pull-Up



Orderable: LP-AM261		Designed for:	Mod. Date: 6/24/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: AM261x_USB2.0 and I2C	
SVN Rev: 522		Assembly Variant: 001	Sheet: 10 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_10_USB_I2C0_IOEXP.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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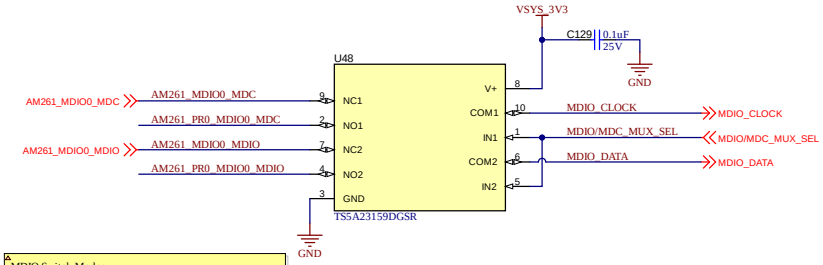
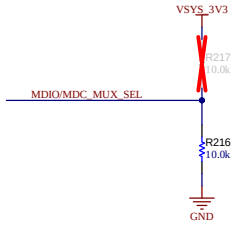
AM261x CPSW - RGMII and MDIO

CPSW/ICSSM MDIO Switch

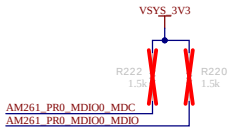


MDIO Switch Modes
- IN1/2 pulled low-CPSW MDIO selected
-IN1/2 pulled high -ICSSM MDIO selected
- IN1/2 pulled low by default - CPSW_MDIO Selected

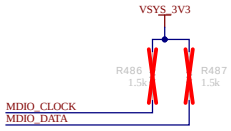
IN	NC to COM, COM to NC	NO to COM, COM to NO
L	ON	OFF
H	OFF	ON

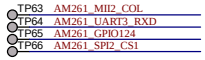


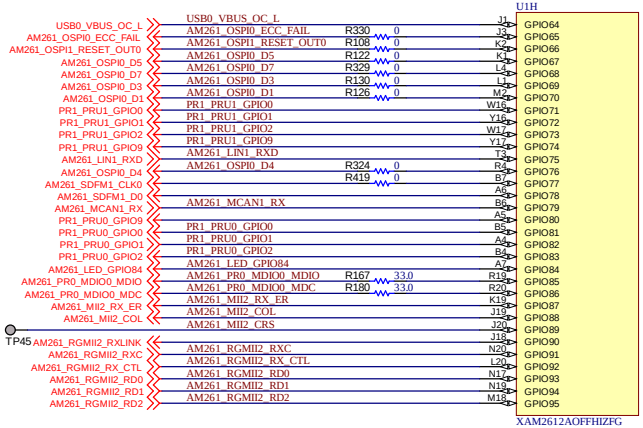
PR0 MDIO Pull-Up

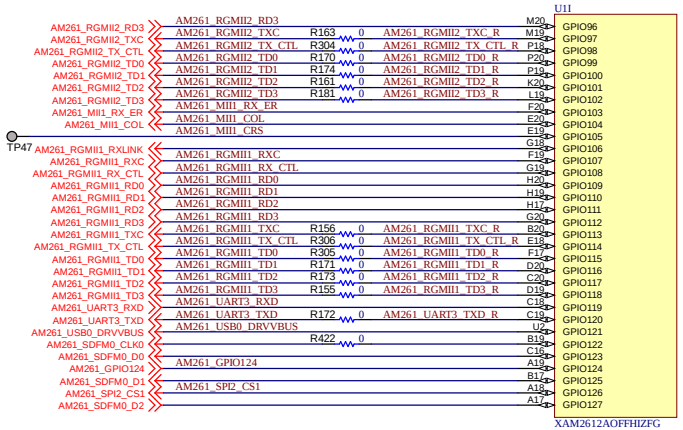


MDIO Pull-Up









Orderable: LP-AM261	Designed for:	Mod. Date: 6/24/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193 Rev: A	Sheet Title: RGMII, MDIO	
SVN Rev: 522	Assembly Variant: 001	Sheet: 11 of 26
Drawn By: Vijetha J, Kiran	File: PROC193A_11_RGMII_MII.SchDoc	Size: B
Engineer: Vijetha J, Kiran	Contact:	

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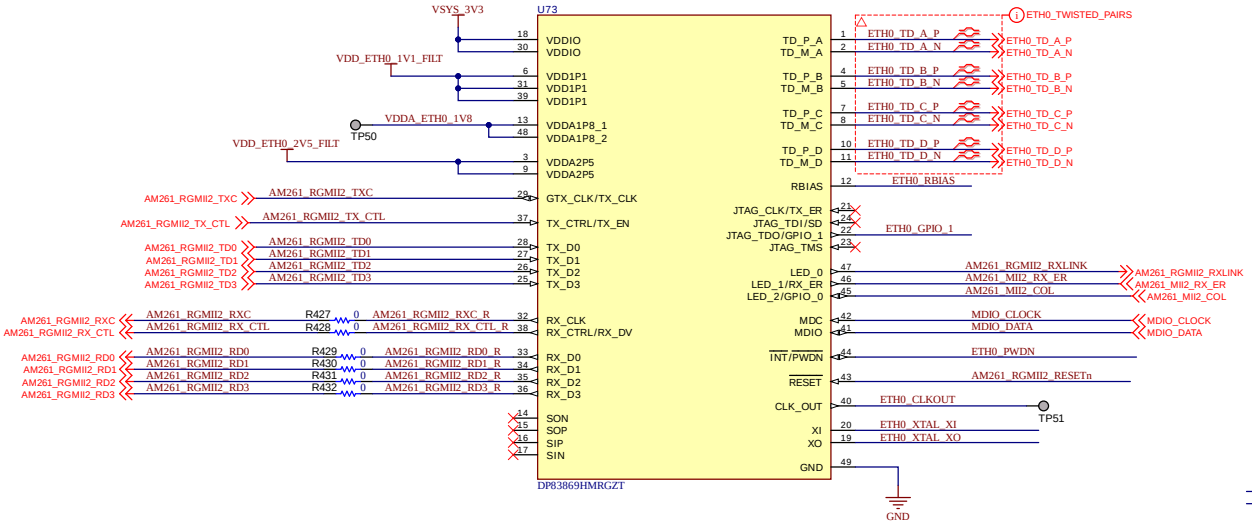
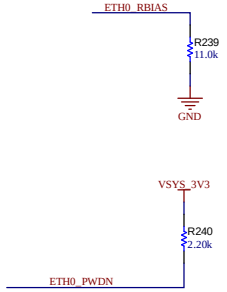
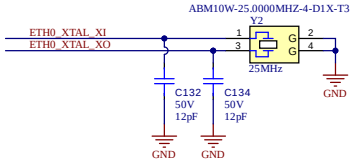
AM261x PR0 PRU0 /CPSW2

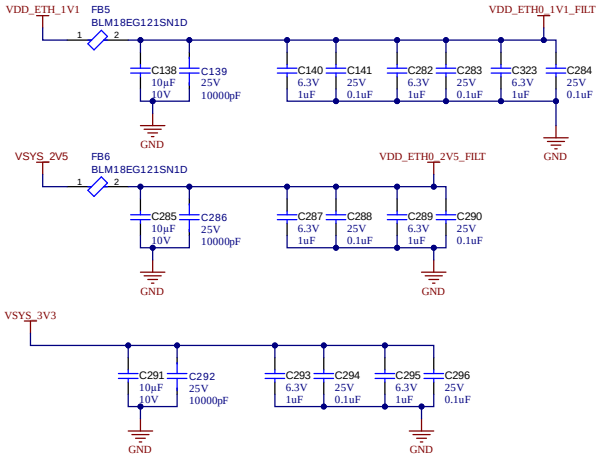




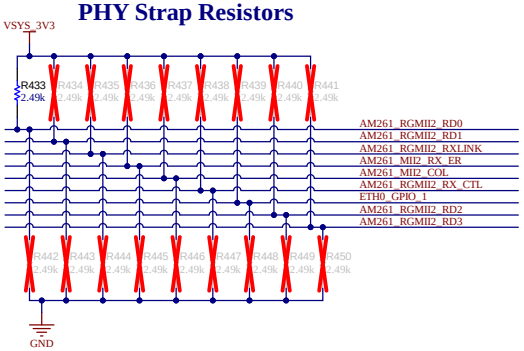
25 MHz Crystal



PHY Decoupling

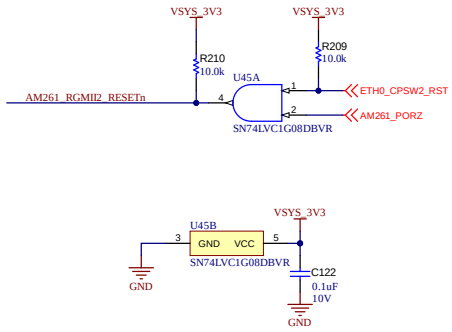


PHY Strap Resistors



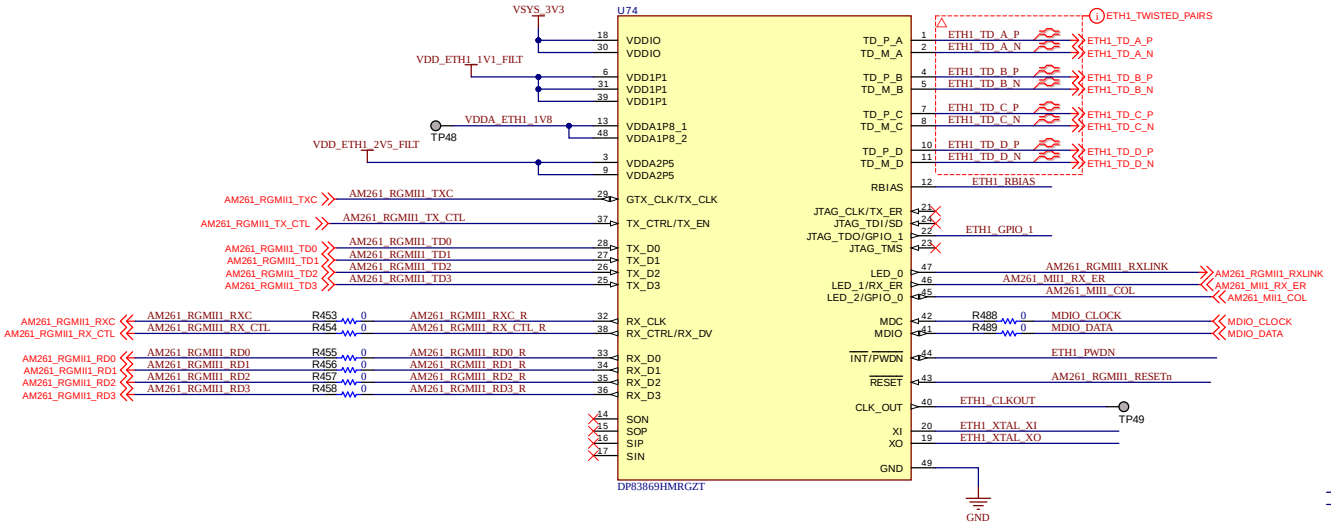
Note: PHY Strap Resistor Selection
- PHY ADDRESS = 0b0011, 0x3
- Auto-negotiation, 10/100/1000 advertised, Auto-MDI-X
- RGMII to Copper (1000BaseT/100Base-TX/10Base-Te)

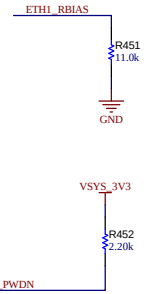
PHY_RST_LOGIC



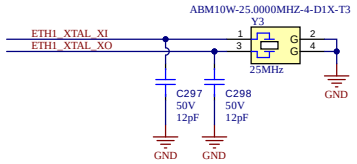
Orderable: LP-AM261		Designed for:	Mod. Date: 7/1/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: Ethernet Port 0	
SVN Rev: 558		Assembly Variant: 001	Sheet: 12 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_12_Ethernet_0.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

AM261x PR0 PRU1 /CPSW1

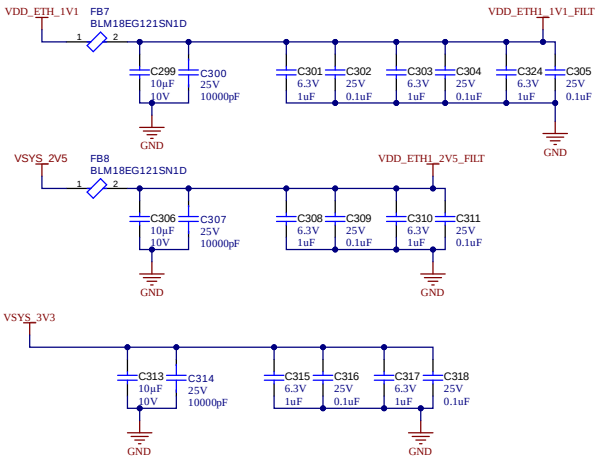




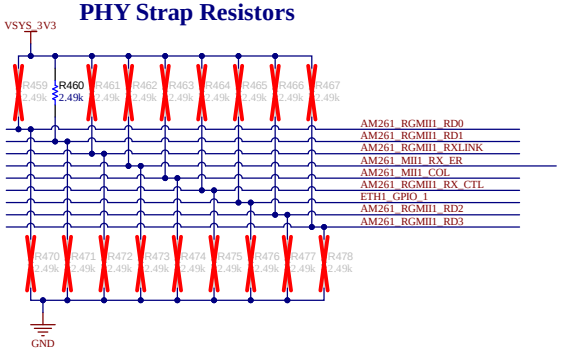
25 MHz Crystal



PHY Decoupling

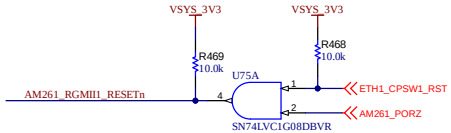


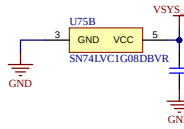
PHY Strap Resistors



Note: PHY Strap Resistor Selection
- PHY ADDRESS = 0b1100, 0xC
- Auto-negotiation, 10/100/1000 advertised, Auto-MDI-X
- RGMII to Copper (1000BaseT/100Base-TX/10Base-Te)

PHY_RST_LOGIC





Orderable: LP-AM261		Designed for:	Mod. Date: 7/1/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: Ethernet Port 1	
SVN Rev: 558		Assembly Variant: 001	Sheet: 13 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_13_Ethernet_1.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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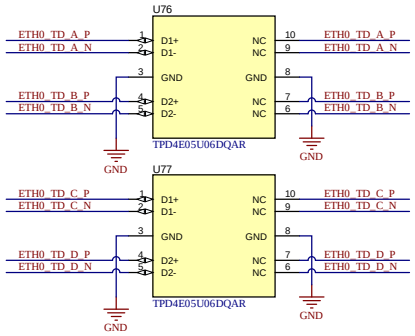
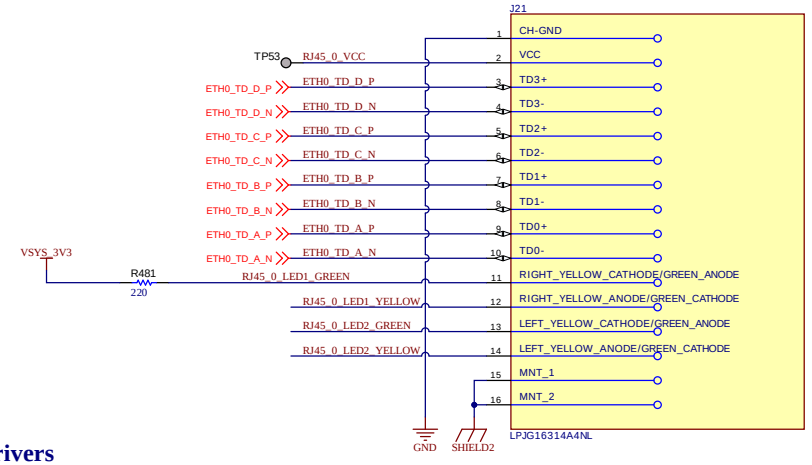
AM261x Ethernet PHY #0 - RJ-45 Jack

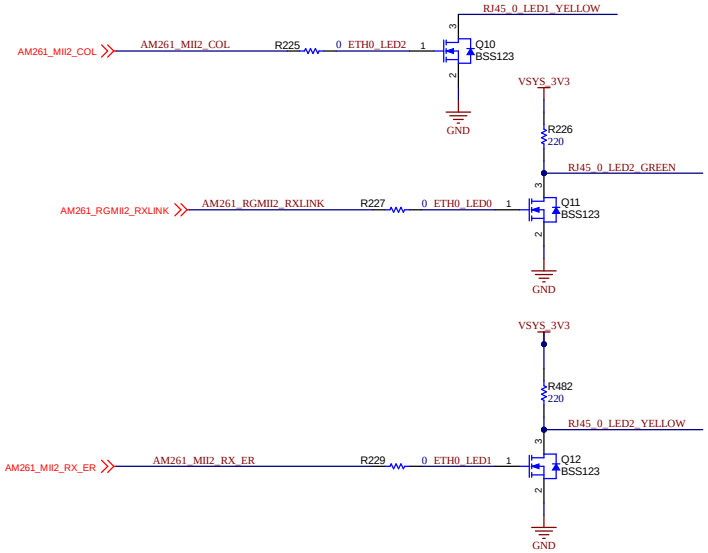
RJ-45 ESD Protection

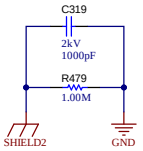


RJ-45 Jack #0



RJ-45 LED Drivers





Orderable: LP-AM261		Designed for:	Mod. Date: 6/10/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title:	
SVN Rev: 483		Assembly Variant: 001	Sheet: 14 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_14_Ethernet_RJ45_0.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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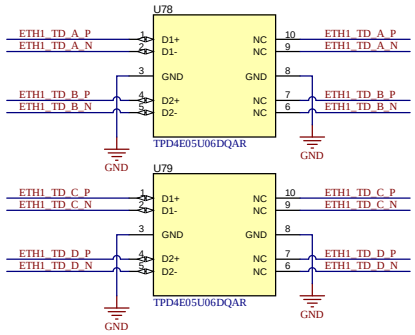
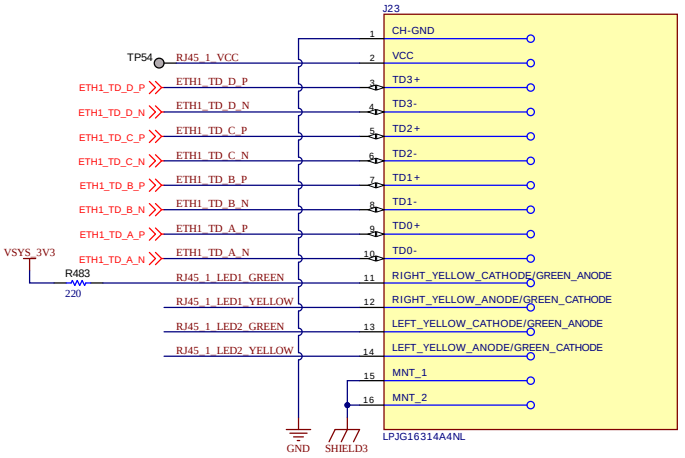
http://www.ti.com

AM261x Ethernet PHY #1 - RJ-45 Jack

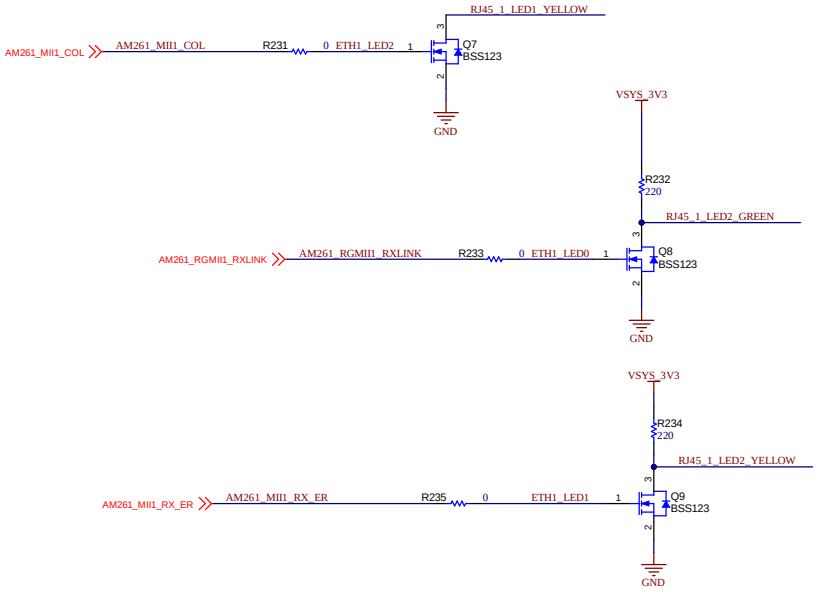
RJ-45 ESD Protection

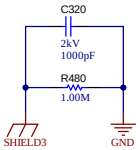


RJ-45 Jack #1



RJ-45 LED Drivers





Orderable: LP-AM261		Designed for:	Mod. Date: 6/10/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title:	
SVN Rev: 483		Assembly Variant: 001	Sheet: 15 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_15_Ethernet_RJ45_1.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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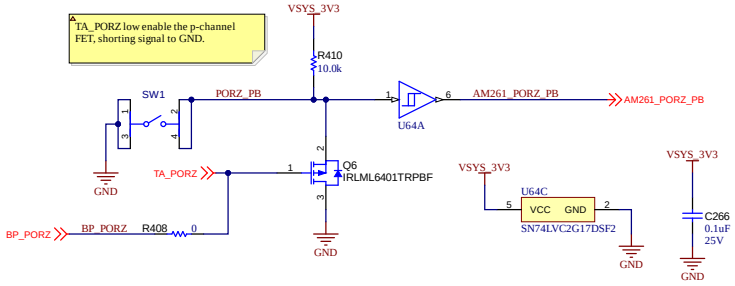
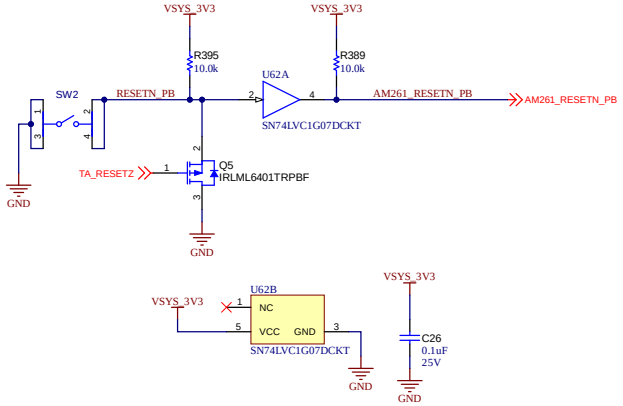
Push-Buttons

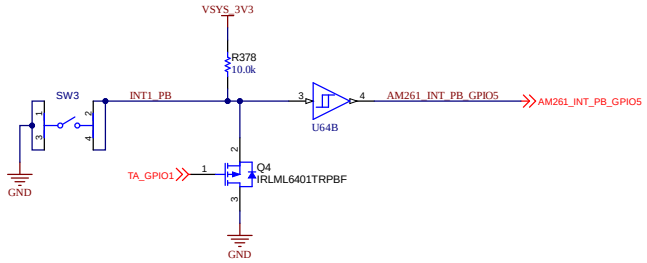
PORZ Push-Button and Test Automation



RESETZ Push-Button and Test Automation



INT1 Push-Button and Test Automation



Orderable: LP-AM261		Designed for:	Mod. Date: 6/24/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: Push Button	
SVN Rev: 522		Assembly Variant: 001	Sheet: 16 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_16_Push_Buttons.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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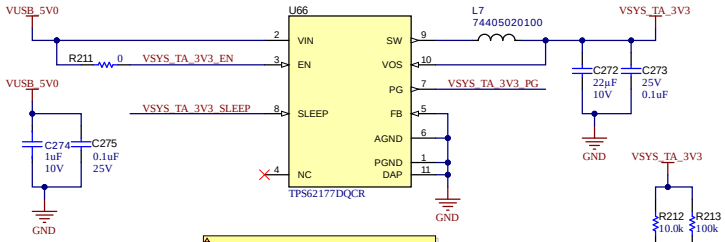
Test Automation Section

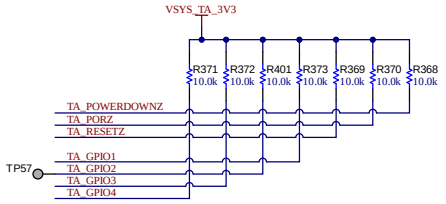
SIGNAL NAME	DESCRIPTION	Direction WRT CTRL	Internal/ External PU/PD states
TA_POWERDOWN	Used to Power down the system	OUTPUT	External Pullup
TA_PORZn	Used to Reset the SoC PORz	OUTPUT	External Pullup
TA_RESETz	SoC Warmreset	OUTPUT	External Pullup
TA_GPIO1	Interrupt to SOC	OUTPUT	External Pullup
TA_GPIO2	Used to Enable or Disable 1.2V Regulator	OUTPUT	External Pullup
TA_GPIO3	Used to Enable the BOOTMODE Buffer	OUTPUT	External Pullup
TA_GPIO4	Used Reset Bootmode IO Exp	OUTPUT	External Pullup

Test Automation 3.3V, 500mA Supply

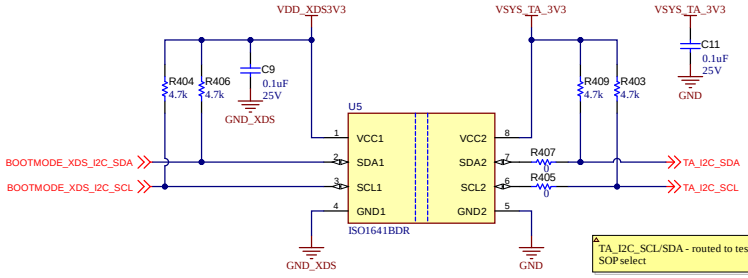


SLEEP: Pulled high to VSYS_TA_3V3 (Connected to VOUT)
FB: short to GND on fixed 3.3V TPS62177

This Pulls provides a defined logic state to the Test Automation signals before XDS110 firmware is loaded

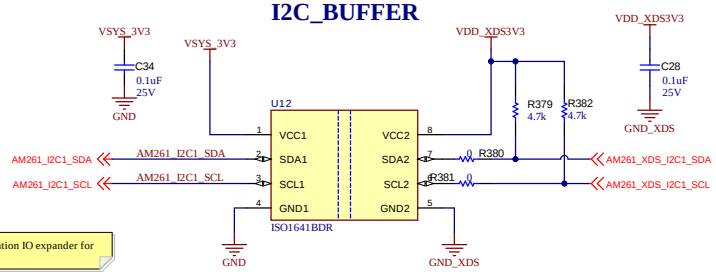


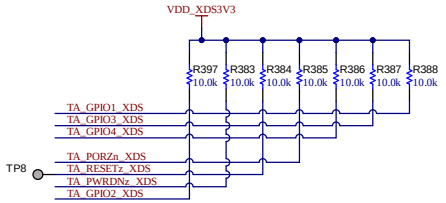
BOOTMODE_I2C_TA BUFFER



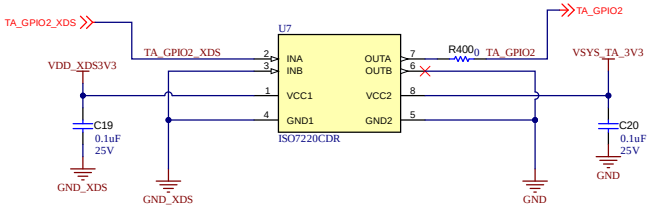
TA_I2C_SCL/SDA - routed to test automation IO expander for SOP select

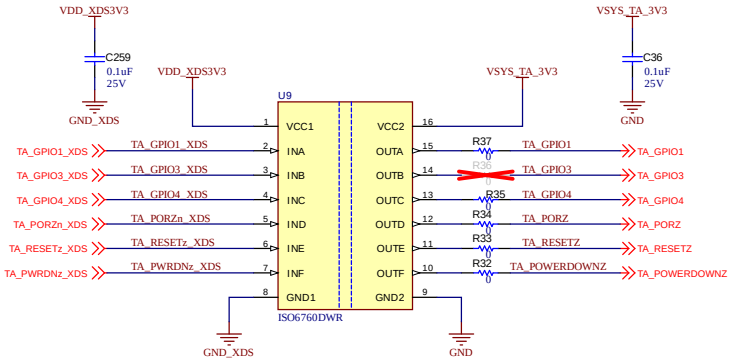
I2C_BUFFER



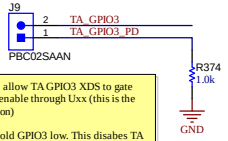


ISOLATION BUFFERS FOR TA SIGNALS





Test-Automation PORz Override



Remove jumper to allow TA GPIO3 XDS to gate SOP driver output enable through Uxx (this is the default configuration)

Install jumper to hold GPIO3 low. This disabes TA XDS SOP driver output enable.

Orderable: LP-AM261		Designed for:	Mod. Date: 6/2/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: Test Automation	
SVN Rev: 473		Assembly Variant: 001	Sheet: 17 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_17_Test_Automation.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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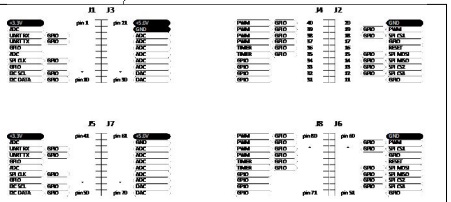
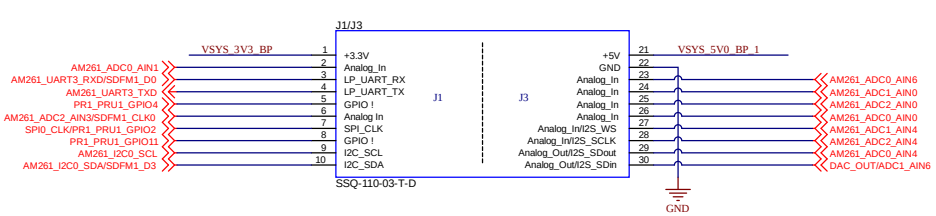
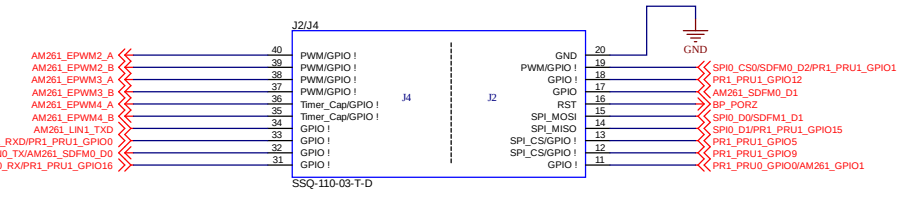
Boosterpack Headers

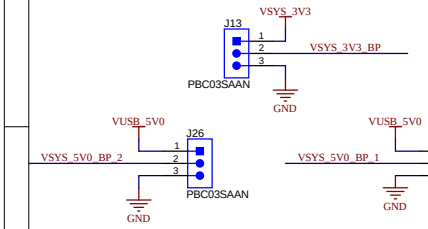


Boosterpack Site 1

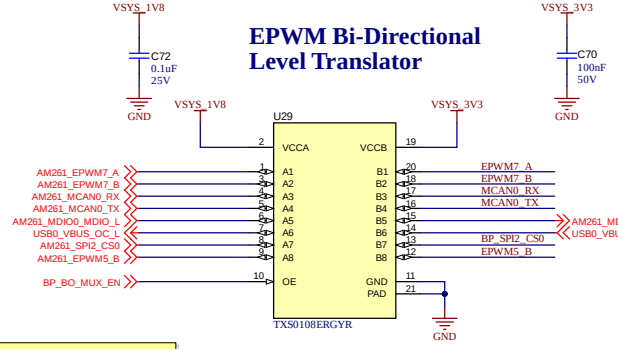




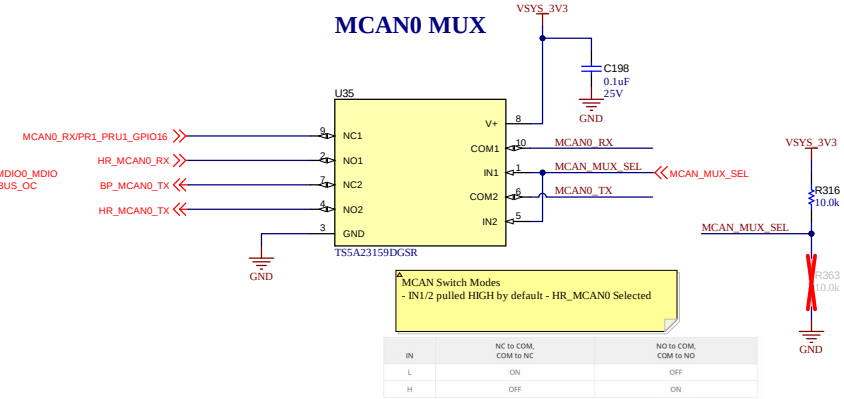
Boosterpack Power



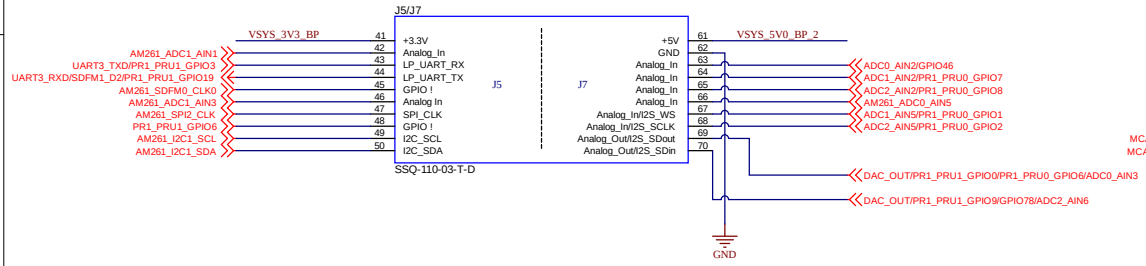
EPWM Bi-Directional Level Translator

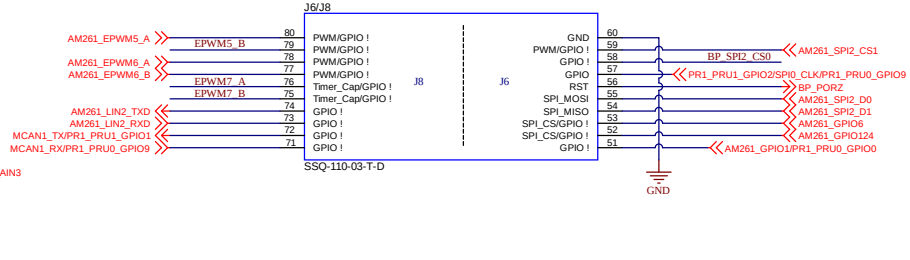


MCAN0 MUX



Boosterpack Site 2





Orderable: LP-AM261		Designed for:	Mod. Date: 6/24/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: Booster Pack Header	
SVN Rev: 522		Assembly Variant: 001	Sheet: 18 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_18_Boosterpack_Headers.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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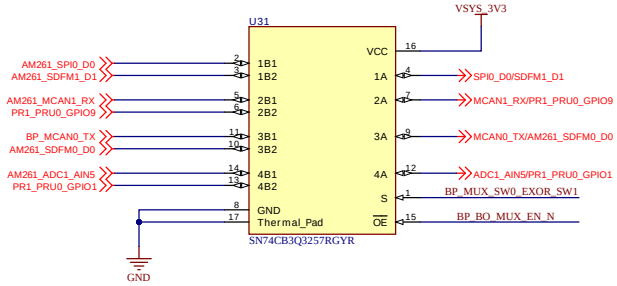
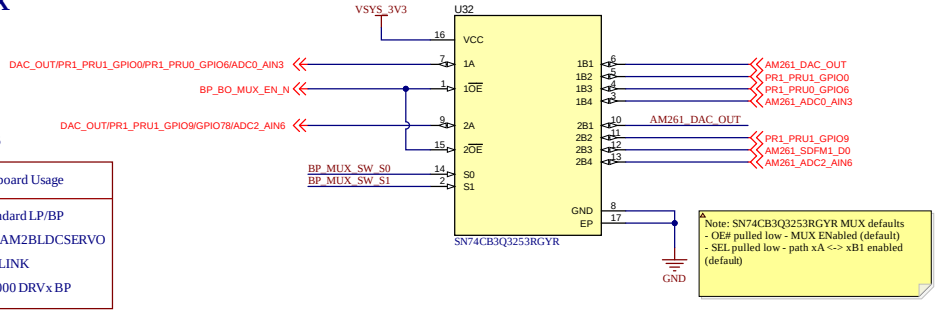
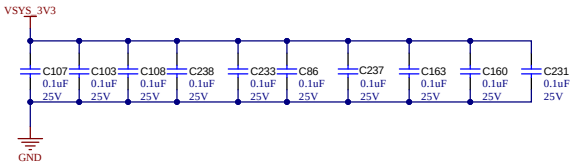
Alternate Boosterpack Function MUX

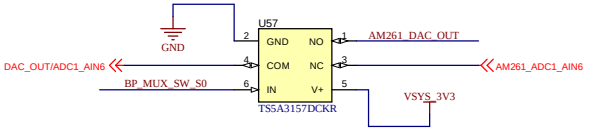
Boosterpack MUX modes

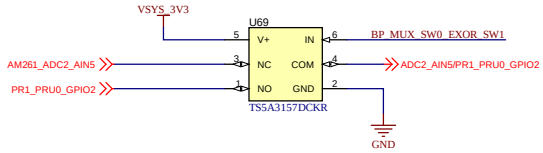
BP_MUX_SW_S1	BP_MUX_SW_S0	BP board Usage
LOW	LOW	Standard LP/BP
LOW	HIGH	BP-AM2BLDCSERVO
HIGH	LOW	IO-LINK
HIGH	HIGH	C2000 DRVx BP

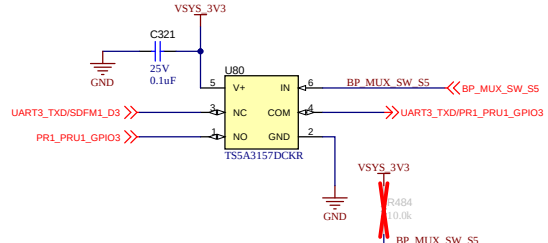




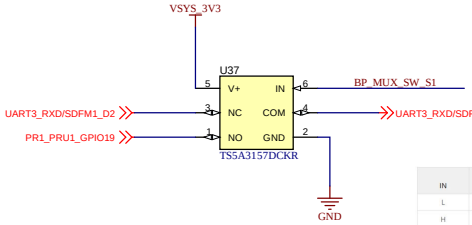




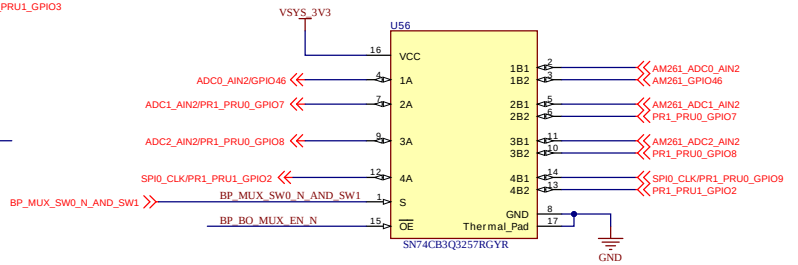




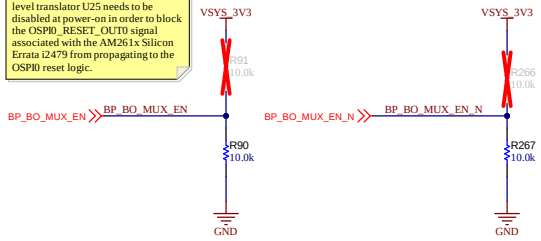
IN	NC to COM, COM to NC	NO to COM, COM to NO
L	ON	OFF
H	OFF	ON

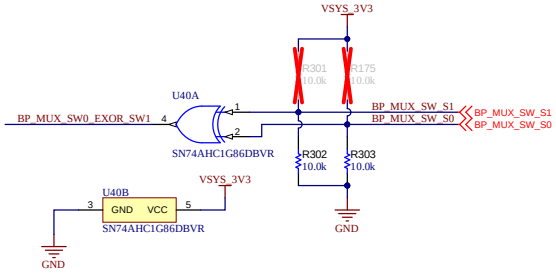


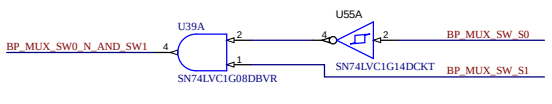
IN	NC to COM, COM to NC	NO to COM, COM to NO
L	ON	OFF
H	OFF	ON

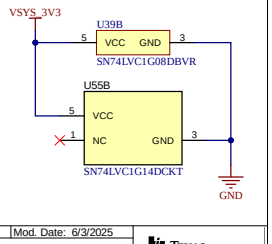


Note: R91 can be populated only if OSPI boot mode is not desired. The level translator U25 needs to be disabled at power-on in order to block the OSPI0_RESET_OUT0 signal associated with the AM261x Silicon Errata i2479 from propagating to the OSPI0 reset logic.









Orderable: LP-AM261	Designed for:	Mod. Date: 6/3/2025
TID #: N/A	Project Title: AM261 Launchpad	
Number: PROC193 Rev: A	Sheet Title: BP Muxes	
SVN Rev: 473	Assembly Variant: 001	Sheet: 19 of 26
Drawn By: Vijetha J, Kiran	File: PROC193A_19_BPMuxes.SchDoc	Size: B
Engineer: Vijetha J, Kiran	Contact:	

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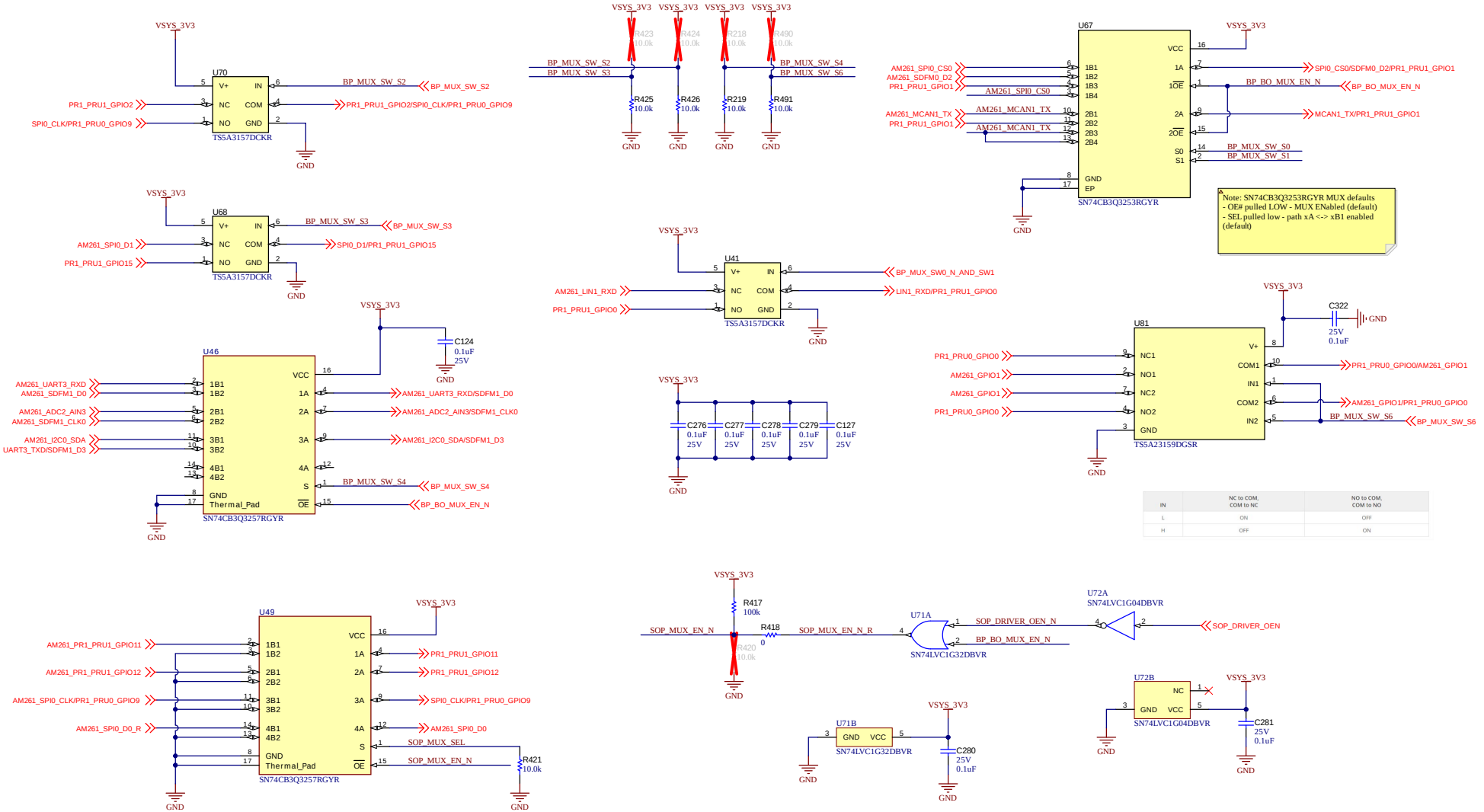
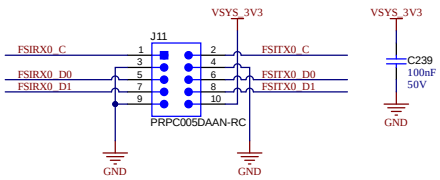
Alternate Boosterpack Function MUX



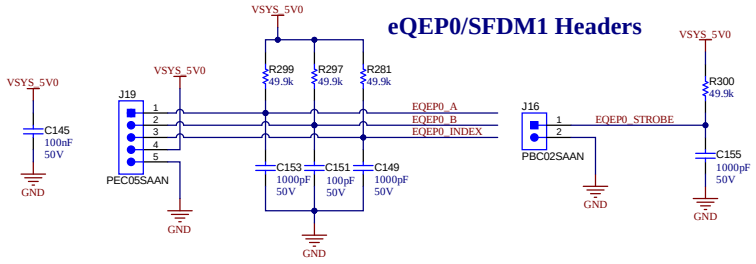
Orderable: LP-AM261		Designed for:	Mod. Date: 6/18/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: BP Muxes1	
SVN Rev: 509		Assembly Variant: 001	Sheet: 20 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_20_BPMuxes1.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

Breakout Headers

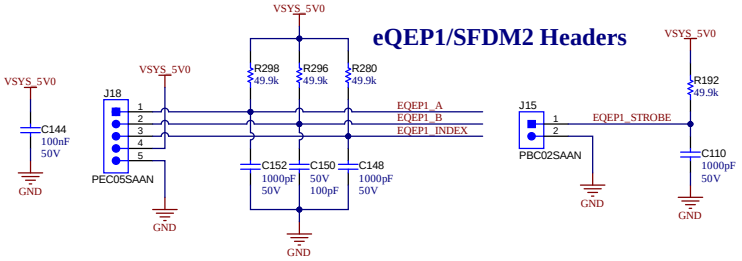
FSI Header



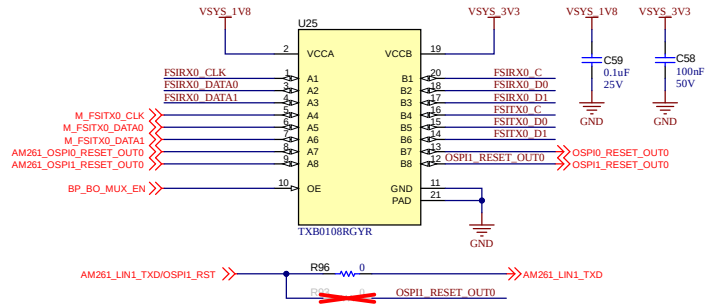
eQEP0/SFDM1 Headers



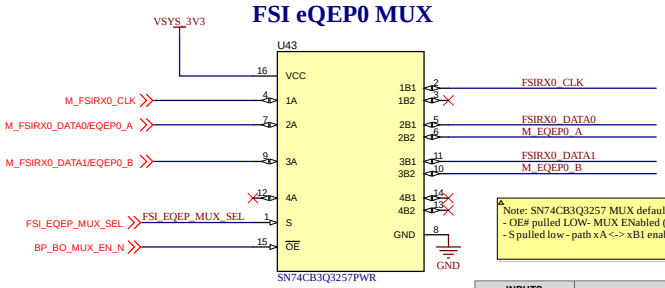
eQEP1/SFDM2 Headers



FSI Bi-Directional Level Translator



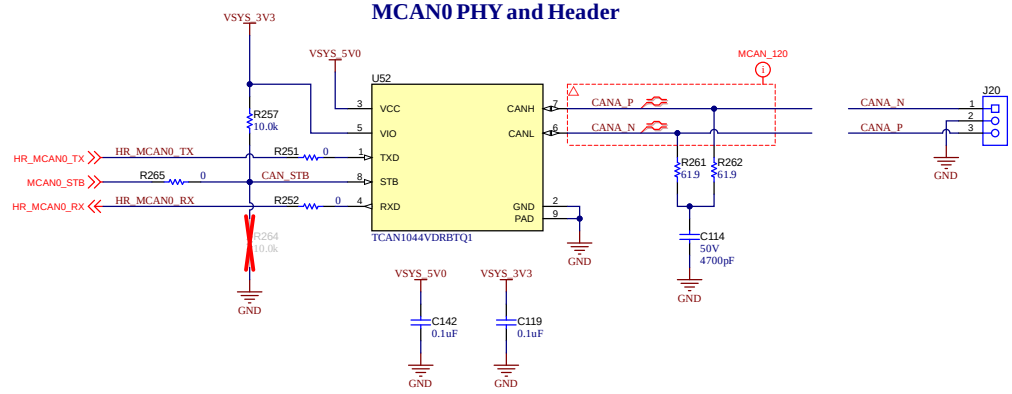
FSI eQEP0 MUX



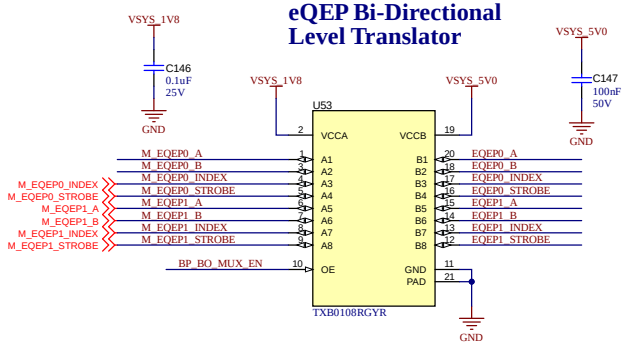
Note: SN74CB3Q3257 MUX defaults
- OE# pulled LOW- MUX ENabled (default)
- S pulled low - path xA <-> xB1 enabled (default)

INPUTS		INPUT/OUTPUT A	FUNCTION
OE	S		
L	L	B1	A port = B1 port
L	H	B2	A port = B2 port
H	X	Z	Disconnect

MCAN0 PHY and Header



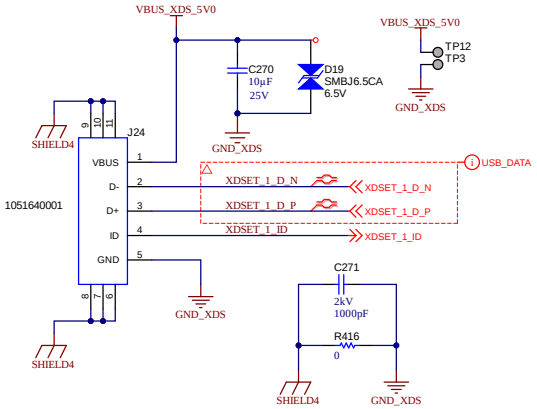
eQEP Bi-Directional Level Translator



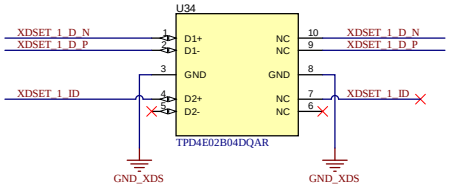
Orderable: LP-AM261		Designed for:	Mod. Date: 6/2/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: Breakout Headers	
SVN Rev: 473		Assembly Variant: 001	Sheet: 21 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_21_Breakout_Headers.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

XDS110 JTAG/USB-to-UART Bridge

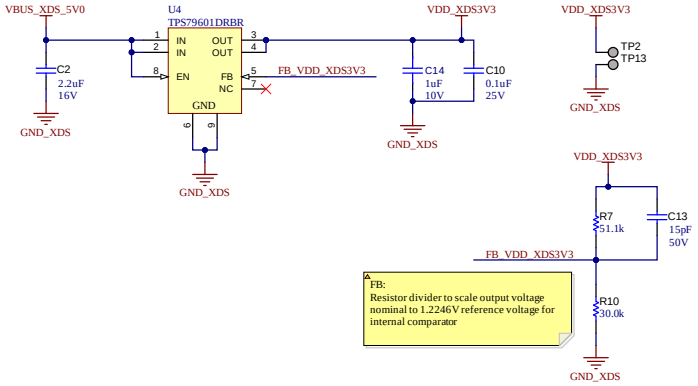
XDS110 USB Micro-B PORT



USB Mini-B ESD Protection



XDS110 3.3V LDO



Orderable: LP-AM261		Designed for:	Mod. Date: 6/2/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: XDS110 /USB2.0 Micro-B Port	
SVN Rev: 473		Assembly Variant: 001	Sheet: 22 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_22_USB_XDS110.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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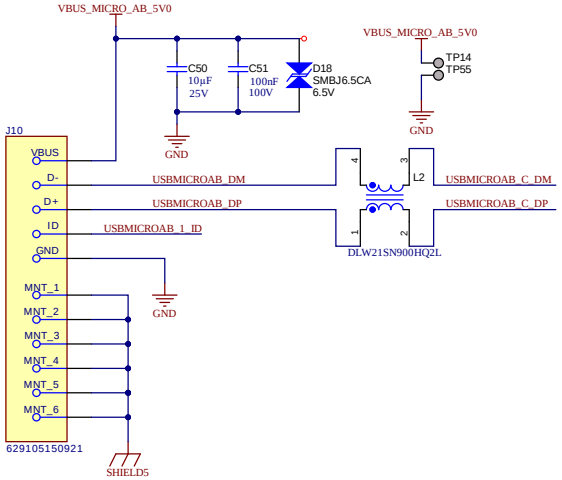
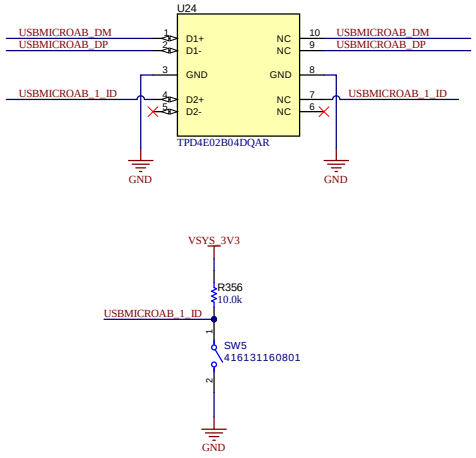
USB Micro-AB 2.0

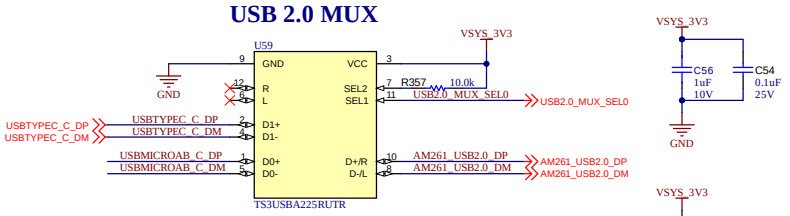
USB2.0 Micro_AB PORT



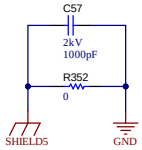
USB Micro-AB ESD Protection

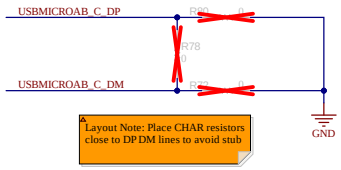


USB 2.0 MUX

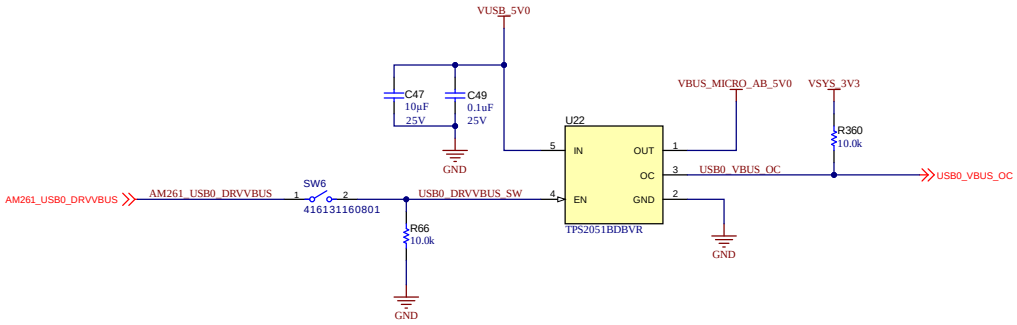


USB 2.0 characterization Res





USB micro AB Power-Distribution Switch



Orderable: LP-AM261		Designed for:	Mod. Date: 6/2/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: USB Micro-AB 2.0	
SVN Rev: 473		Assembly Variant: 001	Sheet: 23 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_23_USB_TYPE_MicroAB.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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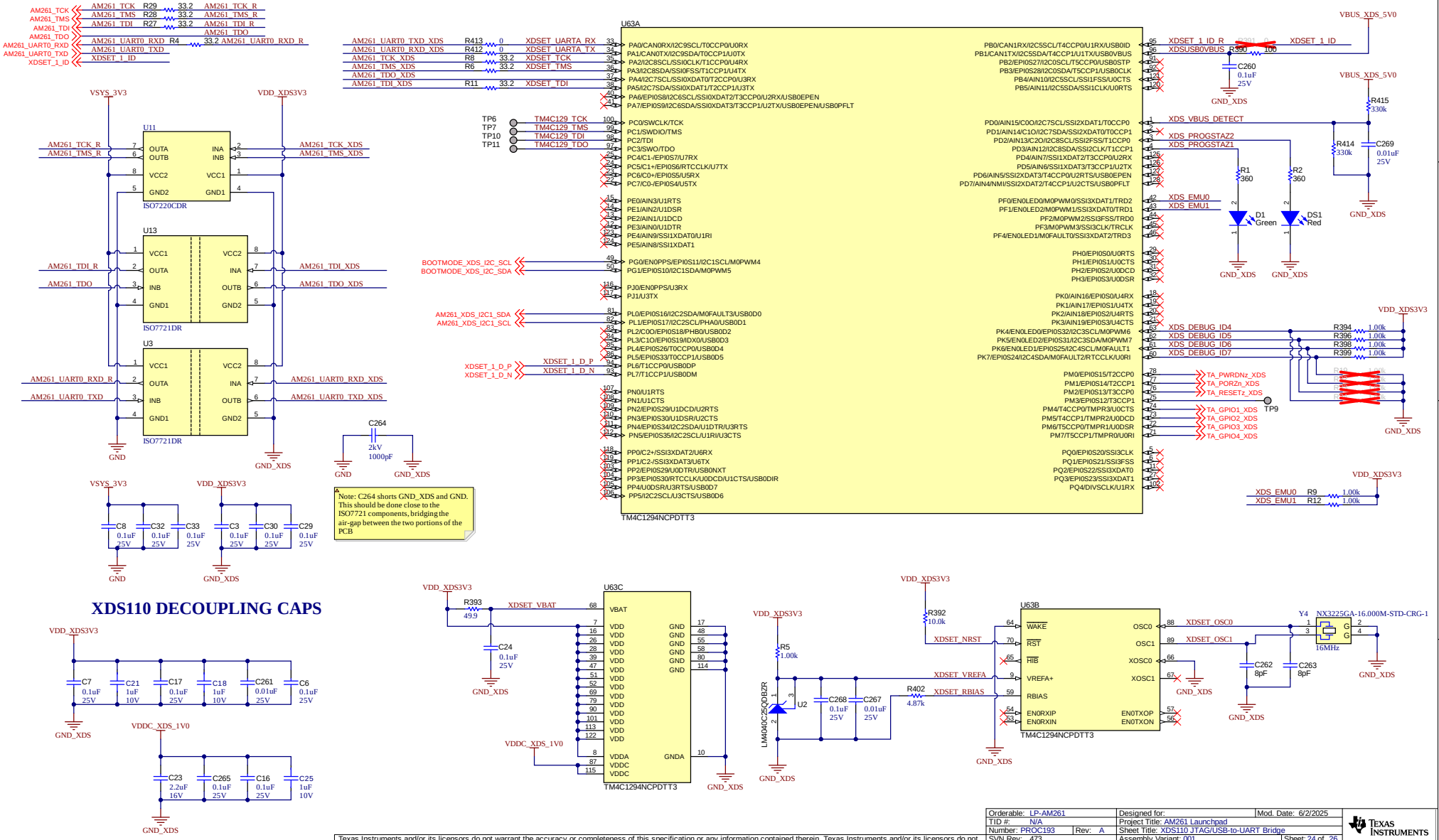
TM4C1294NCPDT Datasheet
XDS110 Collateral

XDS110 JTAG/USB-to-UART Bridge



XDS110 DECOUPLING CAPS

Orderable: LP-AM261		Designed for:	Mod. Date: 6/2/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: XDS110 JTAG/USB-to-UART Bridge	
SVN Rev: 473		Assembly Variant: 001	Sheet: 24 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_24_XDS110_2.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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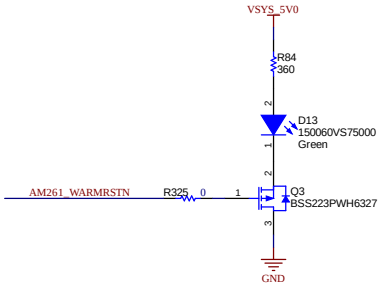
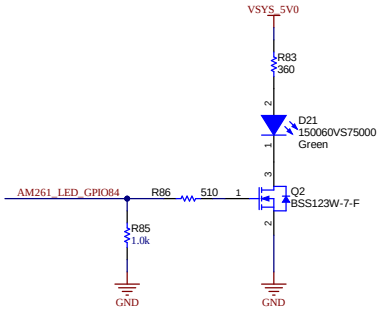
http://www.ti.com

System LED Indicators

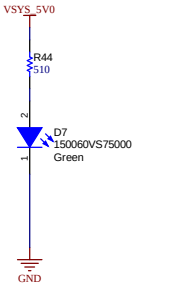
AM261_WARMRSTN



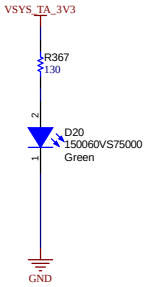
AM261 GPIO LED



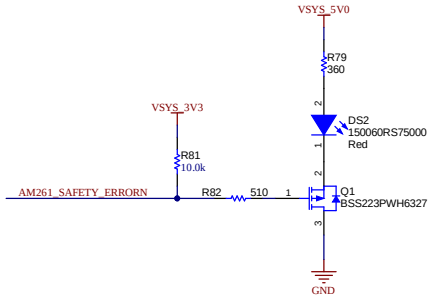
System 5.0V



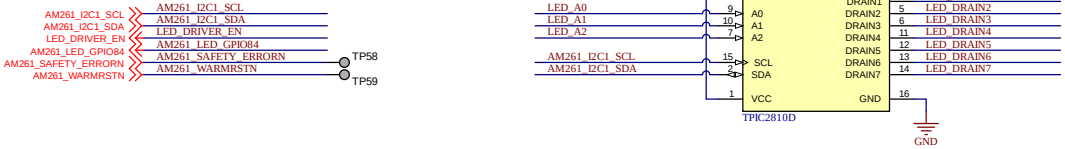
Test Automation 3.3V



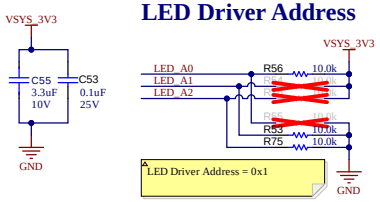
AM261x Safety Error



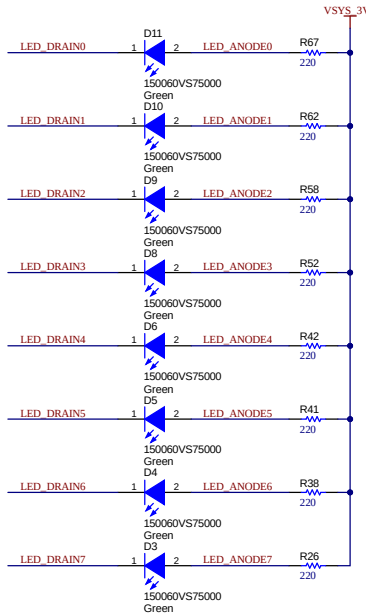
Industrial LED Driver



LED Driver Address



LED Driver Enable



Orderable: LP-AM261		Designed for:	Mod. Date: 6/2/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: System LED Indicators	
SVN Rev: 473		Assembly Variant: 001	Sheet: 25 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_25_LED.SchDoc	Size: B
Engineer: Vijetha J, Kiran		Contact:	

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System Hardware, Notes, Labels

PCB Fiducials

PCB Labels and Silkscreen

PCB Number: PROC193
PCB Rev: A

PCB
LOGO
Texas Instruments


CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

PCB Labels and Silkscreen

ZZ4
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ3
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ1
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: LP-AM261		Designed for:	Mod. Date: 7/3/2025
TID #: N/A		Project Title: AM261 Launchpad	
Number: PROC193	Rev: A	Sheet Title: System Hardware, Notes, Labels	
SVN Rev: 570		Assembly Variant: 001	Sheet: 26 of 26
Drawn By: Vijetha J, Kiran		File: PROC193A_26_Systemhardware_notes_labels.SchDoc	
Engineer: Vijetha J, Kiran		Contact:	

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