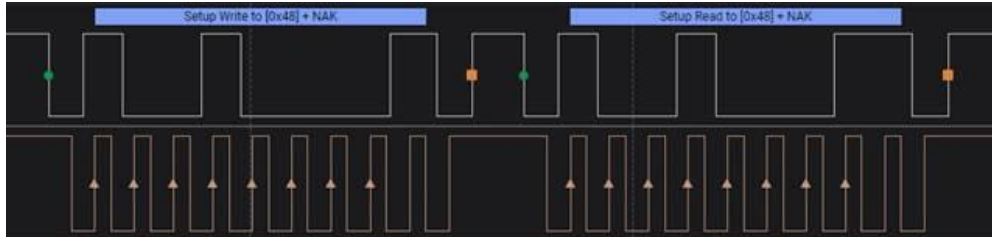
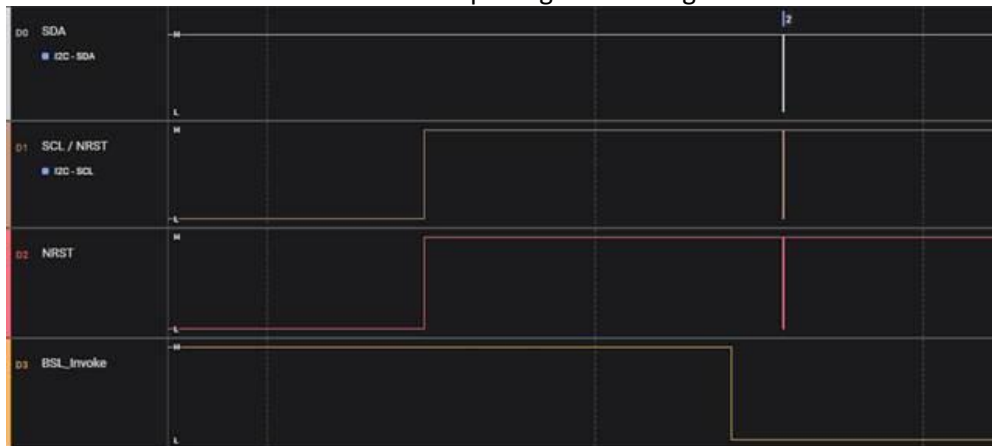


Recommended workaround for Low pin count MSPM0 that share NRST with BSL I2C_SCL with no secondary I2C mapping possible

The workaround refers to: **Add the plugin interface in Flash with NRST Pin disabled in BSL_PI_I2C_init().**

BEFORE

This workaround was tested on SOT16 package. Following is the blank device with bsl hardware invoke:



AFTER

Following is the device with BSL_PI_I2C_init() modified (added driverlib call to disable NRST):

```
115 uint16_t BSL_PI_I2C_init(uint8_t* buffer, uint16_t bufferSize)
116 {
117     BSL_initBuffers(buffer, bufferSize);
118
119     /* Reset and enable power to peripheral */
120     DL_I2C_reset(BSL_I2C);
121     DL_I2C_enablePower(BSL_I2C);
122
123     DL_I2C_selectClockSource(BSL_I2C, BSL_I2C_CLOCK_SOURCE);
124
125     DL_SYSCCTL_disableNRSTPin();
126
127     /* Initialize GPIOs */
```

