



This appears in the linker map at the address indicated
Why is a linker entry pointing to a single assembler instruction??? Application loaded and executed fine up until this started happening. Now, whenever I try to pause, I end up with this "feature".

Is this a hardware interface issue that just cropped up or result of XDC issue.

- 1 - Using SmartRF06 with jumpers as XDC100V3 debug adapter.
- 2 - Target is T4MC1233H6PM.
- 3 - Application is using TI-RTOS.
- 4 - The only hardware in use is two UARTS. The goal being to capture serial packets on one port, buffer within the Tiva, and forward to the second UART. THERE IS NOTHING MAGIC GOING ON HERE.. SIMPLE APP..