

## JTAG Enable/ Disable Option from HSM-RT/ TIFS

## JTAG Enable/ Disable Option from ROM

### Registers written for DBG\_FULL\_ENABLE

|       |    |                              |            |
|-------|----|------------------------------|------------|
| 1200h | 32 | HSM_SEC_MGR_DMSC_DEBUG_CNTL  | 4600 1200h |
| 1210h | 32 | HSM_SEC_MGR_SOC_SEC_DBG_CNTL | 4600 1210h |
| 4200h | 32 | HSM_SEC_MGR_DPM              | 4600 4200h |
| 1230h | 32 | HSM_SEC_MGR_CNTL             | 4600 1230h |
| 1234h | 32 | HSM_SEC_MGR_DBG_TAP_FW_EN    | 4600 1234h |
| 1238h | 32 | HSM_SEC_MGR_TEST_TAP_FW_EN   | 4600 1238h |
| 123Ch | 32 | HSM_SEC_MGR_PWR_FW_EN        | 4600 123Ch |
| 1240h | 32 | HSM_SEC_MGR_CMx_FW_EN        | 4600 1240h |
| 1244h | 32 | HSM_SEC_MGR_AUX              | 4600 1244h |
| 1250h | 32 | HSM_SEC_MGR_SOC_TRACE_CNTL   | 4600 1250h |

### Registers written for DBG\_PUBLIC\_ENABLE

|       |    |                              |            |
|-------|----|------------------------------|------------|
| 1200h | 32 | HSM_SEC_MGR_DMSC_DEBUG_CNTL  | 4600 1200h |
| 1210h | 32 | HSM_SEC_MGR_SOC_SEC_DBG_CNTL | 4600 1210h |
| 4200h | 32 | HSM_SEC_MGR_DPM              | 4600 4200h |
| 1230h | 32 | HSM_SEC_MGR_CNTL             | 4600 1230h |
| 1234h | 32 | HSM_SEC_MGR_DBG_TAP_FW_EN    | 4600 1234h |
| 1238h | 32 | HSM_SEC_MGR_TEST_TAP_FW_EN   | 4600 1238h |
| 123Ch | 32 | HSM_SEC_MGR_PWR_FW_EN        | 4600 123Ch |
| 1240h | 32 | HSM_SEC_MGR_CMx_FW_EN        | 4600 1240h |
| 1244h | 32 | HSM_SEC_MGR_AUX              | 4600 1244h |
| 1250h | 32 | HSM_SEC_MGR_SOC_TRACE_CNTL   | 4600 1250h |

### Registers written for DBG\_PERM\_DISABLE and DBG\_SOC\_DEFAULT

|       |    |                              |            |
|-------|----|------------------------------|------------|
| 1200h | 32 | HSM_SEC_MGR_DMSC_DEBUG_CNTL  | 4600 1200h |
| 1210h | 32 | HSM_SEC_MGR_SOC_SEC_DBG_CNTL | 4600 1210h |
| 4200h | 32 | HSM_SEC_MGR_DPM              | 4600 4200h |
| 1230h | 32 | HSM_SEC_MGR_CNTL             | 4600 1230h |
| 1234h | 32 | HSM_SEC_MGR_DBG_TAP_FW_EN    | 4600 1234h |
| 1238h | 32 | HSM_SEC_MGR_TEST_TAP_FW_EN   | 4600 1238h |
| 123Ch | 32 | HSM_SEC_MGR_PWR_FW_EN        | 4600 123Ch |
| 1240h | 32 | HSM_SEC_MGR_CMx_FW_EN        | 4600 1240h |
| 1244h | 32 | HSM_SEC_MGR_AUX              | 4600 1244h |
| 1250h | 32 | HSM_SEC_MGR_SOC_TRACE_CNTL   | 4600 1250h |

