

1

2

3

4

5

6

A

B

C

D


Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Place Block Diagram here (if appropriate) or delete this text box.  
If using a block diagram from another tool, save the picture as a .bmp file.  
Then, use menu Place|Drawing Tools|Graphic to insert the .png/.svg/.bmp file on the schematic.

Orderable: <a href="#">ChangeMe in variant</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 10/6/2020
TID #: <a href="#">N/A</a>	Project Title: <a href="#">MSPM0L1306xRHB</a>	
Number: <a href="#">MSPM0L1306xRHB</a>	Rev: <a href="#">E2</a>	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">1</a> of <a href="#">3</a>
Drawn By:	File: <a href="#">CoverSheet.SchDoc</a>	Size: B
Engineer: <a href="#">Eddie LaCost</a>	Contact: <a href="#">http://www.ti.com/support</a>	

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

 **TEXAS  
INSTRUMENTS**

<http://www.ti.com>

© Texas Instruments 2018

1

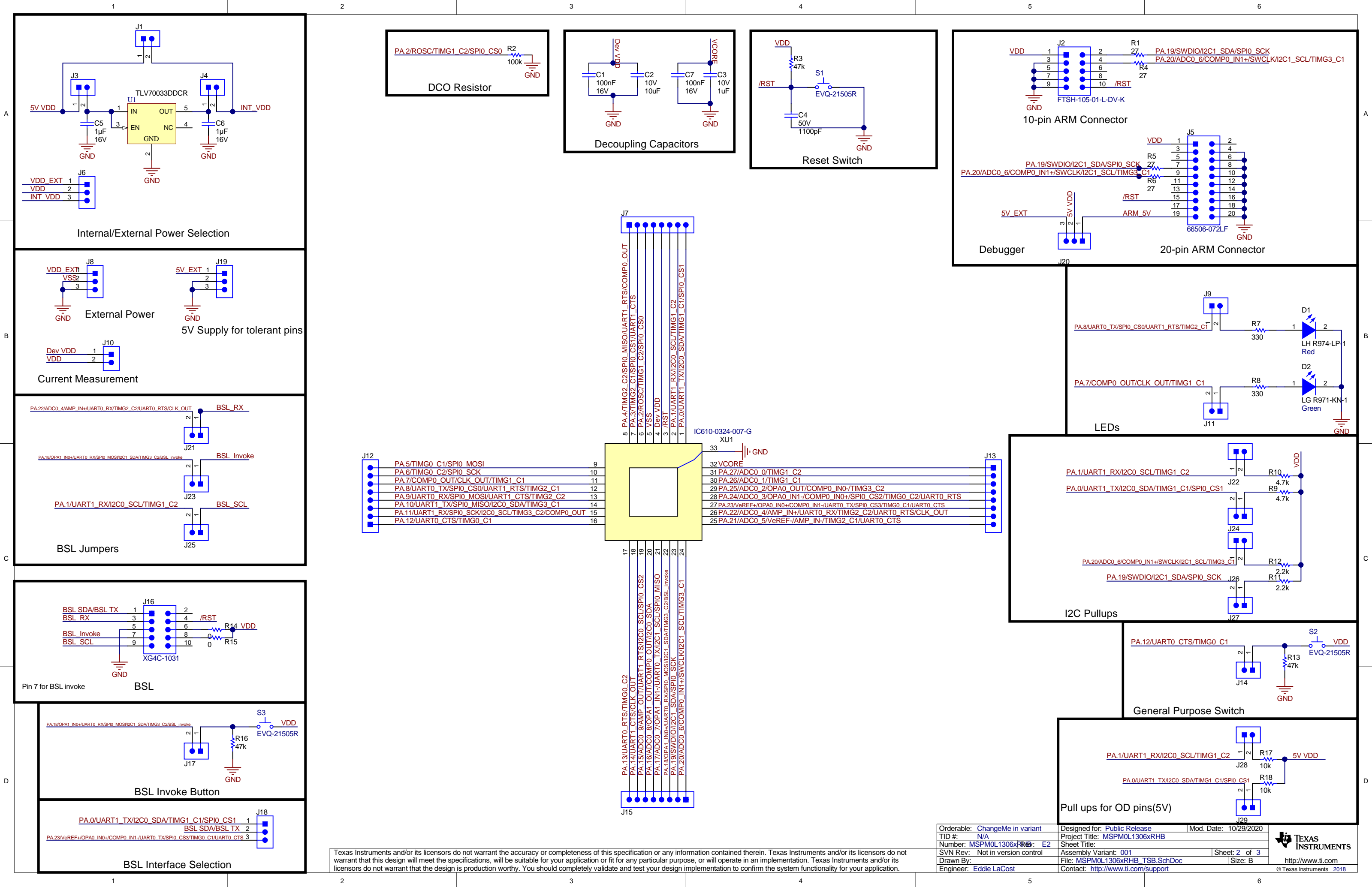
2

3

4

5


6



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.


Orderable: <a href="#">ChangeMe in variant</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 10/29/2020
TID #: <a href="#">N/A</a>	Project Title: <a href="#">MSPM0L1306xRHB</a>	
Number: <a href="#">MSPM0L1306xRHB</a>	Sheet Title: <a href="#">E2</a>	
SVN Rev: <a href="#">Not in version control</a>	Assembly Variant: <a href="#">001</a>	Sheet: 2 of 3
Drawn By: <a href="#">Eddie LaCost</a>	File: <a href="#">MSPM0L1306xRHB_TSB.SchDoc</a>	Size: B
Engineer: <a href="#">Eddie LaCost</a>	Contact: <a href="#">http://www.ti.com/support</a>	

H9




SJ-5303 (CLEAR)

H10




SJ-5303 (CLEAR)

H11



SJ-5303 (CLEAR)

H12



SJ-5303 (CLEAR)

DNP

FID1

DNP

FID2

DNP

FID3

PCB Number: MSPM0L1306xRHB

PCB Rev: E2

PCB

LOGO

Texas Instruments

PCB


LOGO

FCC disclaimer

PCB

LOGO

WEEE logo

 You should delete the nylon screws/standoffs and/or the bumpons as needed for your design (or substitute other parts from Hardware.IntLib). Bumpons are cheaper, but provide less clearance.

Deleting anything else from this page may result in your EVM submission being rejected (until you add them back).

Update the Label Text in the Label Table as needed for each Assembly Variant.

You should delete this note too.

LBL1

PCB Label

THT-14-423-10

Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: <a href="#">ChangeMe in variant</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 10/16/2020
TID #: <a href="#">N/A</a>	Project Title: <a href="#">MSPM0L1306xRHB</a>	
Number: <a href="#">MSPM0L1306xRHB</a>	PCB Rev: <a href="#">E2</a>	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">3</a> of <a href="#">3</a>
Drawn By:	File: <a href="#">Hardware.SchDoc</a>	Size: B
Engineer: <a href="#">Eddie LaCost</a>	Contact: <a href="#">http://www.ti.com/support</a>	