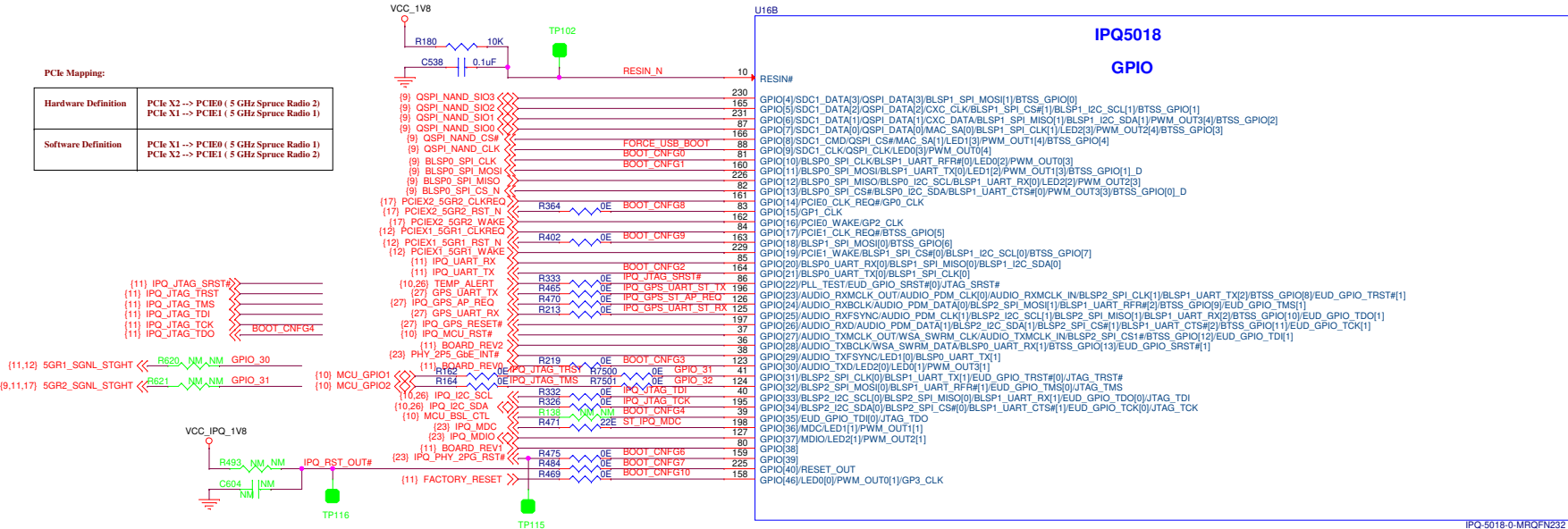


IPQ5018_BOOT_CONFIG

PCIe Mapping:

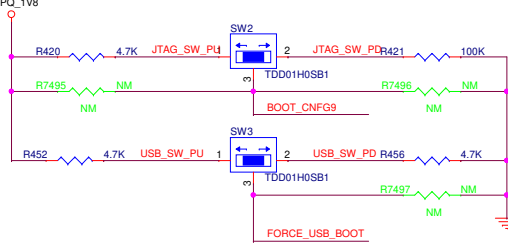
Hardware Definition	PCIe X2 -> PCIe0 (5 GHz Spruce Radio 1)
	PCIe X1 -> PCIe1 (5 GHz Spruce Radio 1)
Software Definition	PCIe X1 -> PCIe0 (5 GHz Spruce Radio 1)
	PCIe X2 -> PCIe1 (5 GHz Spruce Radio 2)



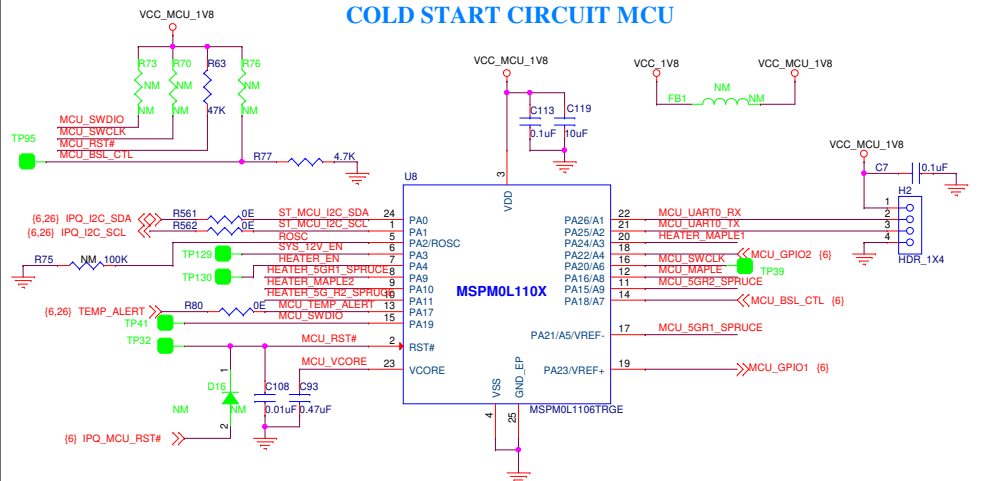
Boot Configuration Table

GPIO_10	BOOT_CFG0	Boot_Config[0]: Auth Enable 0: No auth. (Default) 1: auth is required
GPIO_11 GPIO_21	BOOT_CFG1 BOOT_CFG2	Boot_Config[2:1]: Boot up interface select. 00: SPI ROM 01: eMMC 10: QSPI (4bit) (Default) 11: USB2.0
GPIO_30	BOOT_CFG3	Boot_Config[3]: Watchdog Enable 0: watchdog enable (Default) 1: watchdog disable
GPIO_35	BOOT_CFG4	Boot_Config[4]: Hash in fuse (SW use only) 0: PK hash is stored in boot ROM (Default) 1: PK hash is stored in OTP
GPIO_39	BOOT_CFG6	Boot_Config[6]: Boot ROM boot speed 0: 240MHz (Default) 1: 400MHz
GPIO_40	BOOT_CFG7	Boot_Config[7]: tcxo_mode 0: xo-mode 1: tcxo-mode (Default)
GPIO_15	BOOT_CFG8	Boot_Config[8]: Use Serial Num 0: Use Serial Num (Default) 1: Use OEM ID
GPIO_18	BOOT_CFG9	Boot_Config[9]: jtag_boot_en 0: Used as Normal function GPIO 1: Used as JTAG (Default)
GPIO_46	BOOT_CFG10	Boot_Config[10]: RFA refclk frequency selection 0: 48MHz 1: 96MHz
GPIO_9	BOOT_forced_usb_boot	forced_usb_boot 0: Not force boot from USB 1: force boot from USB

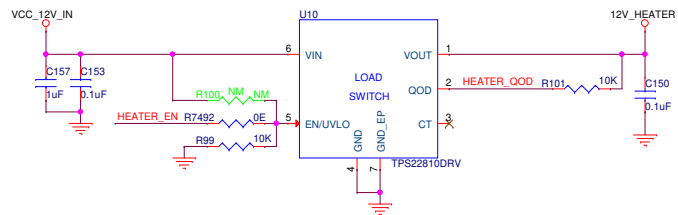
DIP Switch for JTAG and USB Force Boot



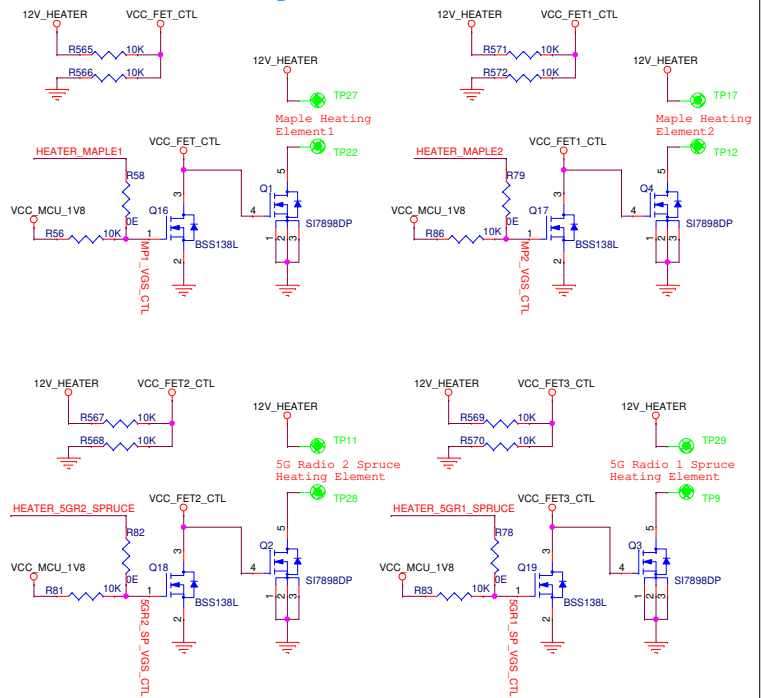
COLD START CIRCUIT MCU



Heater Circuit Load Switch

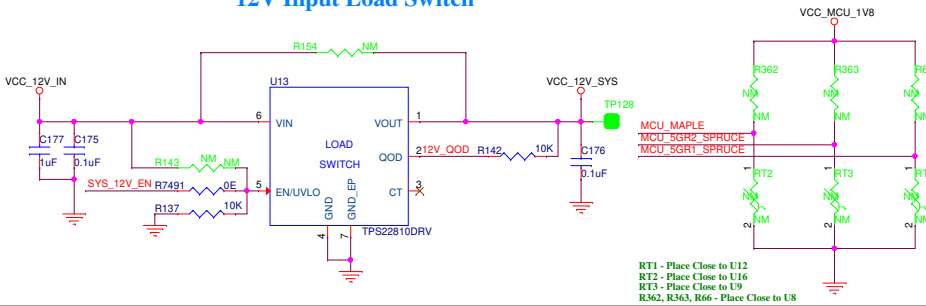


Heating Element Control Circuit

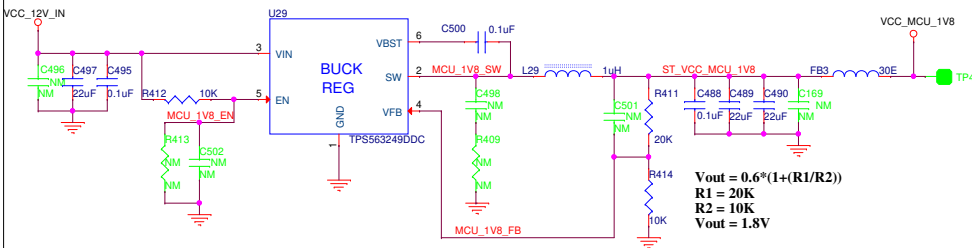


Polyamide heating elements will be soldered in the through hole test points

Thermistor Circuit



MCU_1V8 GENERATION, 90mA



Vout = 0.6*(1+(R1/R2))
R1 = 20K
R2 = 10K
Vout = 1.8V

TEMPERATURE SENSOR

