

1-TM4C123GH6PM Microcontroller\_spm3376e.pdf - Adobe Reader

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Note that each pin must be programmed individually; no type of programming is implied by the columns corresponding GPIO

GPIO pins are configured by the columns corresponding to the GPIOPDR=0. In addition, a pin must be programmed to a specific alternate function set. In addition, a pin must be programmed to a specific alternate function set.

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IO	Pin	Analog Function	1	2	3	4	5	6	7	8	9	14	15
PA0	17	USBRx	-	-	-	-	-	-	-	CAN1Rx	-	-	-
PA1	18	USBTx	-	-	-	-	-	-	-	CAN1Tx	-	-	-
PA2	19	REFCLK	-	-	-	-	-	-	-	-	-	-	-

programming of critical hardware signals including the GPIO pins that can function as JTAG/SWD signals and the NMI signal. The commit control process must be followed for these pins, even if they are programmed as alternate functions other than JTAG/SWD or NMI; see "Commit Control" on page 656.

**Table 10-2. GPIO Pins and Alternate Functions (64LQFP)** To use a pin as GPIO chose any number not used

IO Pin Analog Function Digital Function (GPIOCTL PMCx Bit Field Encoding)<sup>a</sup>

PA0 17 USBx - - - - - - - - - - CAN1Rx - - -

PA1 18 USBx - - - - - - - - - - CAN1Tx - - -

PA2 19 REFCLK - - - - - - - - - - - - - - - -

1.1. Tiva™ C Series Overview

1.2. TM4C123GH6PM Microcontroller Overview

1.3.1. ARM Cortex-M4F Processor Core

4:47 PM 6/10/2020