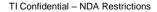
- Hardware
 - Debug connection
 - Pin 1 --- SWDIO
 - Pin 5 --- VCC
 - Pin 8 --- GND
 - Pin11 --- SWCLK

Figure 2. Debug Connection (CTI-20) Pin Mapping





- Hardware
 - Debug connection
 - Pin 1 --- SWDIO
 - Pin 5 --- VCC
 - Pin 8 --- GND
 - Pin11 --- SWCLK

4.1.2 Debug Adaptor Pin Mapping

Table 4 defines the signal mapping used for each adaptor type.

Table 4. CTI to Other Adaptor Pin Mapping

Signal	XDS110 Pin	CTI20 Pin	CM20 Pin ⁽¹⁾	CM10 Pin ⁽¹⁾	TI14 Pin
SWDIO/TMS	1	1	2	2	1
TRSTn	2	2			2
TDI	3	3	8	8	3
TDIS	4	4			4
VTREF	5	5	1	1	5
KEY	6	6	7	7	6
TDO/SWO	7	7	6	6	7
GND	8	8	3,5,9,11,15,17,19	3,5,9	8
RTCK		9			9
GND	10	10			10
SWCLK/TCK	11	11	4	4	11
GND	12	12			12
EMU0/TRIGOUT0	13	13			13
EMU1/TRIGOUT1	14	14			14
nRESET	15	15	10	10	
GND	16	16			
EMU2/TRIGIN0	17	17			
EMU3/TRIGIN1	18	18			
EMU4		19			
NC	20	20			

⁽¹⁾ Any signal not listed in this table is not connected through the adaptor.



Hardware

- Debug connection
 - Pin 1 --- SWDIO
 - Pin 5 --- VCC
 - Pin 8 --- GND
 - Pin11 --- SWCLK





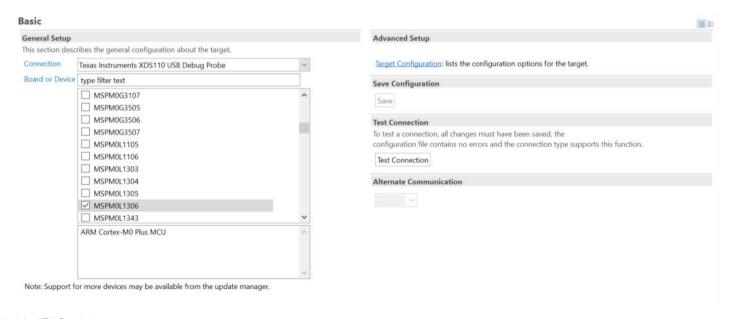
NOTE

- XDS110 cannot provide power supply
- External power supply is needed

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Software

No need to modify the target configs of example code from SDK



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