FPGA Software Release Notes

Production 1.2: Xilinx ver 2016.1 & FPGA ARM S/W ver 1.0

NOTE: Changes from Previous Release shown in Blue. Important comments are in red.

IMPORTANT NOTE: THIS SOFTWARE REQUIRES THE USAGE OF FOLLOWING FPGA XC7Z020-1CLG484I4493

General Information

These are release notes for FPGA Software.

The software released from TI will be the FPGA software which includes the FPGA ARM binary, the FPGA bootloader and the FPGA bit stream. The FPGA software should be programmed onto the FPGA flash.

Features Supported in this release (new ones shown in Blue)

- ✓ **Compressed FPGA Size**
- ✓ Keystone for motion processing
- ✓ Keystone correction support for XPR sources
- ✓ FPGA software boot up support
- ✓ 1D-Keystone support
- ✓ FPGA software version information
- ✓ FPGA software status and error reporting

Bugs Fixed in this release

- ✓ N/A

Unsupported Features

- QuadSPI

Known Issues and Limitations

- Updated keystone valid input ranges:

- Throw Ratio	- 0.2 to 0.4	- 0.4 to 0.6	- 0.6 to 2.0
- Pitch	- -30° to +15°	- -40° to +25°	- -40° to +35°
- Vertical Offset	- 0% to 150%	- 0% to 150%	- 0% to 150%

For questions or trouble getting EVM to work properly, please contact your regional
Texas Instruments DLP Support Engineer.

- PROJ_ON non-reliable when power-up with TPG. This issue has been seen mostly when system power-up with TPG. So far it hasn't been seen with Splash or External TPG. If issue occurs it can be recovered with another PROJ_ON cycle.
- Keystone has been tested on HW only for non-XPR sources. XPR sources are not tested on HW
- FPGA_SW interrupts are not hooked up properly.

EVM Limitations

- None.

Notes

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