NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "DMD_P3P3V" represents connection to the +3.3V digital power plane.
2. The netname "DMD_P2P5V" represents connection to the +2.5V digital power plane.
3. The netname "DMD_P1P8V" represents connection to the +1.8V digital power plane.
4. The symbol ⏚ represents connection to the digital ground plane.
5. A "Z" suffix on a signal name indicates an active low signal.
6. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.
7. All resistor values are in ohms, 1/16W and 5% unless otherwise specified.

Z_PCB1

PCB, Automotive DLP 0.3 S450 DMD
DLP021
2514104

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COMPUTER GENERATED DRAWING. DO NOT REVISE MANUALLY

REVISIONS

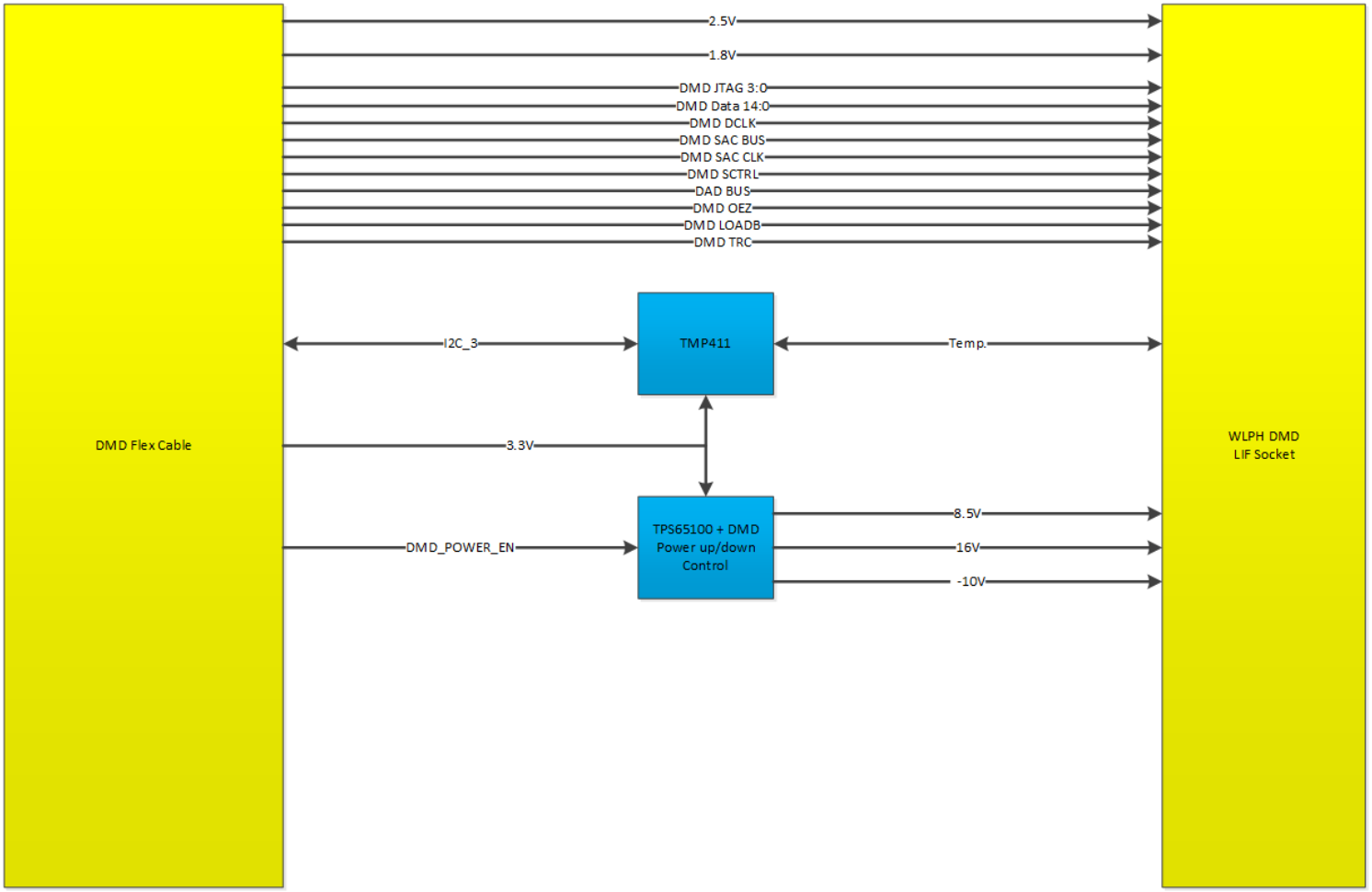
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	10/29/14	10/29/14
B	Changed heater components to DNI Added PCB.	3/22/18	3/22/18
C	Removed heater components.	7/18/18	7/18/18

DLP®

TEXAS INSTRUMENTS

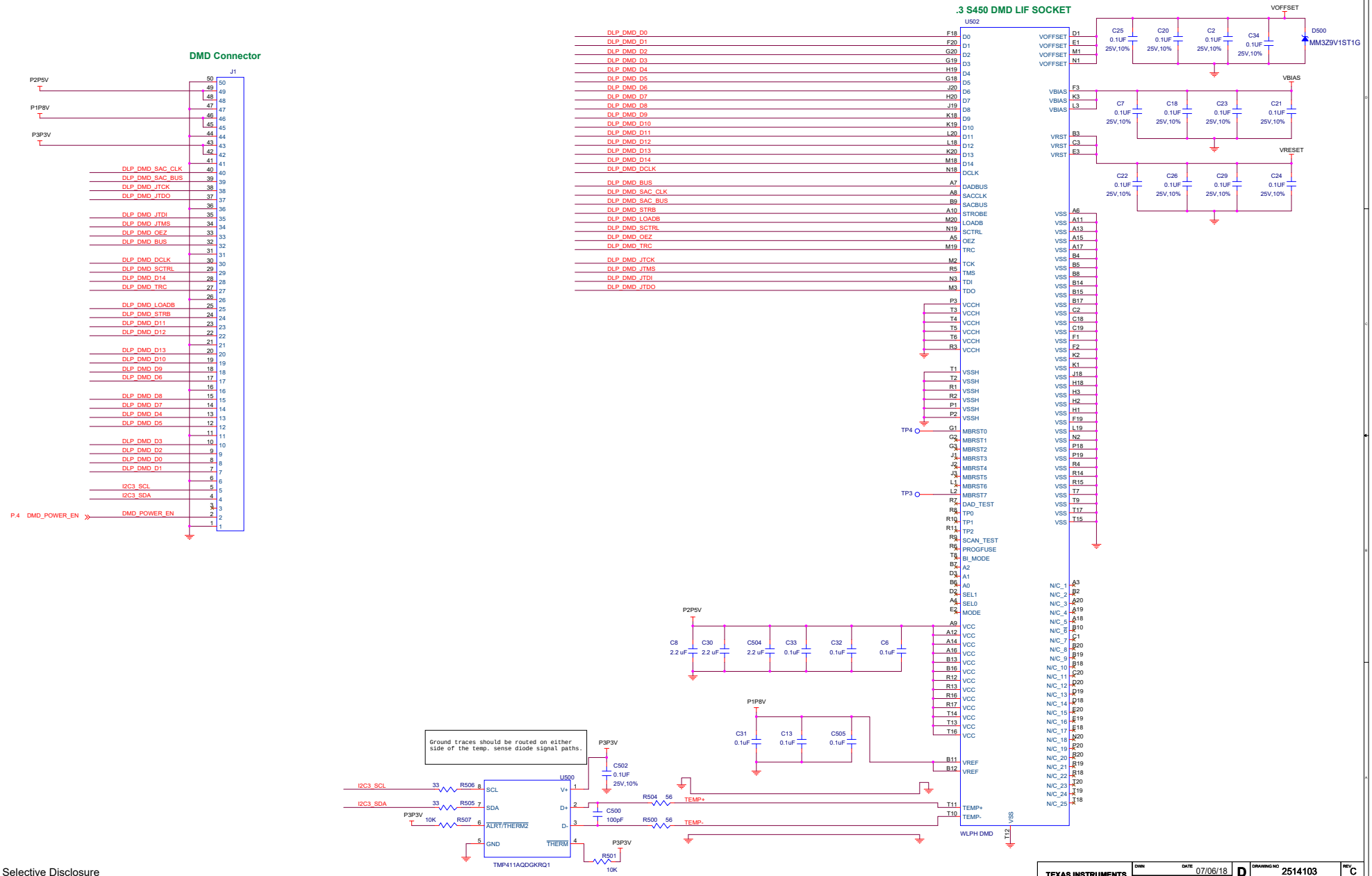
TI Information - Selective Disclosure

		DWN John Cooper DATE 10/29/14	TEXAS INSTRUMENTS
		ENGR John Cooper	(C) COPYRIGHT 2014 TEXAS INSTRUMENTS ALL RIGHTS RESERVED
		SYST Jeff Farris	
		PRJ Jason Thompson	TITLE: Schematic, Automotive HUD .3 S450 DMD Board DLP021 DLP3030Q1EVM
NEXT ASSY	USED ON		D DRAWING NO 2514103 REV C
APPLICATION		SW Cadence Capture 16.6	SCALE SHEET 1 of 4

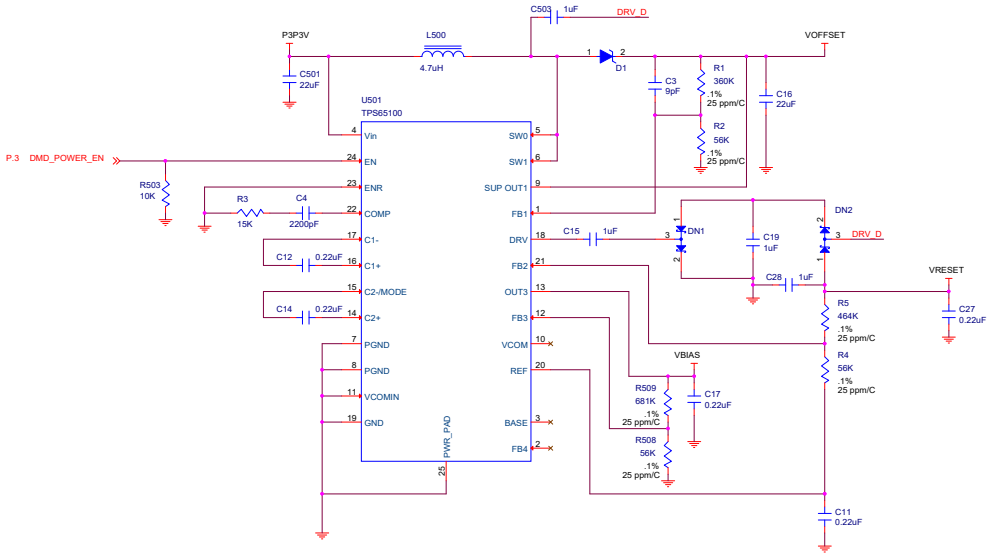


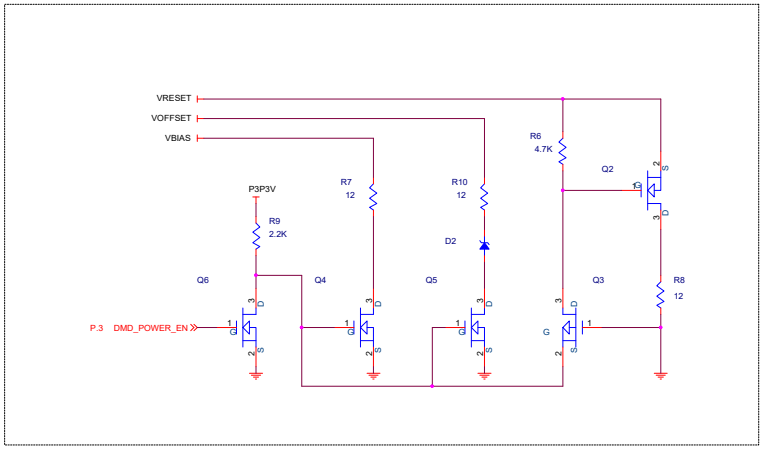
DMD BLOCK DIAGRAM

TEXAS INSTRUMENTS	DWN	DATE 07/06/18	D	DRAWING NO 2514103	REV C
	ISSUE DATE 07/06/18			SCALE	SHEET 2 OF 4



TEXAS INSTRUMENTS	DWN	DATE 07/06/18	D	DRAWING NO 2514103	REV C
	ISSUE DATE 07/06/18			SCALE	SHEET 3 OF 4





This circuit is used to keep the power up and power down sequencing on the DMD reset voltages in spec.

TEXAS INSTRUMENTS	DWN	DATE 07/06/18	D	DRAWING NO 2514103	REV C
	ISSUE DATE 07/06/18			SCALE	SHEET 4 OF 4