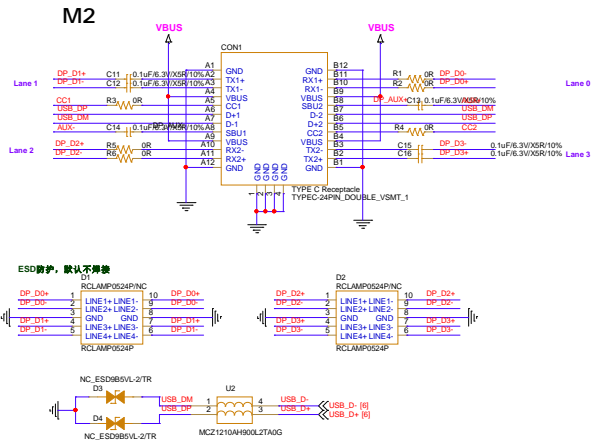
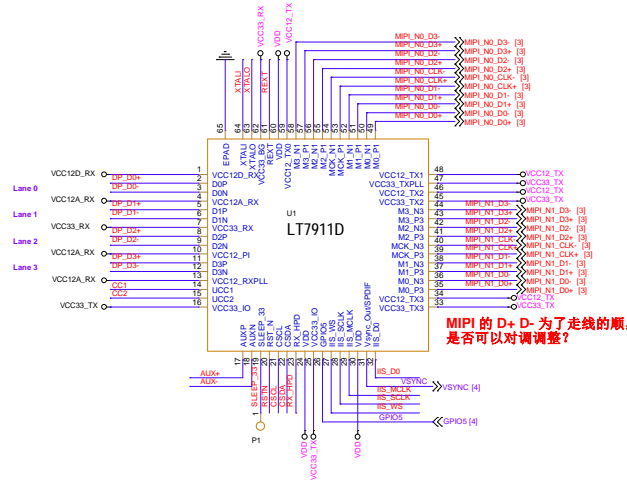


# TYPE C INPUT

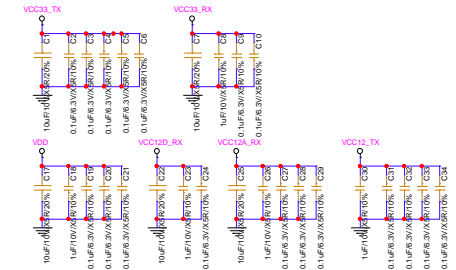
NOTE:LT7911D supports DP lane swap. Both M1 and M2 are available. Usually Plug use M1, Receptacle use M2, layout will be more convenient.



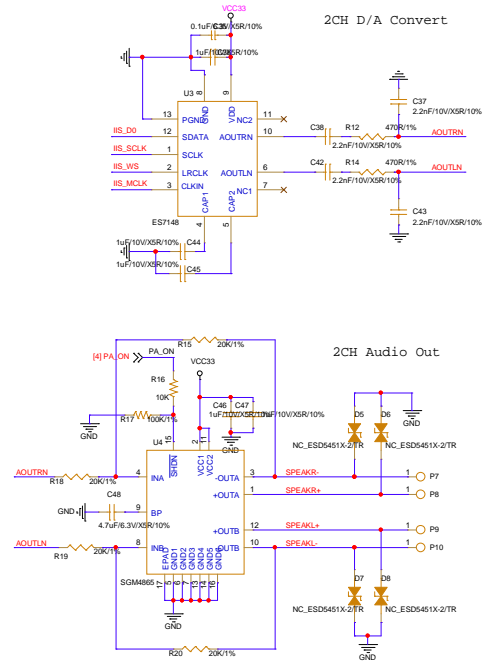
# LT7911D



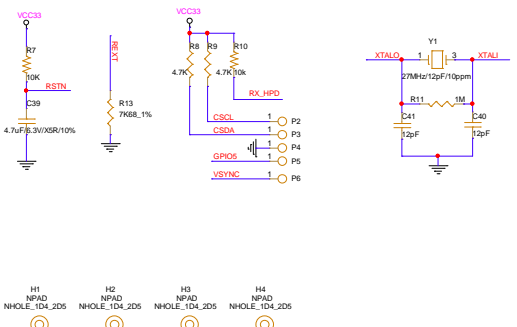
Decouple capacitance: Close IC power pin



# AUDIO



# EXTERNAL



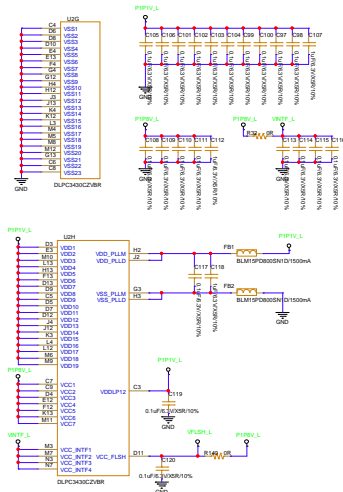
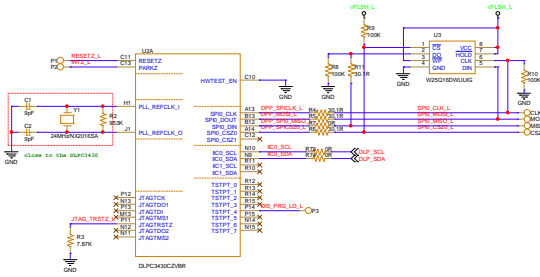
Glass Left

DLPC3430 Power

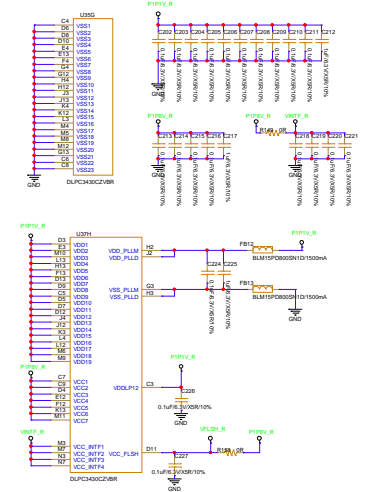
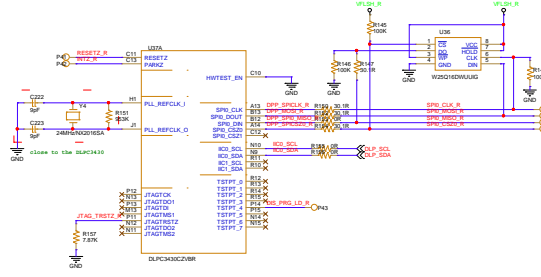
Glass Right

DLPC3430 Power

DLPC3430 Clock, Reset, SPI and Flash Memory



DLPC3430 Clock, Reset, SPI and Flash Memory

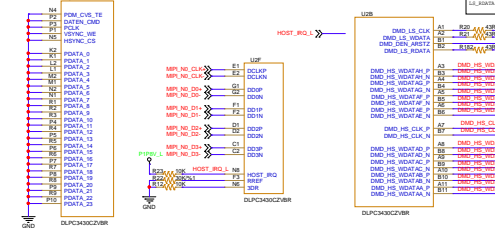


RGB Input

DSI Input

DSI output

Signal-Bonded Impedance: 48 ± 10% Ohm  
The signal trace length is 100 ± 10%mm.  
The signal trace length is 100 ± 10%mm.  
The signal trace length is 100 ± 10%mm.

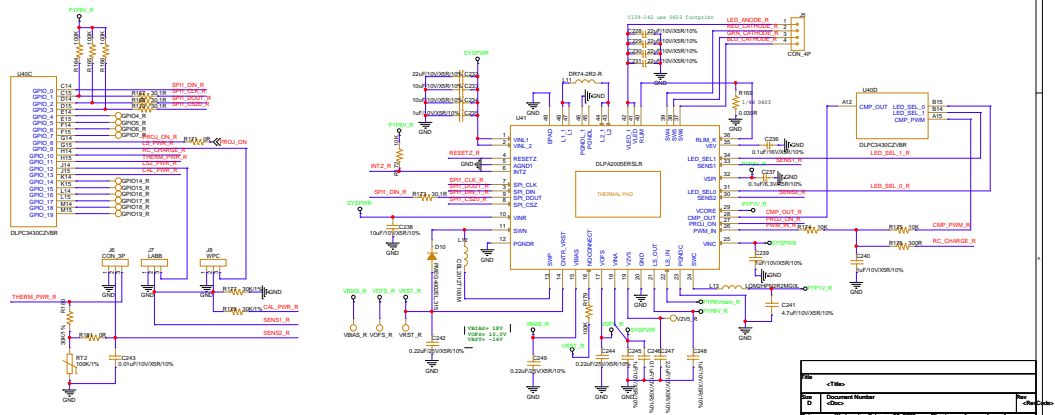
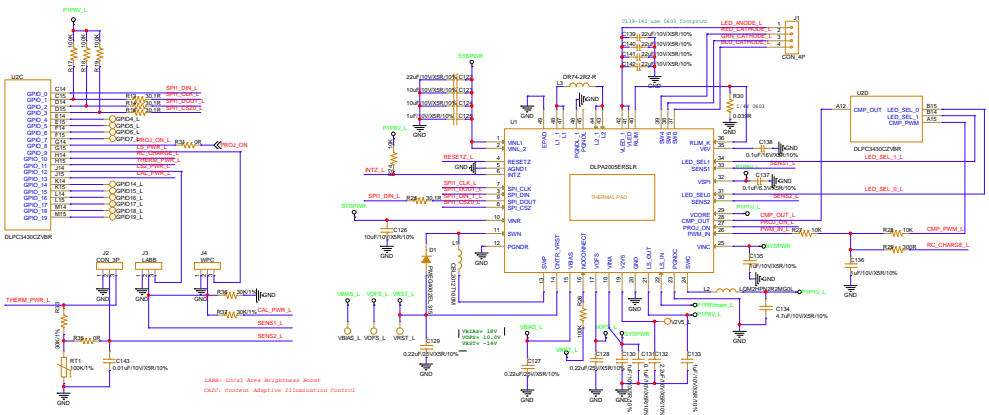
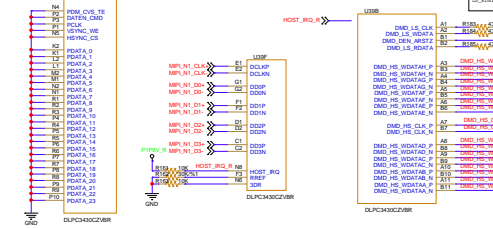


RGB Input

DSI Input

DSI output

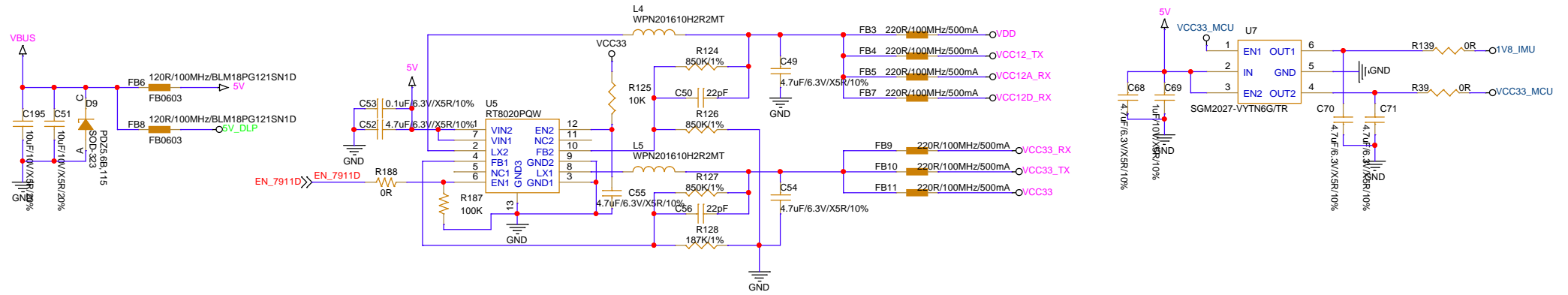
Signal-Bonded Impedance: 48 ± 10% Ohm  
The signal trace length is 100 ± 10%mm.  
The signal trace length is 100 ± 10%mm.  
The signal trace length is 100 ± 10%mm.



# SYSTEM 5V POWER

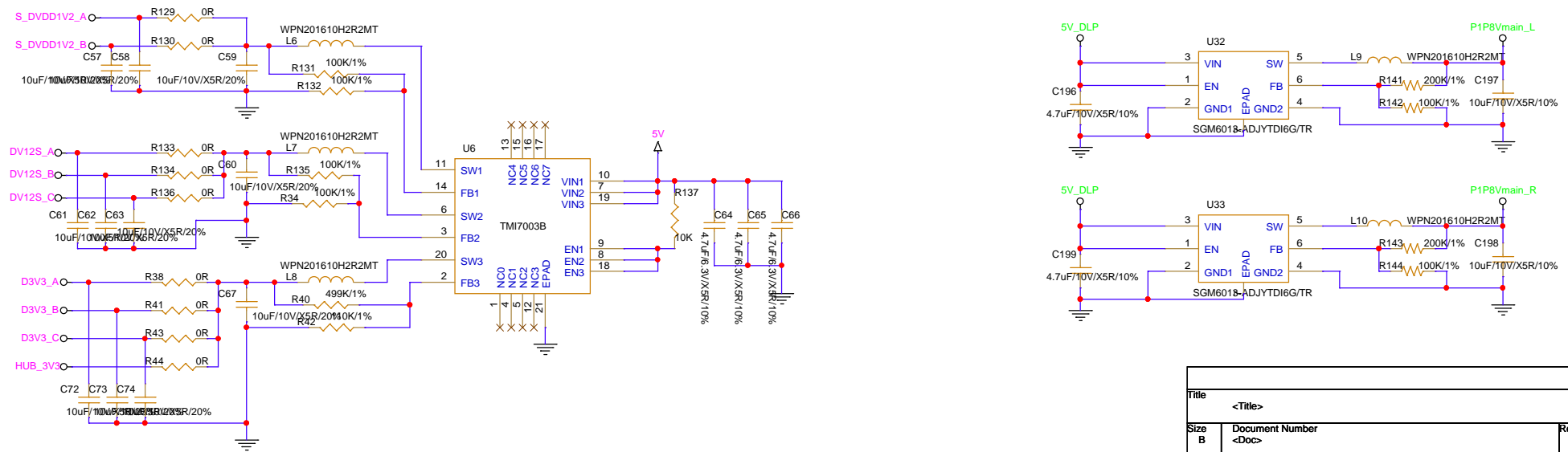
# LT7911D\_POWER

# MCU&IMU POWER



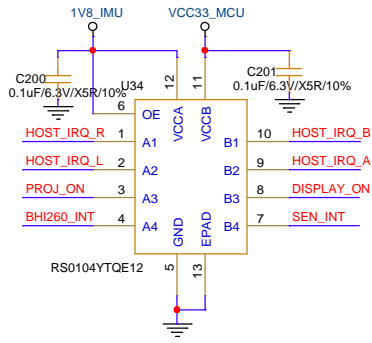
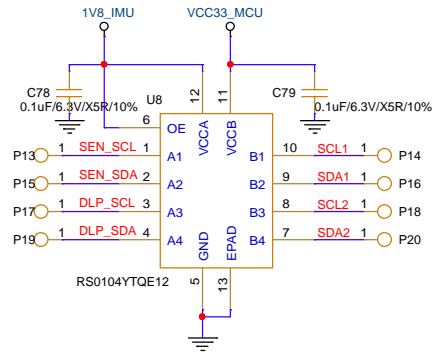
# DSP POWER

# DLPA2005 1V8



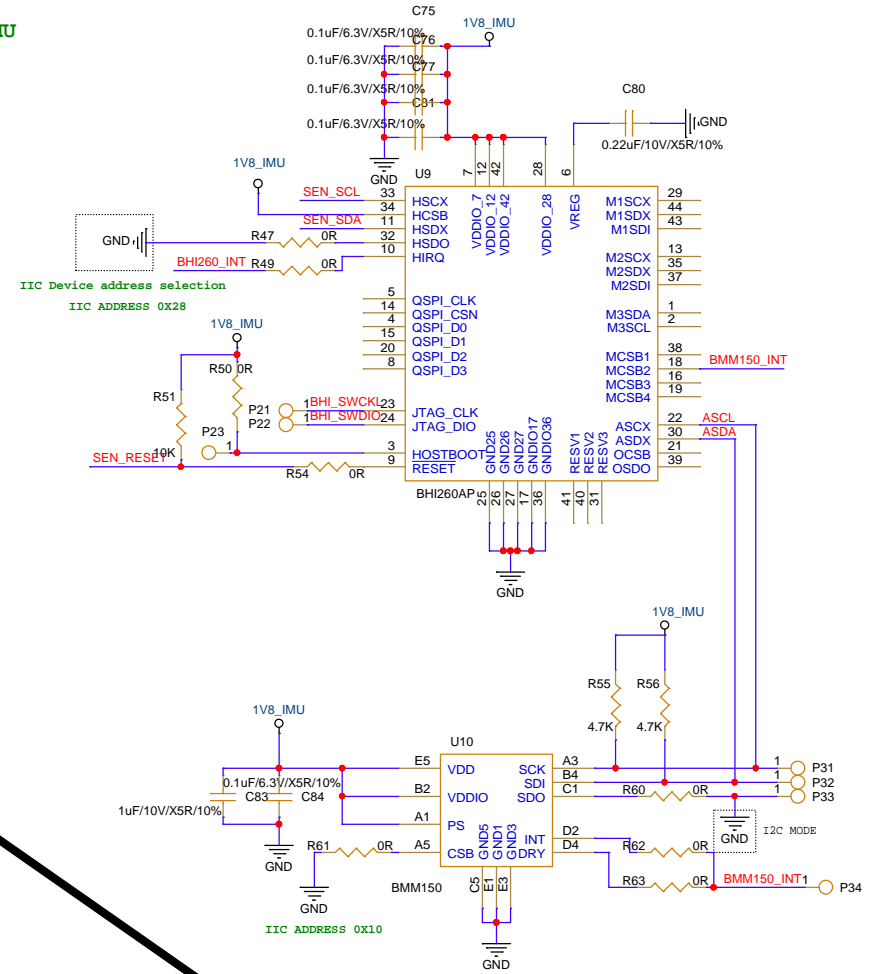
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level shift

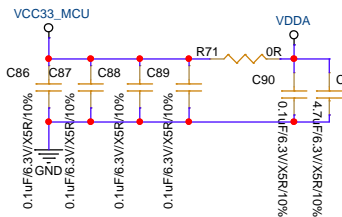
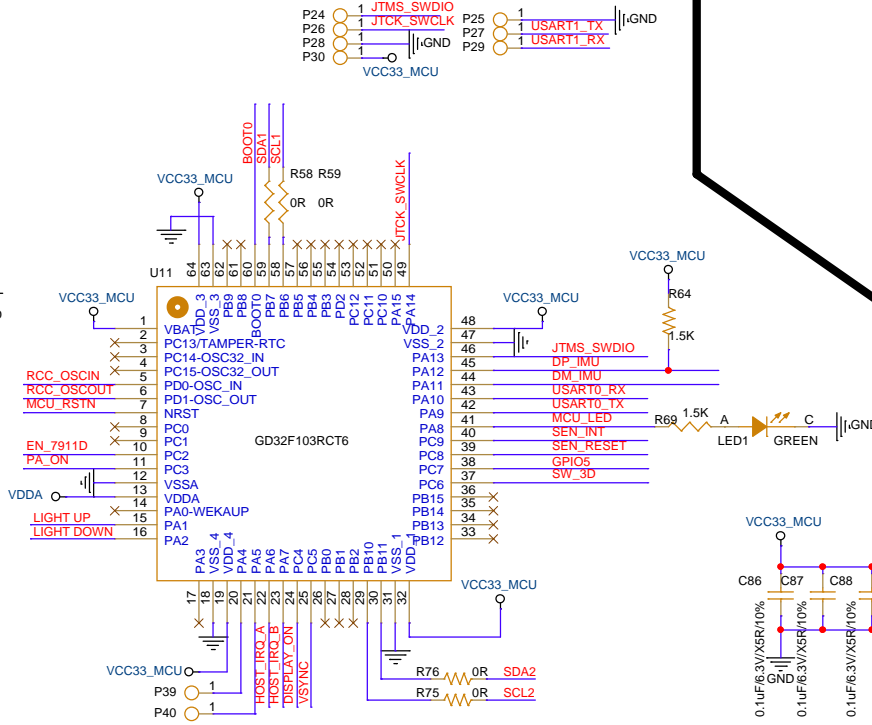
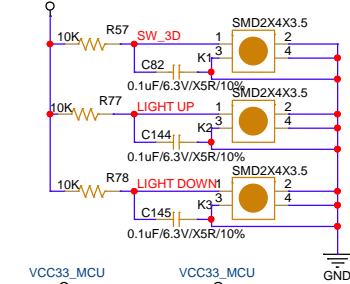


- >>PA\_ON [3]
- >>DLP\_SCL [3]
- >>DLP\_SDA [3]
- >>VSYNC [3]
- >>GPIO5 [3]
- >>DP\_IMU[3]
- >>DM\_IMU[3]
- >>PROJ\_ON [3]
- >>HOST\_IRQ\_R [3]
- >>HOST\_IRQ\_L [3]
- >>EN\_7911D [3]

9X IMU



KEY



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