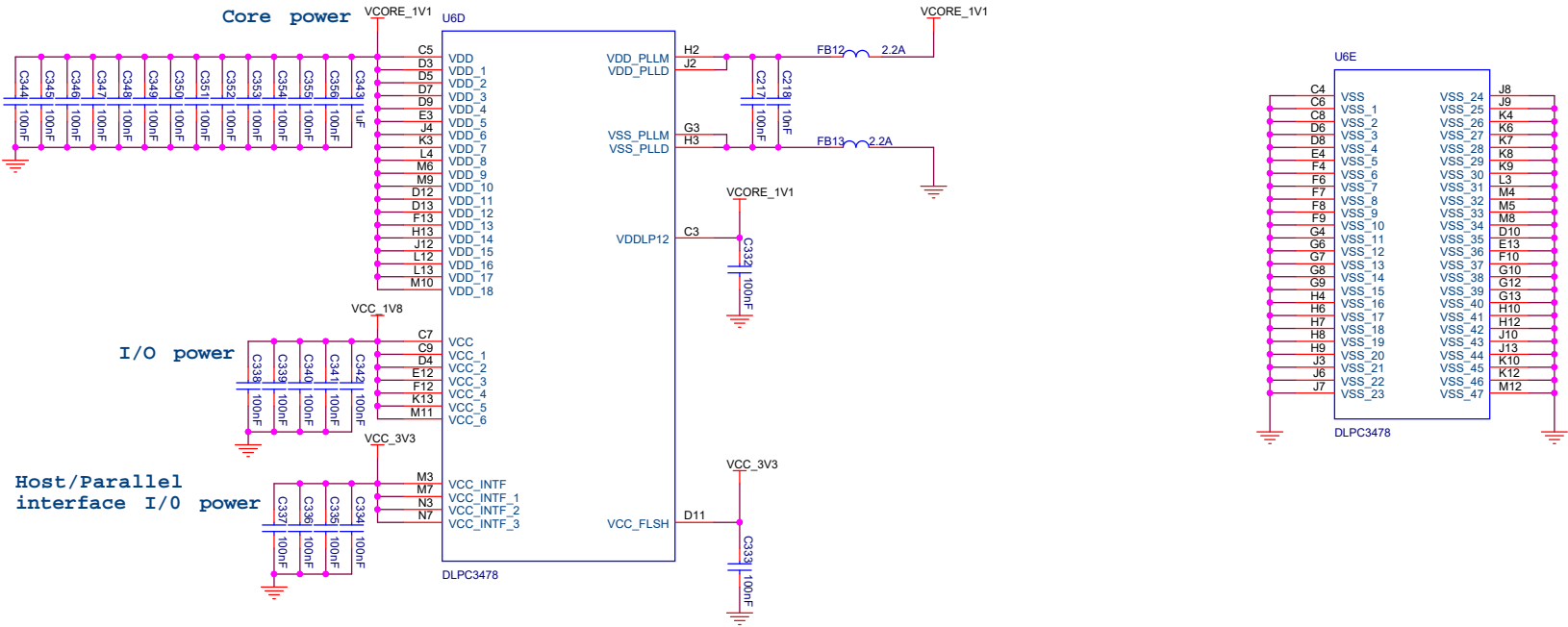
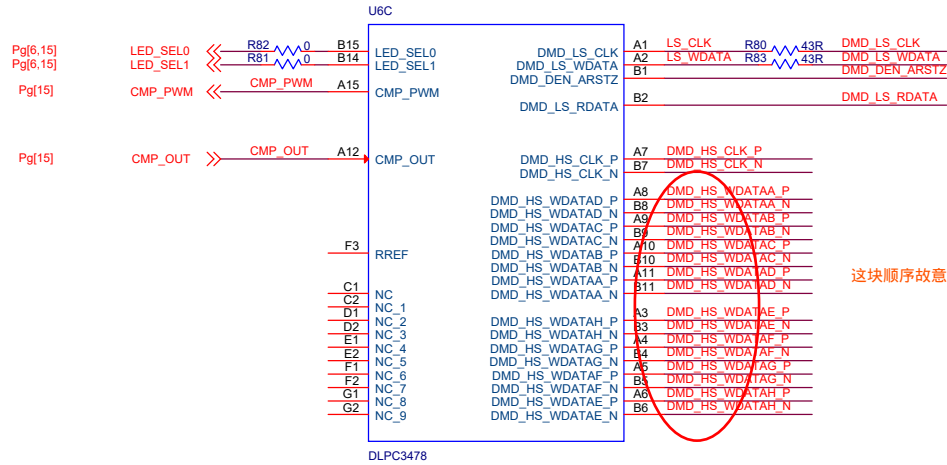


Hardware Department				
Designer:	Drawing Title:			
Drawn by:	Page Title: ?			
Approved:	Size	Document Number	Stage marker	
	A3	SCH-XXX PDF:SPF-XXX	S	
Date:	Friday, March 11, 2022	Sheet:	12 of 19	Rev: S02



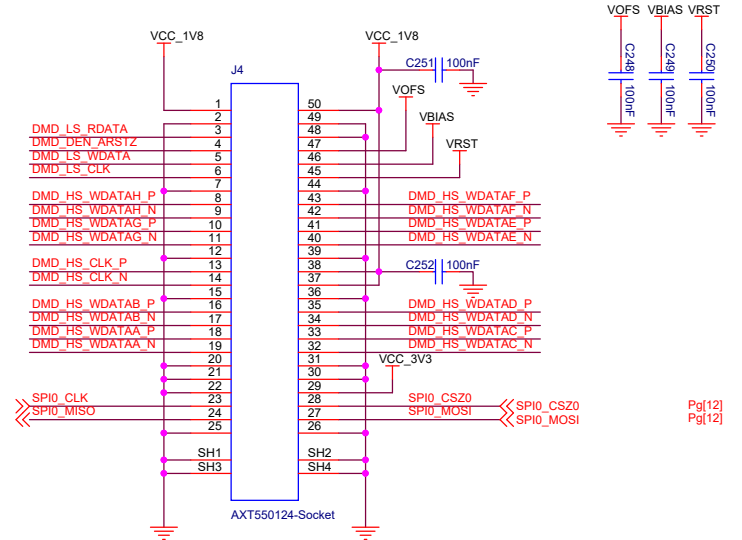
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Date: Friday, March 11, 2022		Sheet: 13 of 19		Rev: S02			



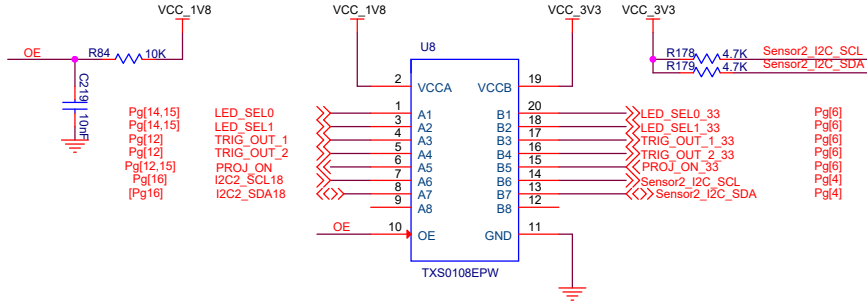
这块顺序故意反的，因为DMD那端接反了

Pg[12]
Pg[12]

SPI0_CLK
SPI0_MISO



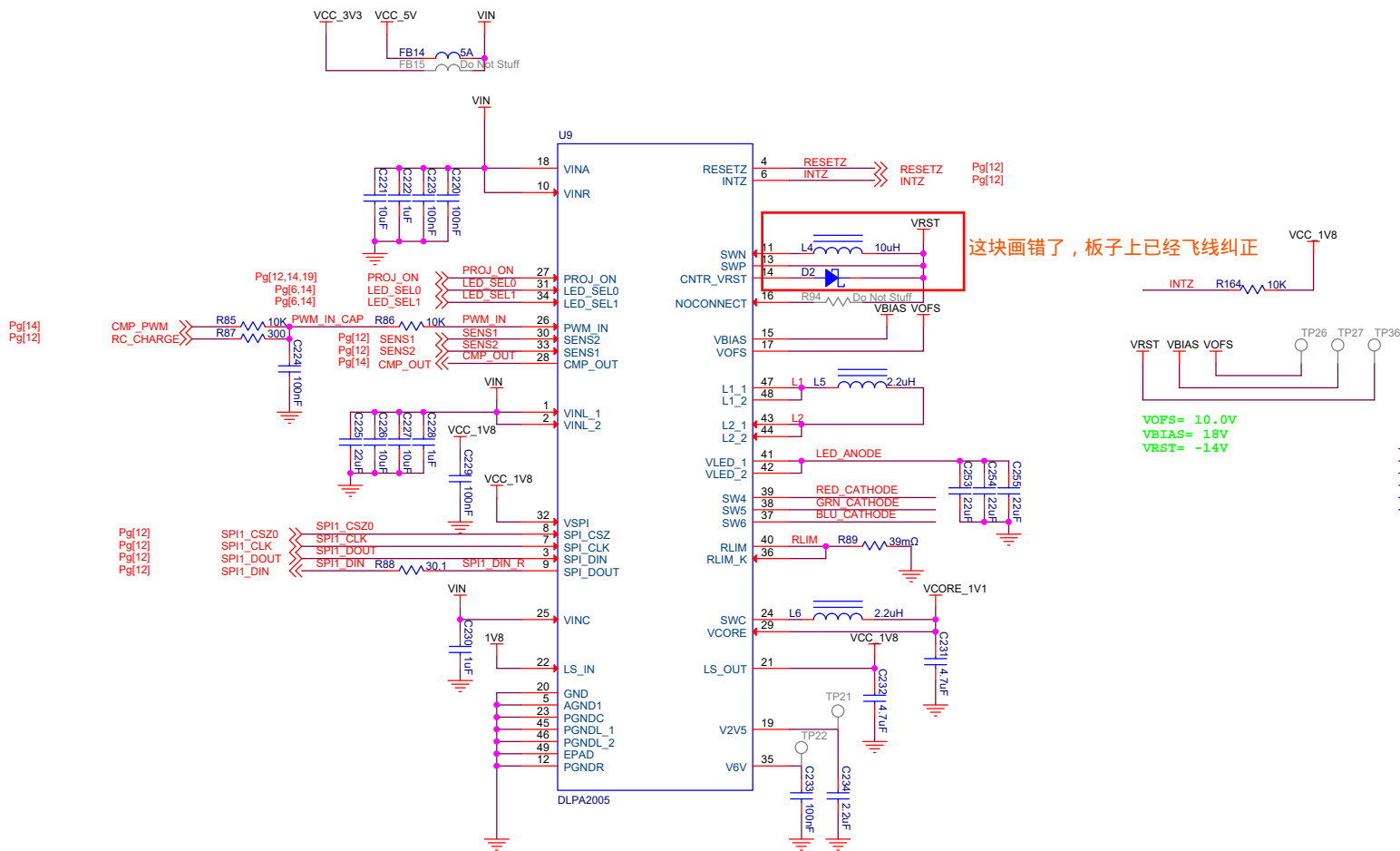
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Pg[12]



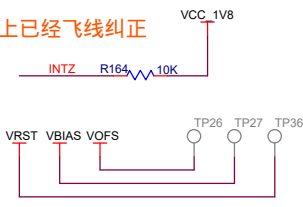
Pg[14,15]
Pg[14,15]
Pg[12]
Pg[12,15]
Pg[16]
Pg[16]

Pg[6]
Pg[6]
Pg[6]
Pg[6]
Pg[4]
Pg[4]

Designer:		Drawing Title: RYM02MB_SCH_VA0311		
Drawn by:		Page Title: ?		
Approved:	Size	Document Number	Stage marker	
	A3	SCH-XXX PDF:SPF-XXX	S	
Date: Friday, March 11, 2022		Sheet: 14 of 19 Rev: S02		



这块画错了，板上已经飞线纠正



VOFS= 10.0V
VBIAS= 18V
VRST= -14V

LED ANODE	1
LED ANODE	2
LED ANODE	3
RED CATHODE	4
GRN CATHODE	5
BLU CATHODE	6

Hardware Department			
Designer:	Drawing Title: RYM02MB_SCH_VA0311		
Drawn by:	Page Title: ?		
Approved:	Size: A3	Document Number: SCH-XXX PDF:SPF-XXX	Stage marker: S
Date: Friday, March 11, 2022	Sheet: 15 of 19	Rev: S02	