

NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "DMD_P3P3V" represents connection to the +3.3V digital power plane.
2. The symbol \equiv represents connection to the digital ground plane.
3. A "Z" suffix on a signal name indicates an active low signal.
4. All components with designators "U", "D", "Y" and "Q" are electrostatic discharge sensitive.
5. All resistor values are in ohms, 1/16W and 5% unless otherwise specified.

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	ECO 2120805: Initial Release	01/04/2013	DH

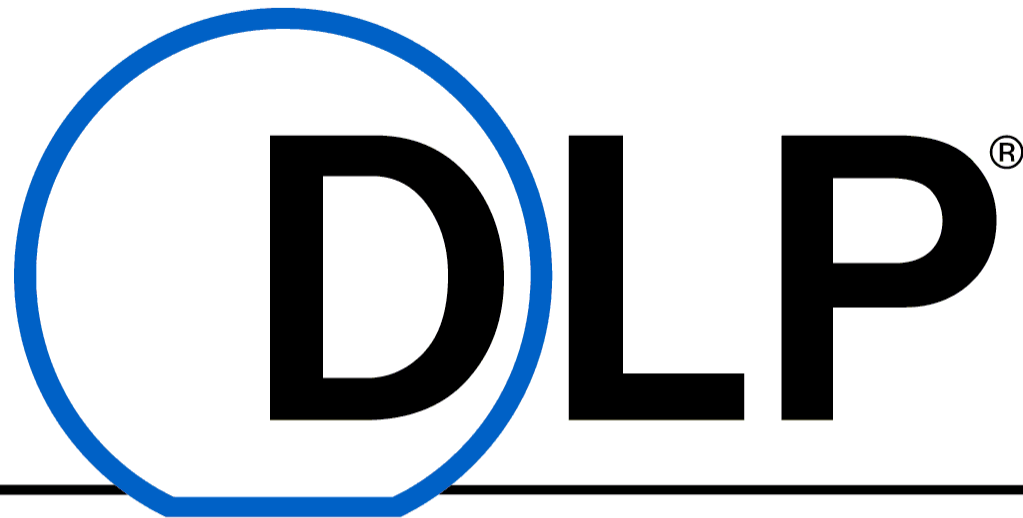
BEFORE USING TECHNICAL INFORMATION, THE USER SHOULD CAREFULLY READ THE FOLLOWING TERMS.

The term "Technical Information" includes reference designs, drawings, specifications, and other information relating to TI digital imaging products or applications, contained herein or provided separately in any format or via any medium.

TI is providing Technical Information for the convenience of purchasers of digital imaging products ("Users"), and will not accept any responsibility or liability arising from providing the Technical Information or its use. Any use or reliance on Technical Information is strictly the responsibility of the User.

1. **No Warranty. THE TECHNICAL INFORMATION IS PROVIDED "AS IS".** TI MAKES NO WARRANTIES OR REPRESENTATIONS, EXPRESS, IMPLIED OR STATUTORY, INCLUDING LACK OF VIRUSES, ACCURACY, OR COMPLETENESS. TI DISCLAIMS ANY WARRANTY OF TITLE, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, QUIET ENJOYMENT, QUIET POSSESSION, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS WITH REGARD TO THE TECHNICAL INFORMATION OR THE USE OF THOSE MATERIALS.
2. **Warranty for Products Not Affected.** The foregoing exclusion and disclaimer of warranty does not affect or diminish any warranty rights with regard to digital imaging products. Such rights are governed exclusively by the terms of a written and signed purchase agreement with TI.
3. **Limitations and Exclusion of Damages.** IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, SPECIAL, INCIDENTAL, CONSEQUENTIAL OR INDIRECT DAMAGES, HOWEVER CAUSED, ON ANY THEORY OF LIABILITY AND WHETHER OR NOT TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES, ARISING IN ANY WAY OUT OF THE TECHNICAL INFORMATION OR THE USE OF THE TECHNICAL INFORMATION.
4. **No Engineering Services.** User is fully responsible for all design decisions and engineering with regard to its products, including decisions relating to application of digital imaging products. By providing Technical Information TI does not intend to offer or provide engineering services or advice concerning User's design. If User desires engineering services, then User should rely on its retained employees and consultants and/or procure engineering services from a licensed professional engineer ("LPE").
5. **Compliance with Export Control Laws.** Unless prior authorization is obtained from the U.S. Department of Commerce, User may not export, re-export, or release, directly or indirectly, any Technical Information, or export, directly or indirectly, any direct product of such Technical Information to any destination or country to which the export, re-export or release of the Technical Information or direct product is prohibited by the Export Administration Regulations of the U.S. Department of Commerce ("EAR").

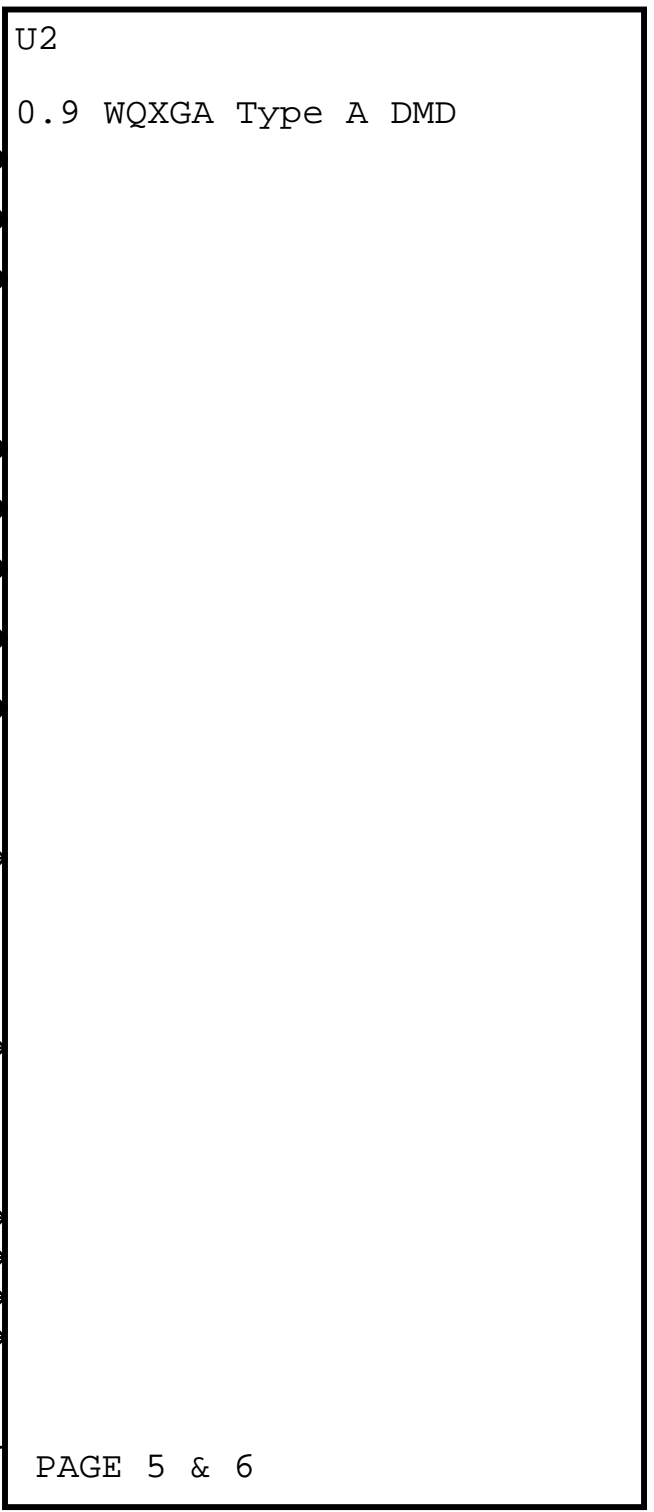
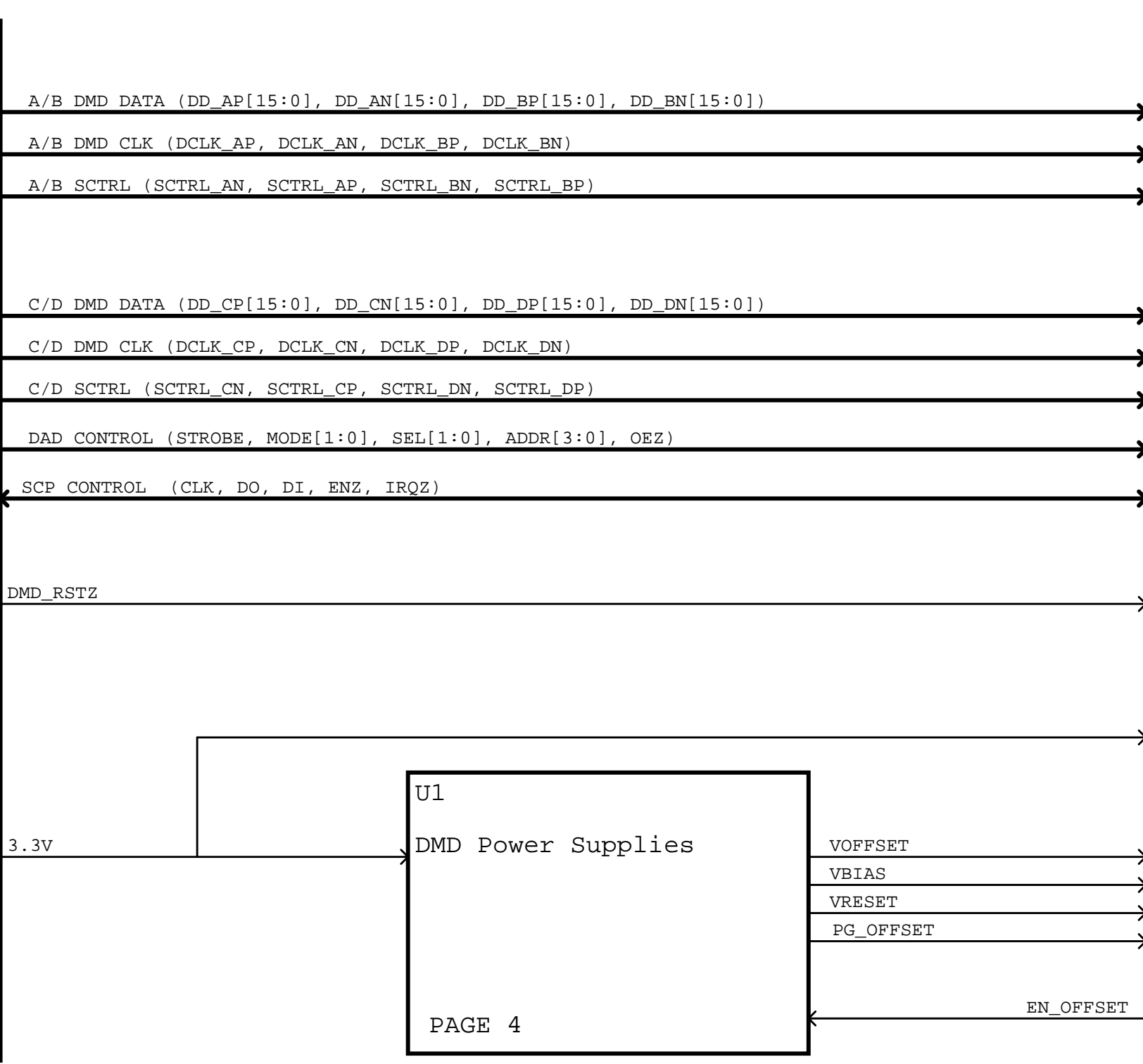
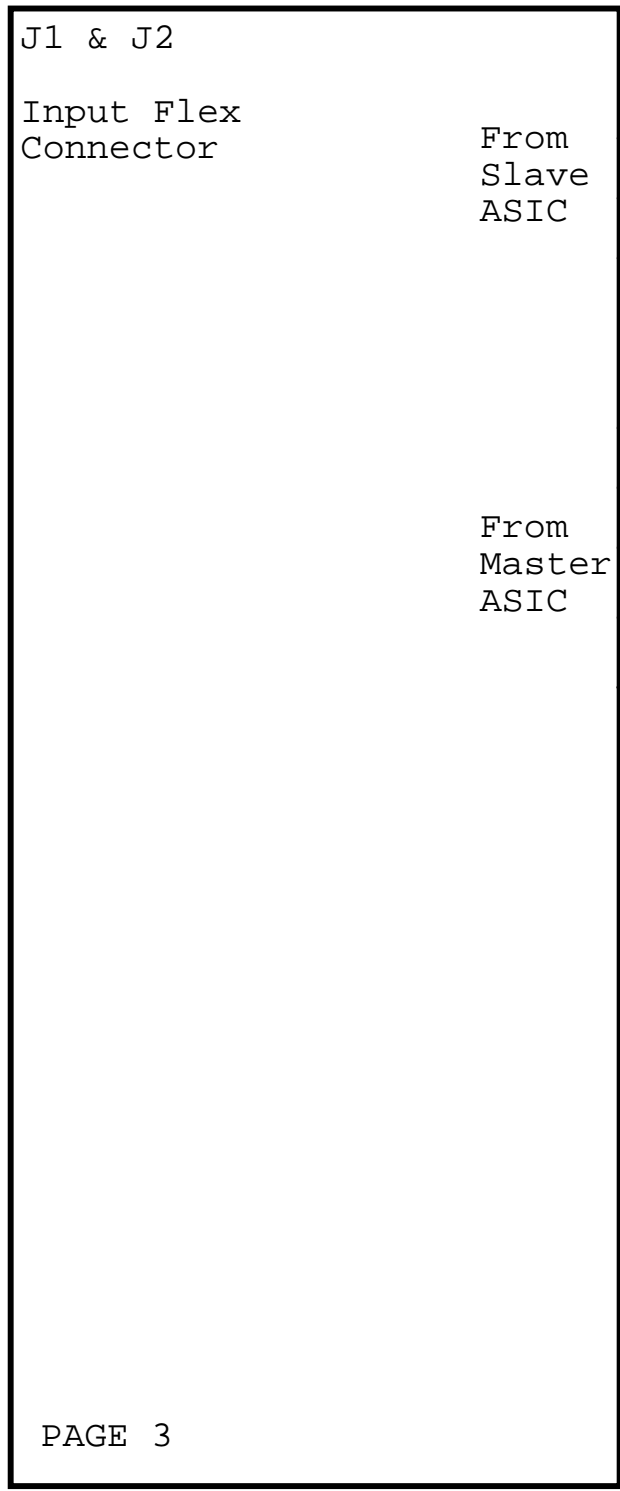
HIGHEST REFERENCE DESIGNATORS USED						
C90	D5	J3	L5	R33	TP43	U2
Q9						

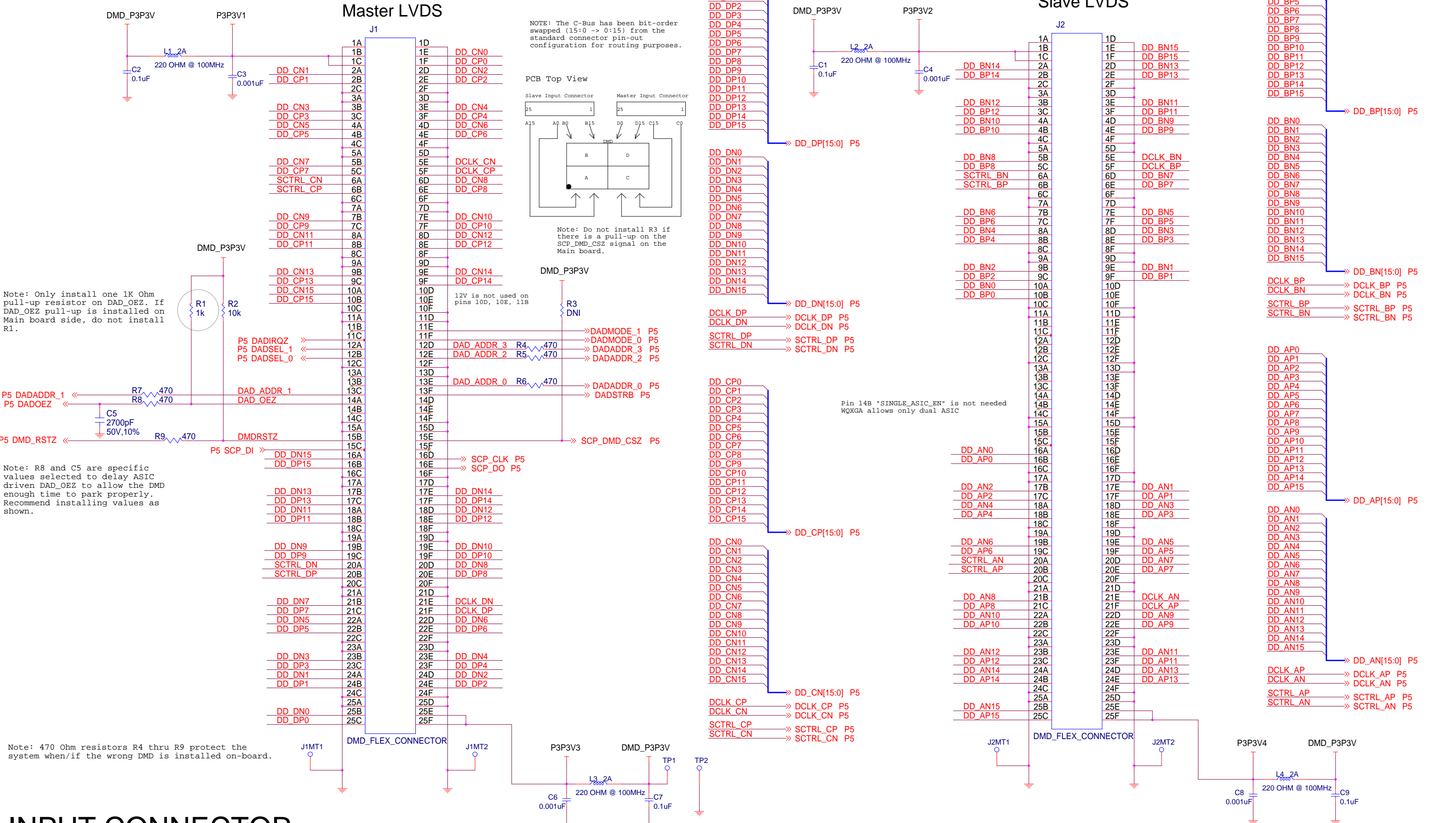


TEXAS INSTRUMENTS

TI Information - Selective Disclosure

		DWN	A. Ng	DATE	01/02/2013	TEXAS INSTRUMENTS <small>(C) COPYRIGHT 2012 TEXAS INSTRUMENTS ALL RIGHTS RESERVED</small>
		ENGR	A. Ng	01/02/2013		
		APVD	Nathan Buettner	01/02/2013		
		MFG				
2512147	0314SS	QA	Richard Gall	01/03/2013		TITLE ESD, .9 WQXGA Type A DMD Board
NEXT ASSY	USED ON					D DRAWING NO 2512146
APPLICATION		SW	Cadence CIS 16.3			REV A
						SCALE SHEET 1 of 6





Note: Only install one 1K Ohm pull-up resistor on DAD_OEZ. If DAD_OEZ pull-up is installed on Main board side, do not install R1.

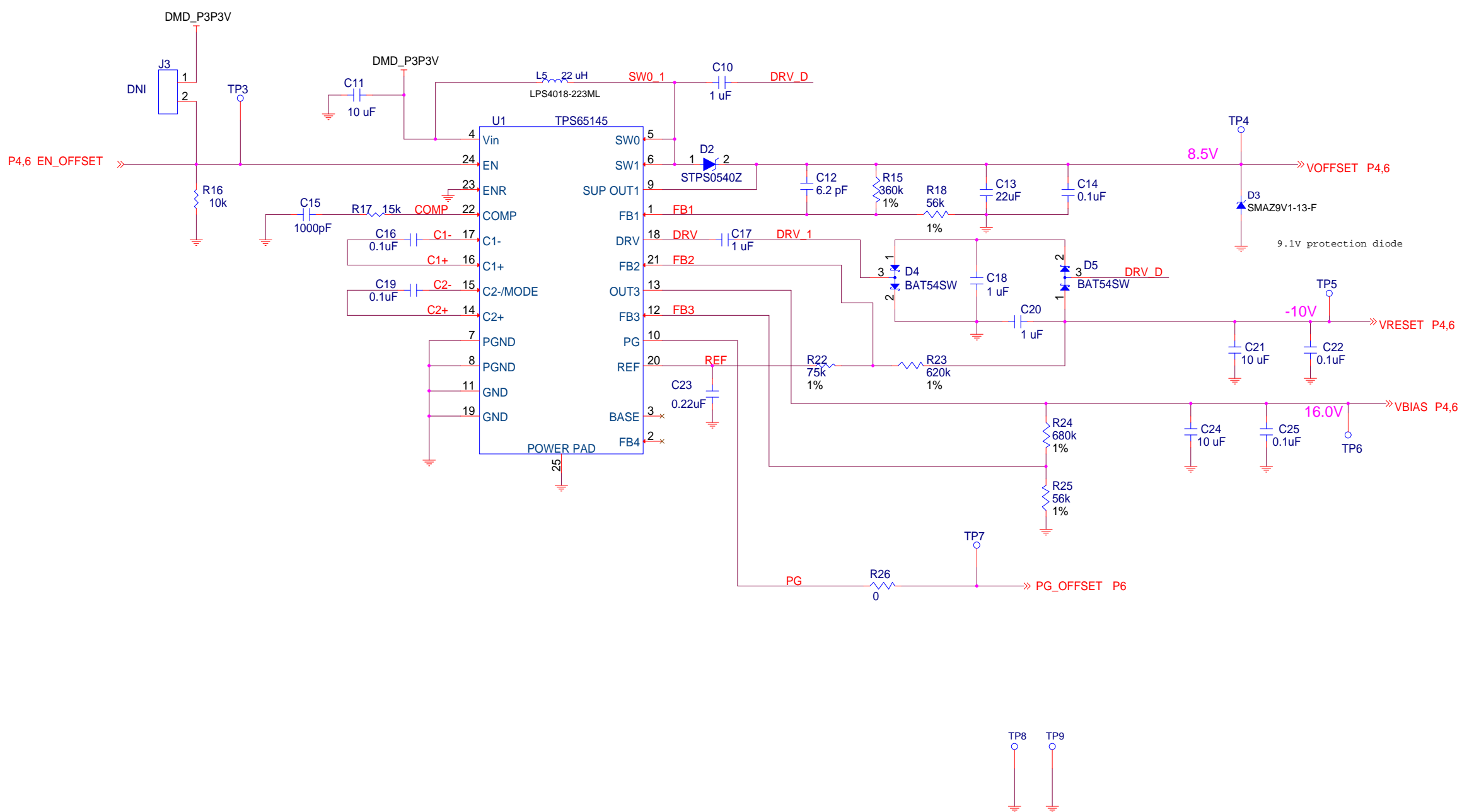
Note: R8 and C5 are specific values selected to delay ASIC driven DAD_OEZ to allow the DMD enough time to park properly. Recommend installing values as shown.

Note: 470 Ohm resistors R4 thru R9 protect the system when/if the wrong DMD is installed on-board.

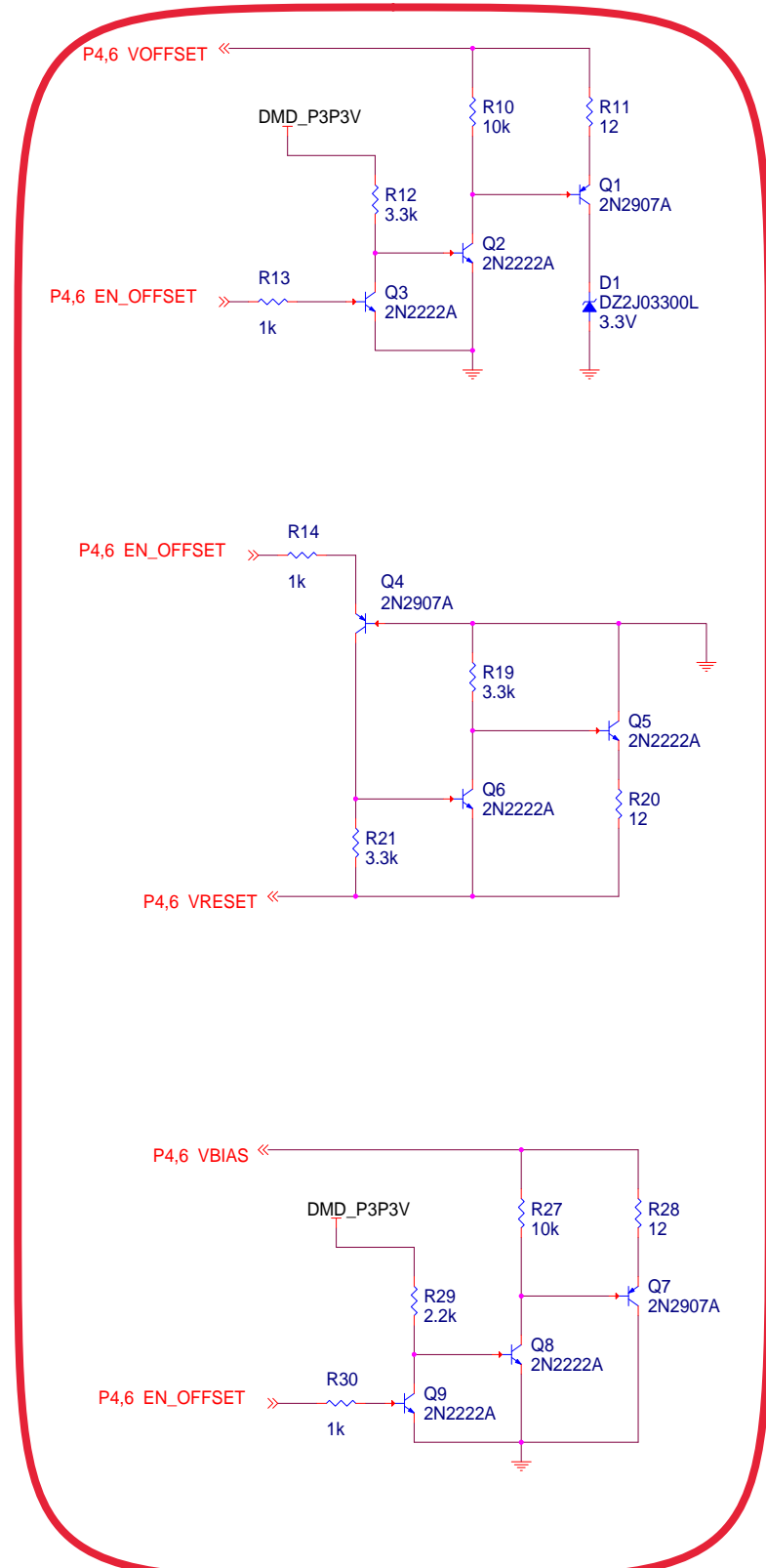
NOTE: The C-Bus has been bit-order swapped (15:0 -> 0:15) from the standard connector pin-out configuration for routing purposes.

Note: Do not install R3 if there is a pull-up on the SCP_DMD_CSZ signal on the Main board.

Pin 14B "SINGLE_ASIC_EN" is not needed WQXGA allows only dual ASIC



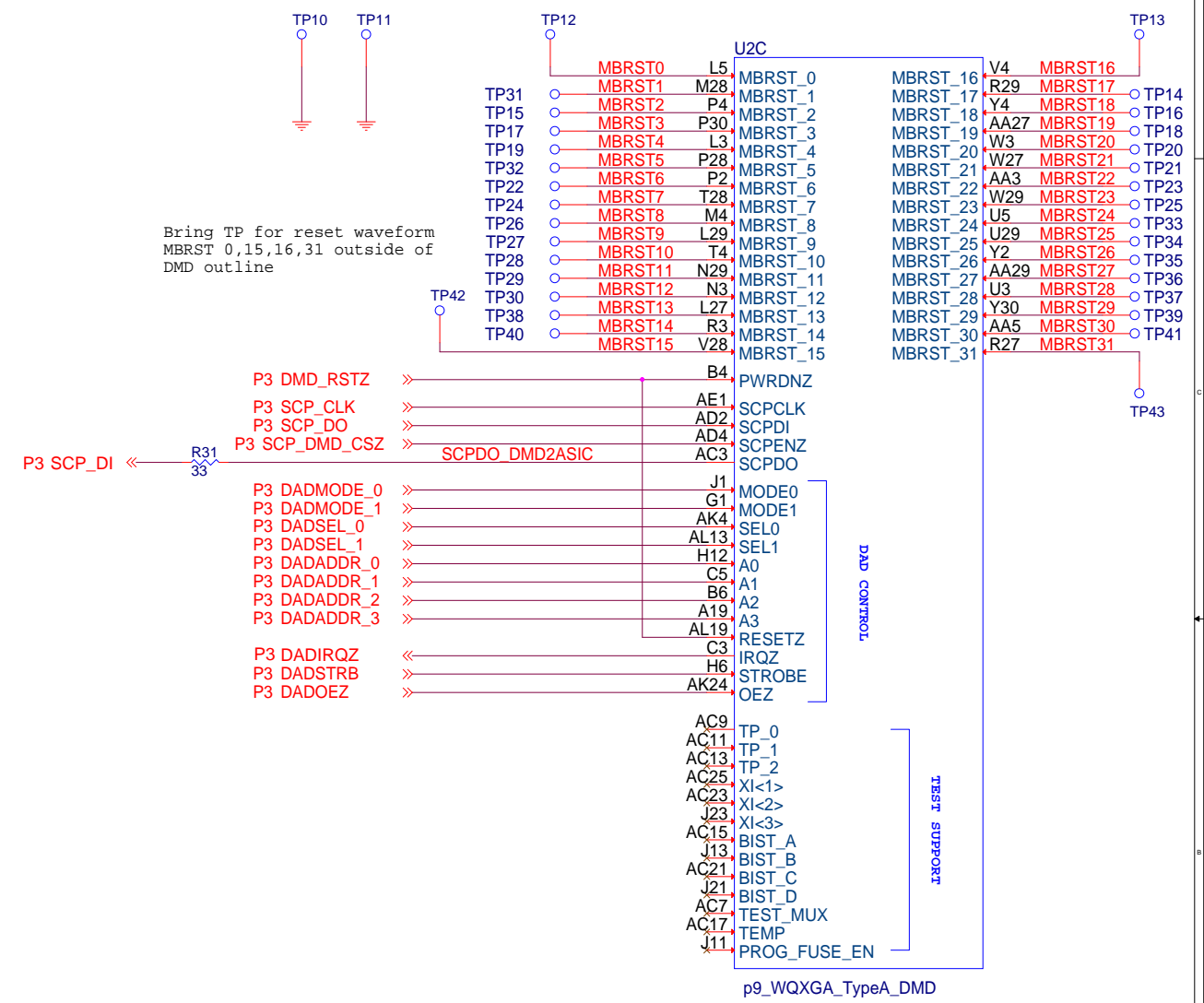
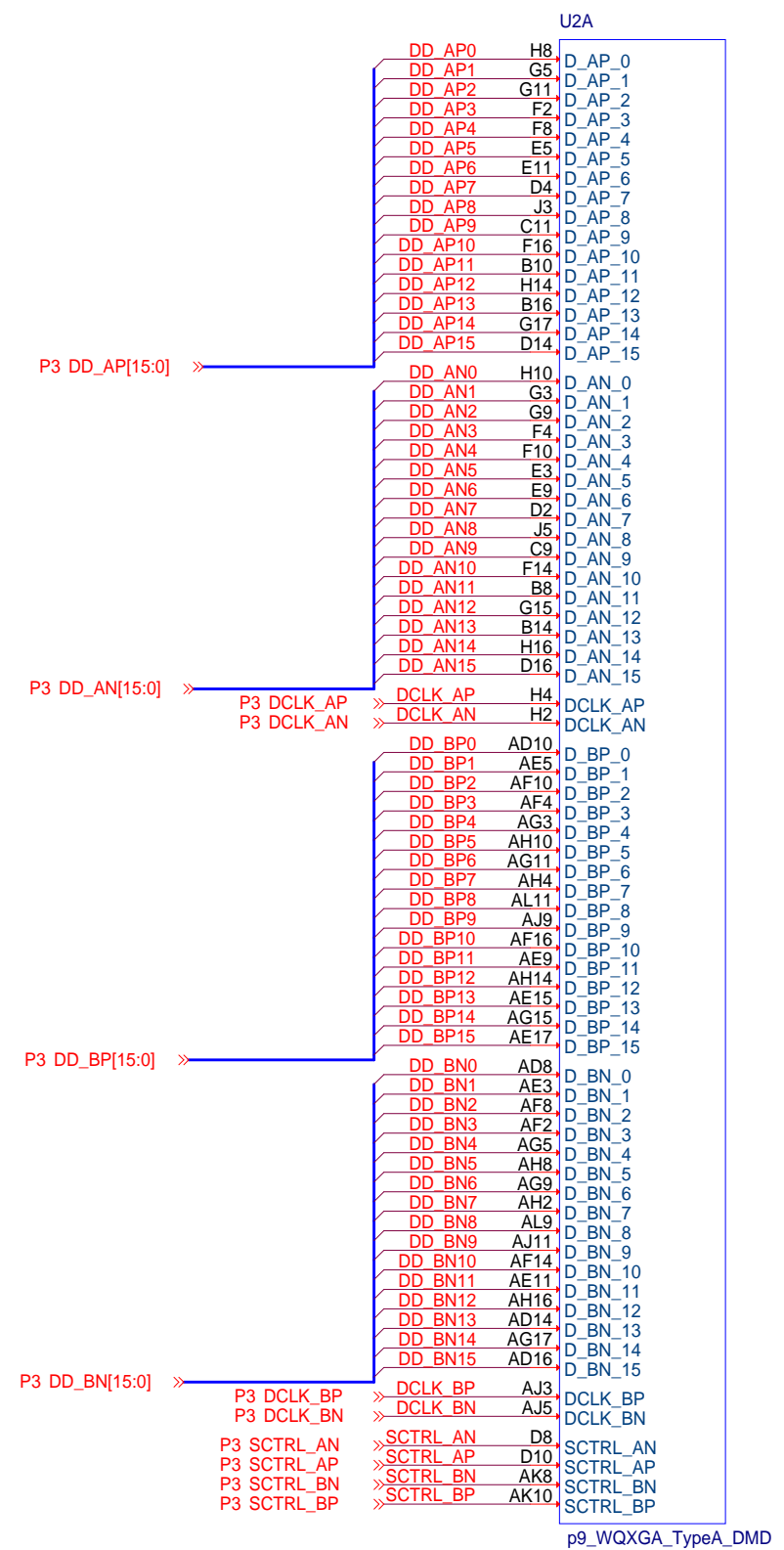
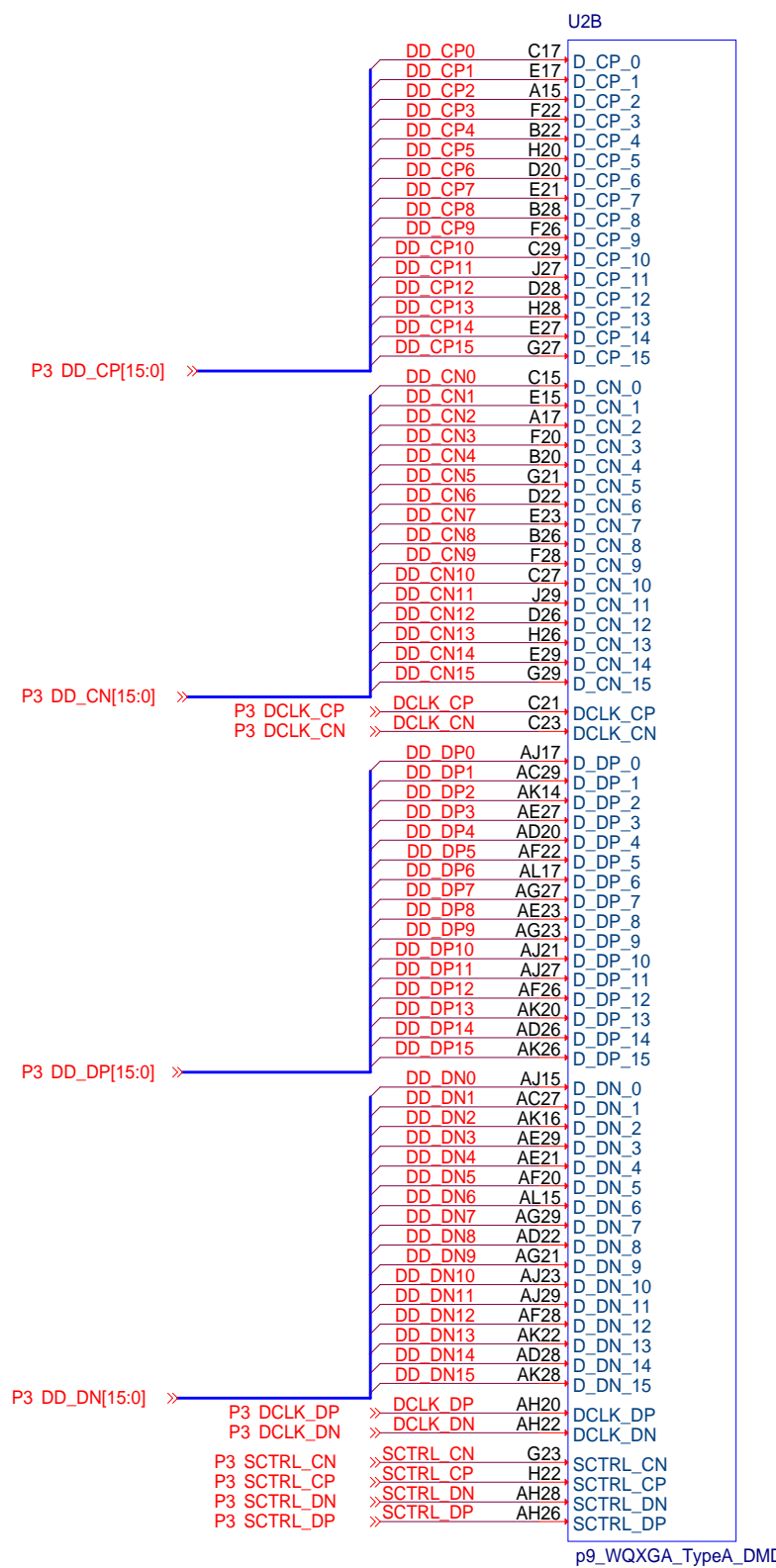
Power Down Circuitry



DMD Power Supplies

TI Information - Selective Disclosure

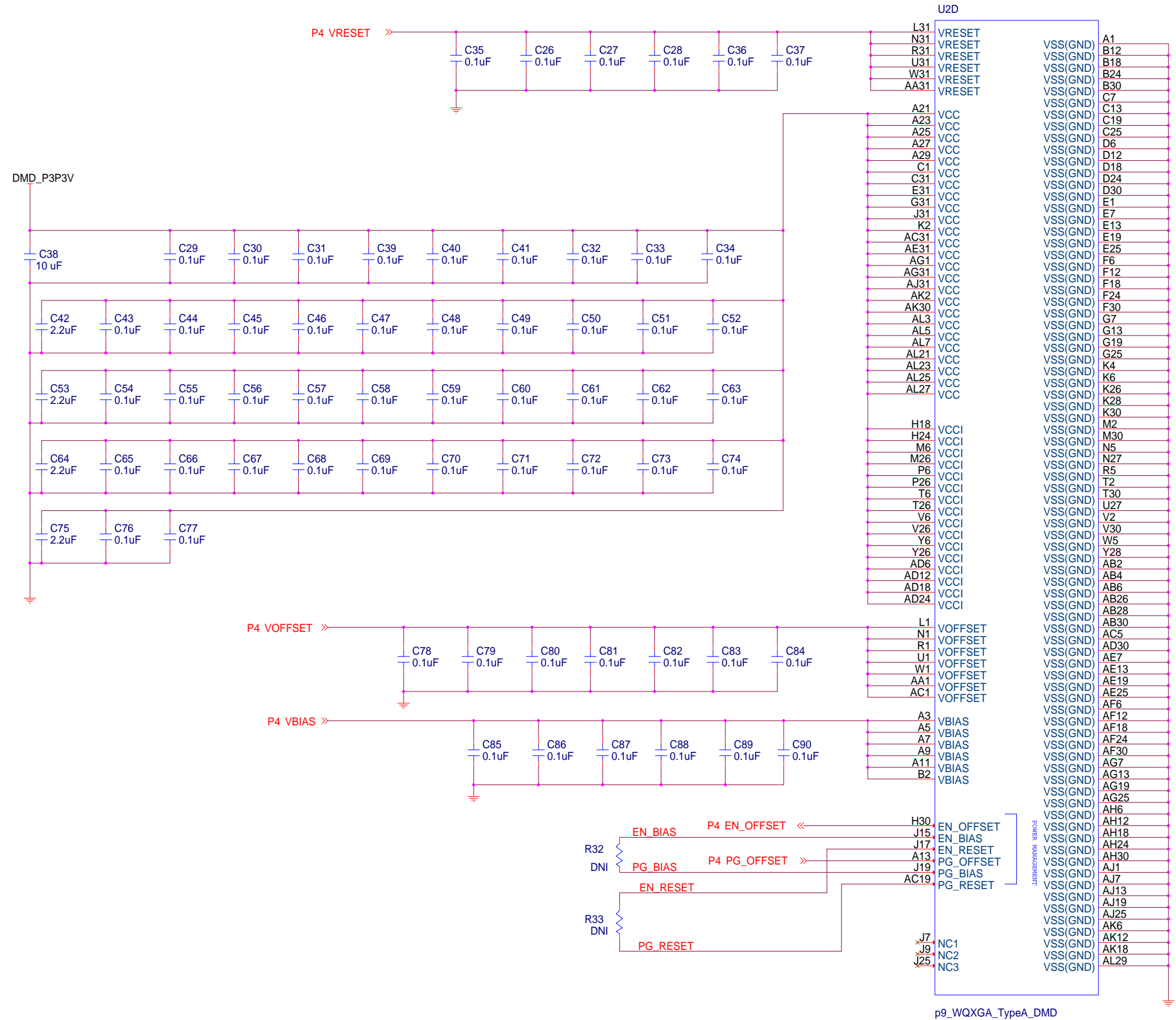
TEXAS INSTRUMENTS	DWN	NG	DATE	1/15/2012	D	DRAWING NO	2512146	REV	A
	ISSUE DATE					SCALE		SHEET 4 OF 6	



DMD Data/Control

TI Information - Selective Disclosure

TEXAS INSTRUMENTS	DWN	A. NG	DATE	1/15/2012	D	DRAWING NO	2512146	REV	A
	ISSUE DATE					SCALE		SHEET 5 OF 6	



p9_WQXGA_TypeA_DMD

DMD Power/Gnd

TI Information - Selective Disclosure

TEXAS INSTRUMENTS	DWN	A. NG	DATE	1/15/2012	D	DRAWING NO	2512146	REV	A
	ISSUE DATE					SCALE		SHEET 6 OF 6	