The basic requirements for the Flash part are listed below:

1. - Flash Type: NOR
2. - Common Flash Interface (CFI)
3. - Flash access speed: 110ns or faster
4. - 16 bit interface
5. - Sectors: bottom boot block preferred
6. - 16MBit to 128MBit (for ARM software, sequences, configuration, etc)
7. - Supply Voltage: 3.3V
8. - Optional: Multiple banks to support simultaneous read and write operations for gamma
9. download, splash download and splash capture

Simultaneous read/write flash is needed for Splash Capture, but this is not supported in DPP6401.

 If the capability to download Splash & Gamma while displaying an image is not required, then simultaneous read/write flash with multiple banks are not needed.

A single bank flash memory can be used with DPP6401.

The following are some of the supported flash memory parts

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Part Number**  | **Manufacturer**  | **Size**  | **Sectors**  | **Speed**  | **Banks** |
| S29PL032J-70  | Spansion  | 32MBit | Bottom Boot Block  | 70ns  | 4  |
| K8D3216UBC-PIO70  | Samsung  | 32MBit | Bottom Boot Block  | 70ns  | 2  |
| M29DW323DB70  | ST Micro  | 32MBit | Bottom Boot Block  | 70ns  | 2  |
| S29GL032A90TFIR40  | Spansion  | 32MBit | Bottom Boot Block  | 90ns  | 1  |
| AT49BV320D-70TU  | Atmel  | 32MBit | Bottom Boot Block  | 70ns  | 1  |
| JS28F320J3D-75  | Intel  | 32MBit | Uniform  | 75ns  | 1  |

 If a new or unsupported part is used, then a new entry in the Composer flash parameters may be needed.