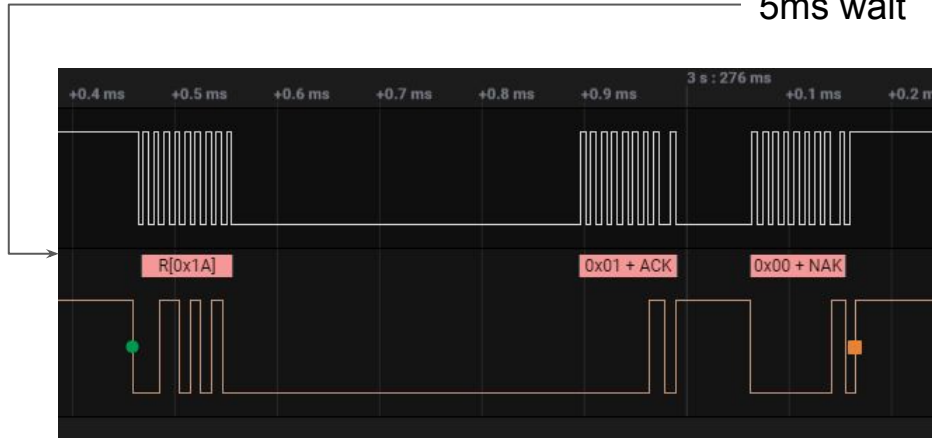


# I2C transaction for reading display mode (command dmddm)



5ms wait



Type	Start	Duration	ack	address	read	data
start	3.269 158 980 s	20 ns				
address	3.269 168 980 s	86.6 μs	true	0x1A	false	
data	3.269 263 440 s	485.1 μs	true			0x69
stop	3.269 761 680 s	20 ns				
start	3.269 766 520 s	20 ns				
address	3.269 776 520 s	86.58 μs	true	0x1A	true	
data	3.270 202 840 s	92.52 μs	true			0x00
data	3.270 371 220 s	90.44 μs	false			0x00
stop	3.270 472 040 s	20 ns				
start	3.275 459 000 s	20 ns				
address	3.275 468 980 s	86.62 μs	true	0x1A	true	
data	3.275 896 220 s	92.78 μs	true			0x01
data	3.276 063 900 s	90.48 μs	false			0x00
stop	3.276 164 760 s	20 ns				