PCB Stackup Data for Impedance Control

1) PCB Type：FLA7 EVM Board　REV：A
2) PCB Layer：　6
3) PCB thickness：1.6mm±10%
6) Impedence tolerance :±10%

Layer	Cu/PrePreg/Core	Material Type	Thickness (mil)	Dielectric Constant (ER)	Impedance							
					50Ω (Single end)		68Ω (Single end)		100Ω (Differential pair)		90Ω (Differential pair)	
Via Type		SolderMask	0.80	3.75								
		Add Plating	0.60									
L1-Top	1oz	0.5oz	0.60		5mil/ 49.23Ω	ref L2	L3 7.5mil/ 68.88Ω	ref L3	4/8/4 mil/99.85Ω	ref L2	5 / 8 / 5 mil / 90.12Ω	ref L2
	PrePreg		2.9	3.88								
L2-GND		1.0oz	1.20									
	core	0.1mm	4	4								
L3-Signal		1.0oz	1.20		5mil/ 50.07Ω	ref L2&L5						
	core+Prepreg		40	4.5								
L4-Signal		1.0oz	1.20		5mil/ 50.07Ω	ref L2&L5						
	core	0.1mm	4	4								
L5-PWR		1.0oz	1.20									
	PrePreg		2.9	3.88								
L6-Bottom	1oz	0.5oz	0.60	3.75	5mil/ 49.23Ω	ref L5	7.5mil/ 68.88Ω	ref L4	4/8/4 mil/99.85Ω	ref L5	5 / 8 / 5 mil / 90.12Ω	ref L5
		Add Plating	0.60									
		SolderMask	0.80									
		Total thickness (mil)	62.60									
		Total thickness(mm)	1.59									