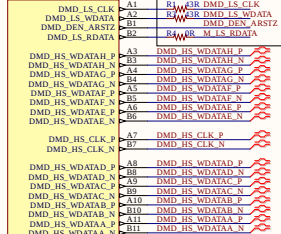


MASTER DMD INTERFACE

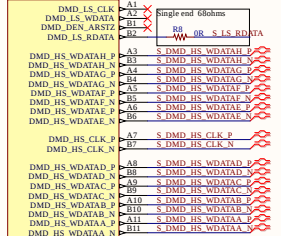
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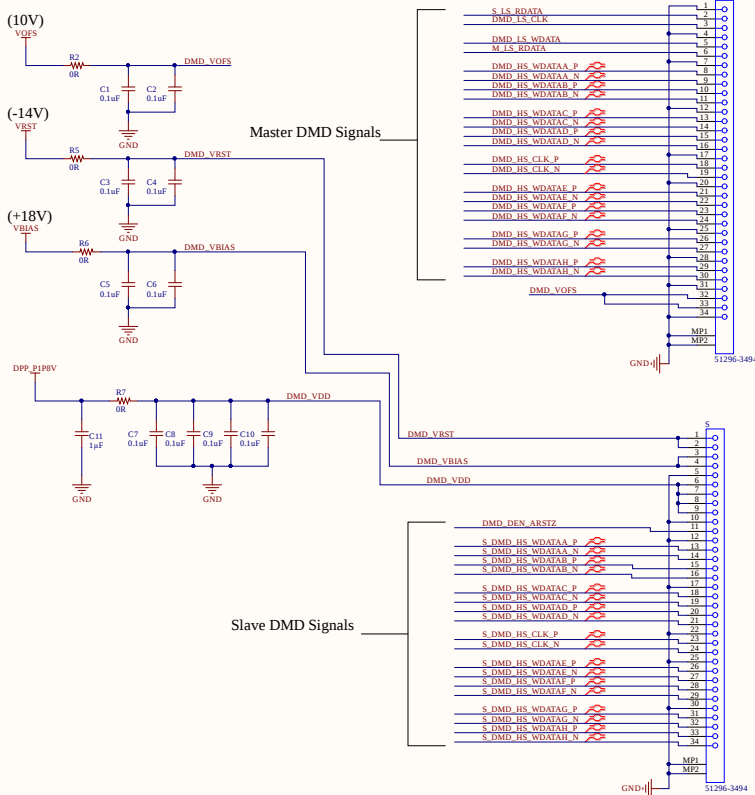
DLP3439

SLAVE DMD INTERFACE

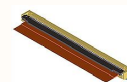
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DLP3439

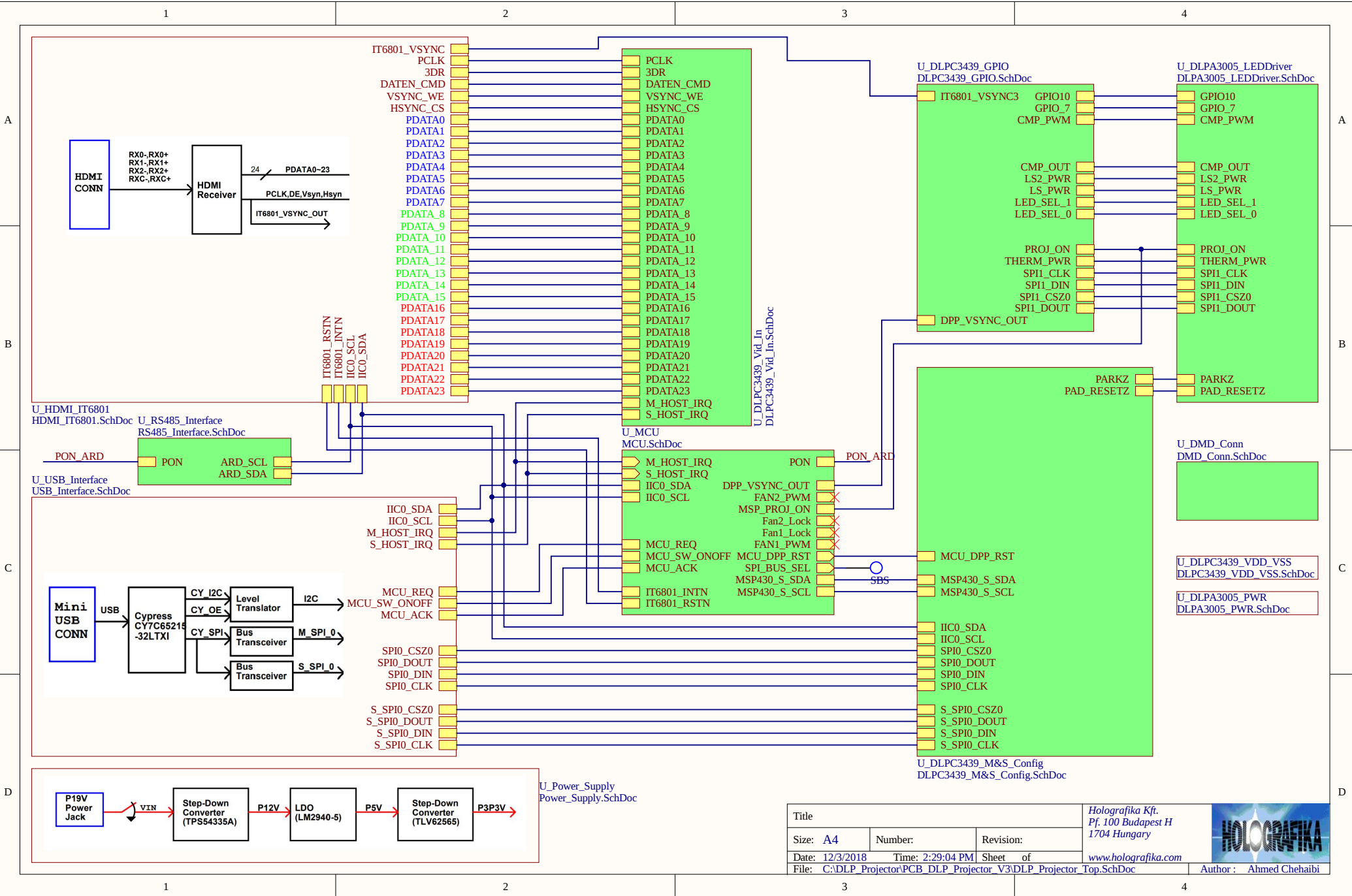


Configuration		
DLP3439 Side		DMD Side
Option 2 - Master	Option 1 - Slave	Input DATA_p_0
HS_WDATA_E_P	HS_WDATA_D_N	Input DATA_n_0
HS_WDATA_E_N	HS_WDATA_C_P	Input DATA_p_1
HS_WDATA_F_P	HS_WDATA_C_N	Input DATA_n_1
HS_WDATA_F_N	HS_WDATA_B_P	Input DATA_p_2
HS_WDATA_G_P	HS_WDATA_B_N	Input DATA_n_2
HS_WDATA_G_N	HS_WDATA_A_P	Input DATA_p_3
HS_WDATA_H_P	HS_WDATA_A_N	Input DATA_n_3
HS_WDATA_H_N	HS_WDATA_I_P	Input DATA_p_4
HS_WDATA_I_P	HS_WDATA_I_N	Input DATA_n_4
HS_WDATA_J_P	HS_WDATA_J_N	Input DATA_p_5
HS_WDATA_K_P	HS_WDATA_K_N	Input DATA_n_5
HS_WDATA_L_P	HS_WDATA_L_N	Input DATA_p_6
HS_WDATA_M_P	HS_WDATA_M_N	Input DATA_n_6
HS_WDATA_N_P	HS_WDATA_N_N	Input DATA_p_7
HS_WDATA_O_P	HS_WDATA_O_N	Input DATA_n_7

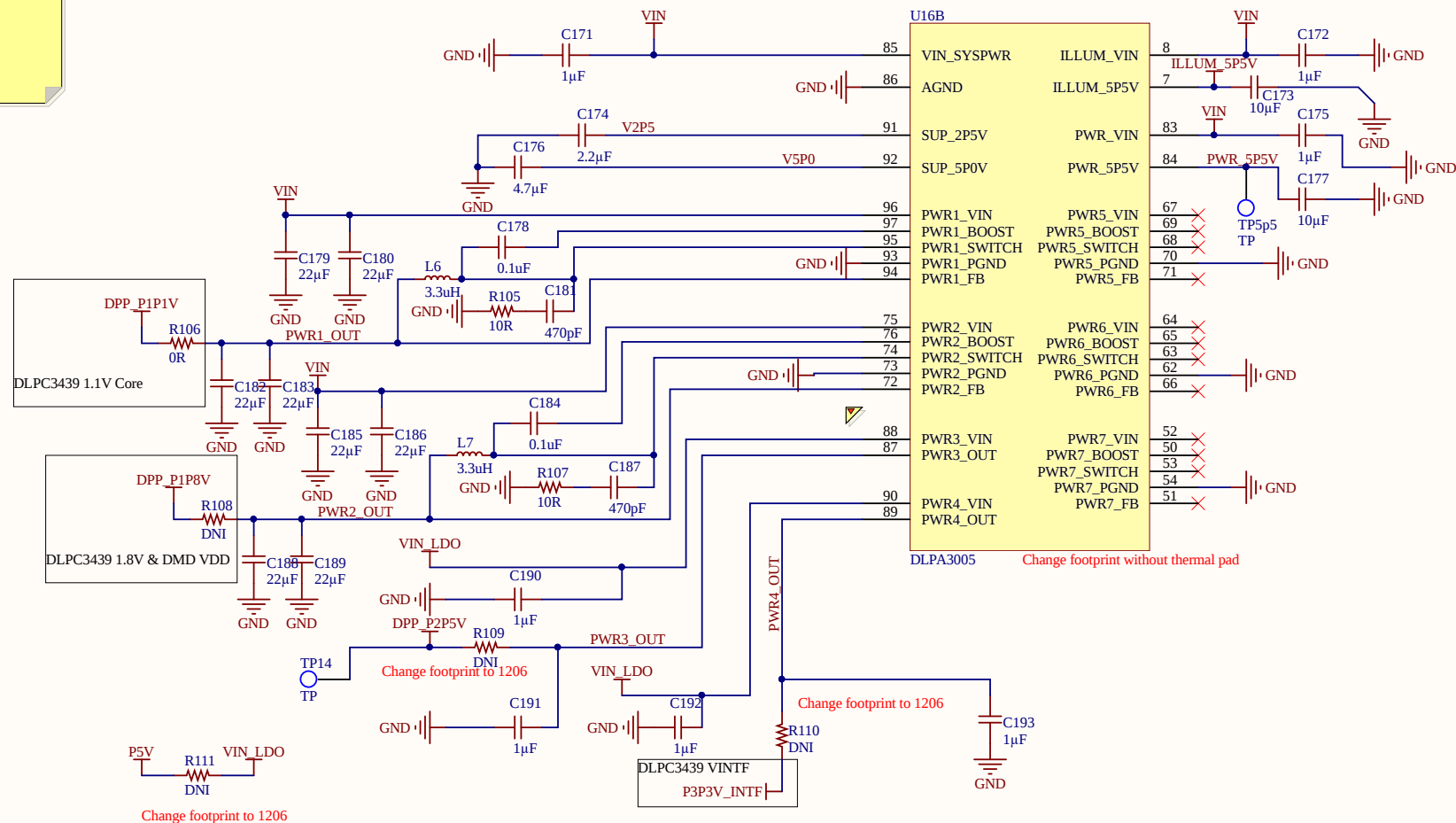


FPC Connector

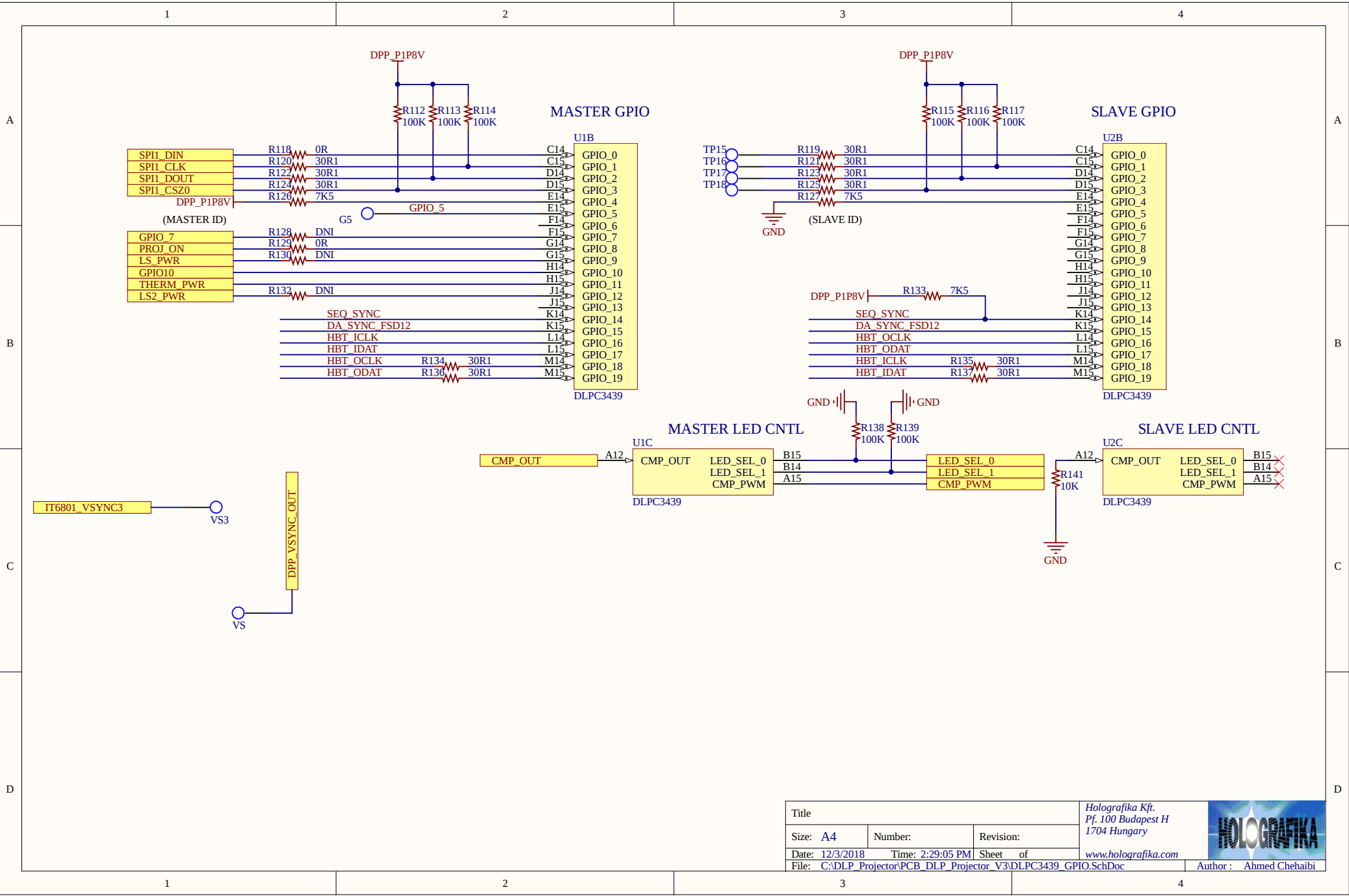


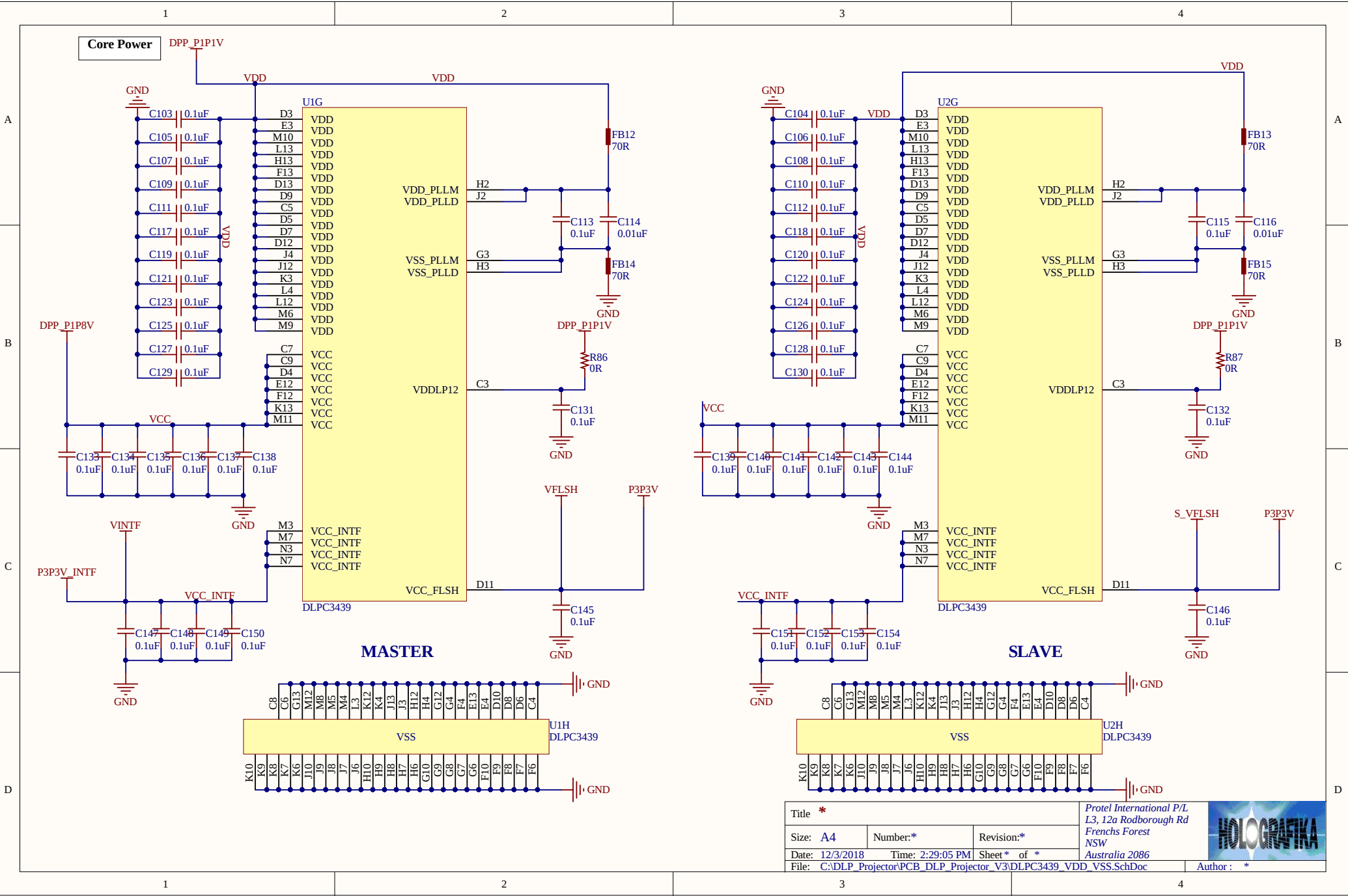


DNP all the resistors which deliver power to the DLP and DMD !! After test place them (0V Output)
R106
R108
R109
R110



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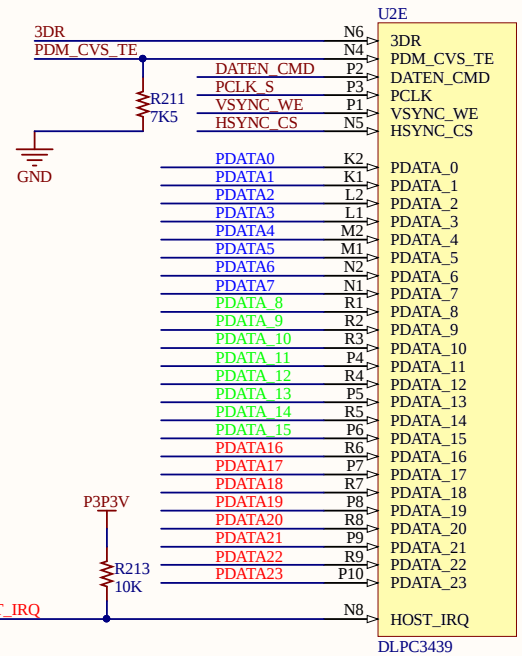
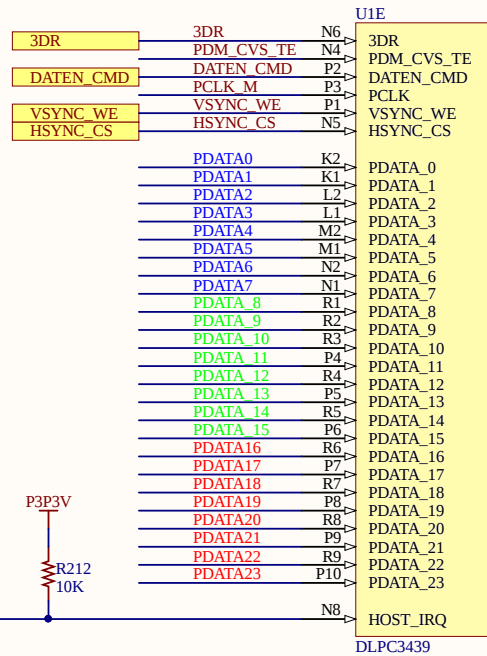
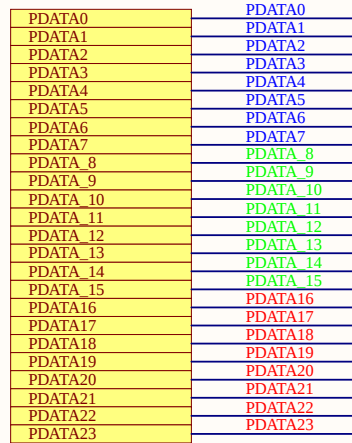
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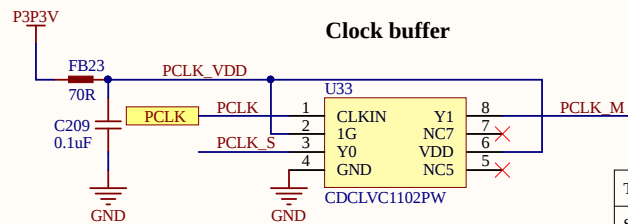
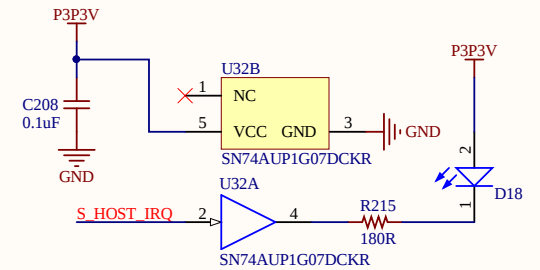
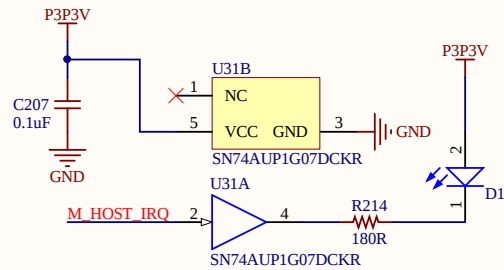
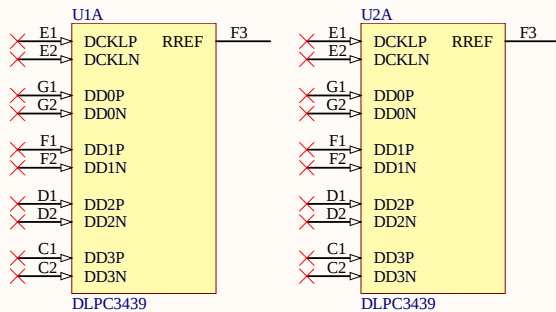
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**From IT6801
HDMI Receiver**

The DSI Port is not used.
Inputs are left unconnected per ASIC spec recommendation.



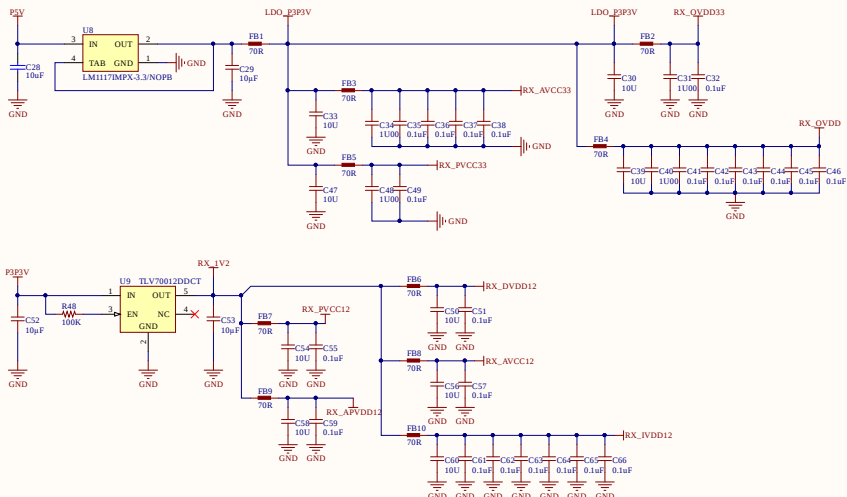
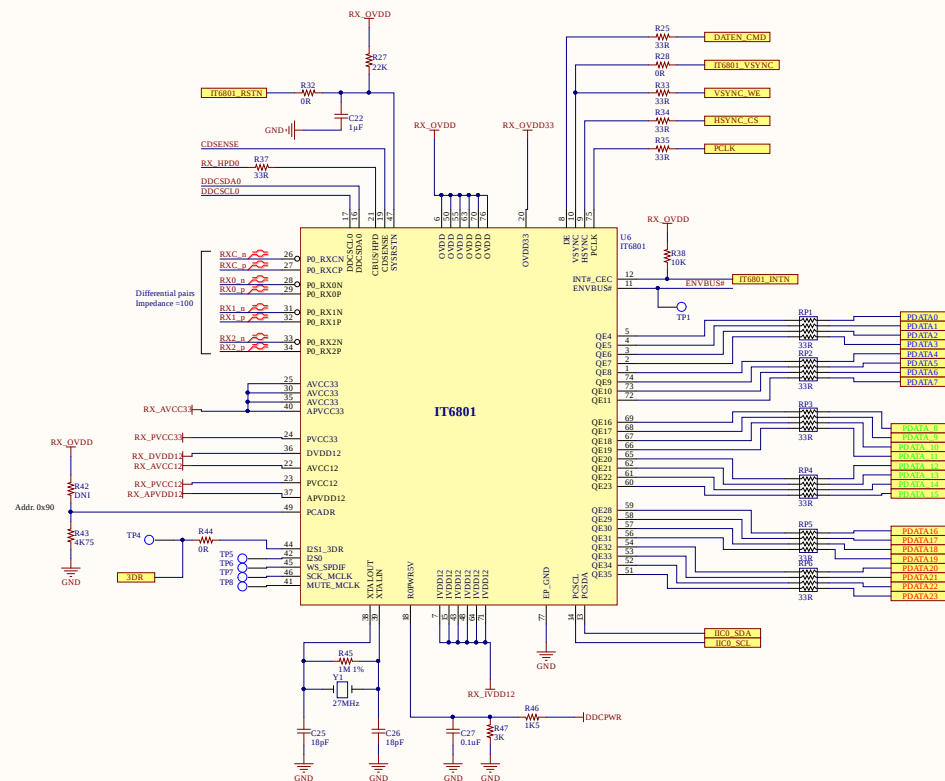
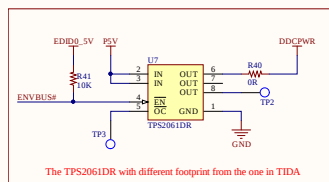
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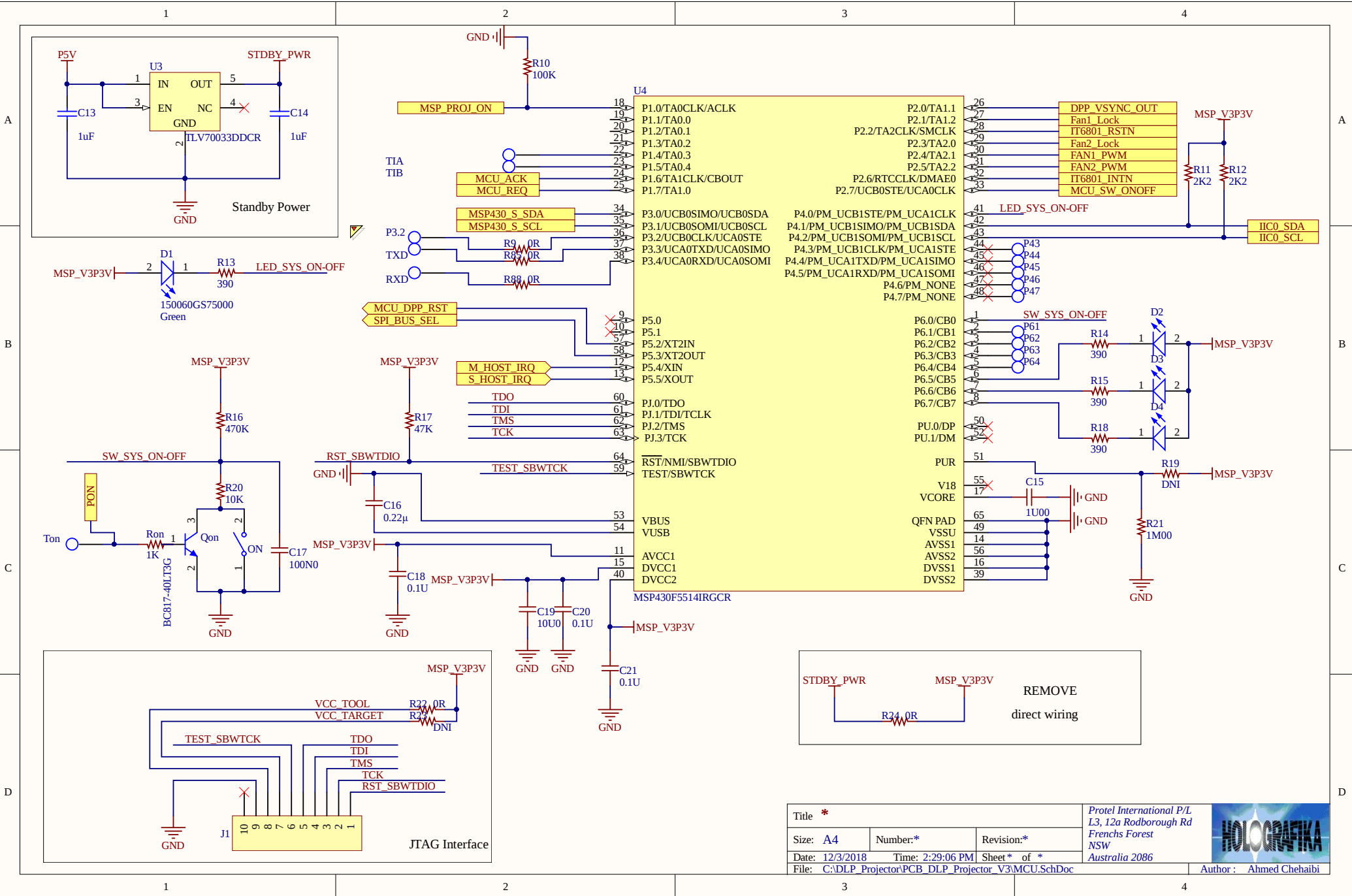
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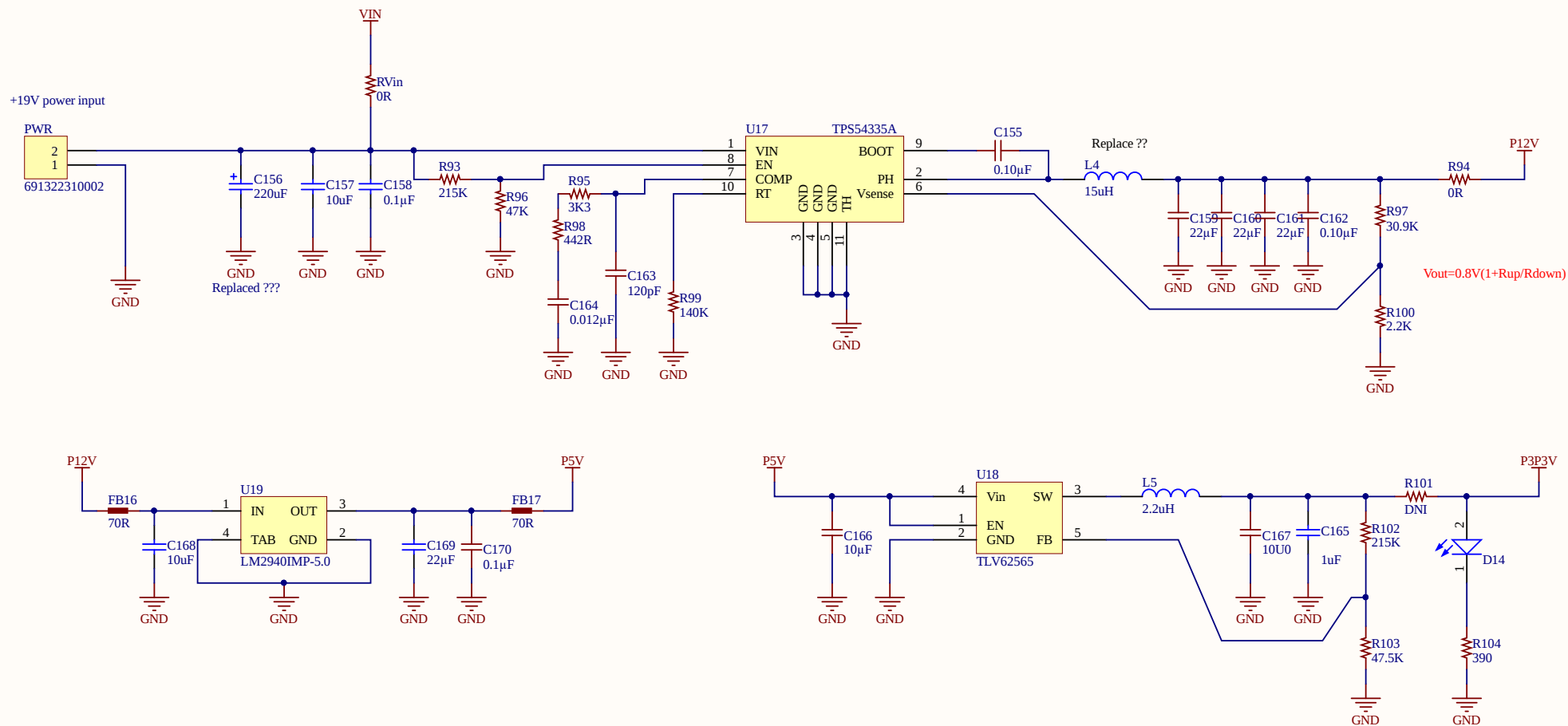
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