
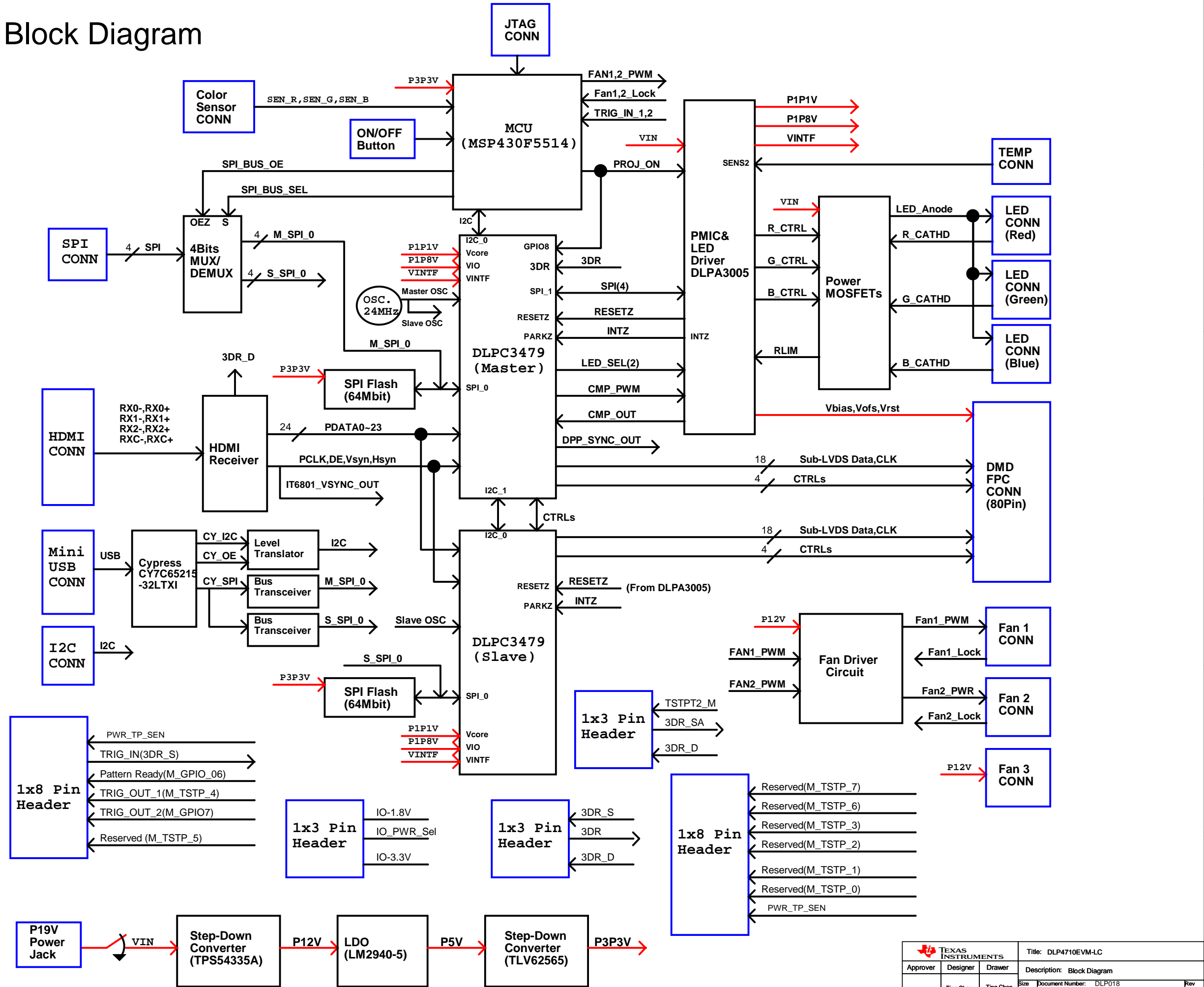


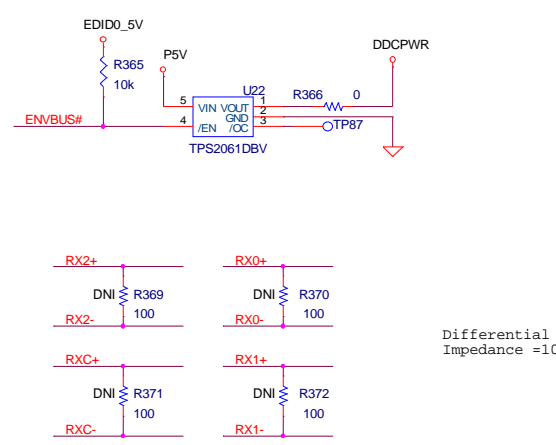
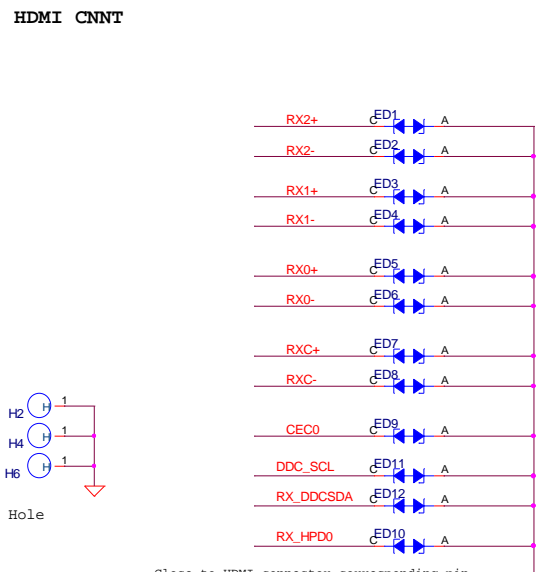
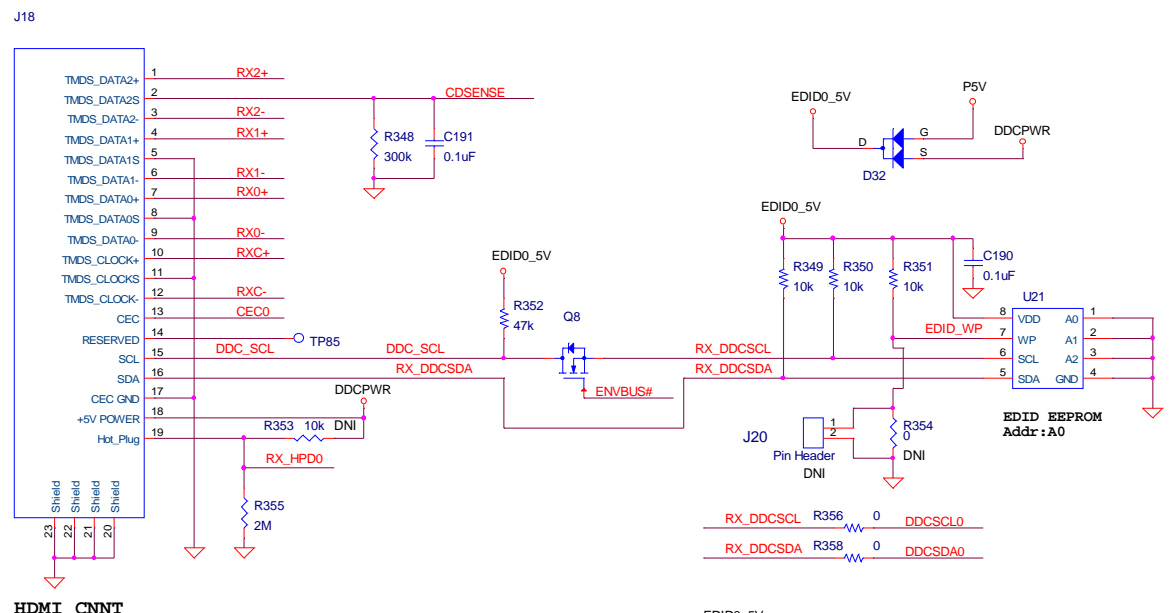
Version	Release Date	History
T0	2018/02/09	<ol style="list-style-type: none"> <li>1. First release.</li> <li>2. Modify Trigger Signals base on DLPDLCR4710EVM-G2 REV B</li> <li>3. The GPIO_5 signal switch to the 3DR pin</li> <li>4. PCB--00.LH908G001 REV A</li> </ol>
A	2018/09/28	<ol style="list-style-type: none"> <li>1. DLPC3479 : GPIO_6 from the Slave has to be connected to the GPIO_5 on the Master</li> <li>2. Add J9 Connector for Sensing function.</li> <li>3. Add U68 for level shit function.</li> </ol>
B	2018/12/18	<ol style="list-style-type: none"> <li>1. Change U68 from SN74AUP1G07DCK to SN74AUP1T17DCK</li> <li>2. U52,U53,U54,U55,U56,U57,U58,U59,U60,U61,U66 will change from SN74AUP1G07DCK to SN74LVC1G07DCK</li> </ol>
B.1	2022/03/18	<ol style="list-style-type: none"> <li>1. Updated Block Diagram description for M_TSTPT5 on header</li> </ol>

 <b>TEXAS INSTRUMENTS</b>			Title: DLP4710EVM-LC		
Approver	Designer	Drawer	Description: History		
Jim C.C	Ting Chan	Ting Chan	Size A	Document Number: DLP018	Rev B.1
Date: Friday, March 18, 2022				Sheet 01 of 12	

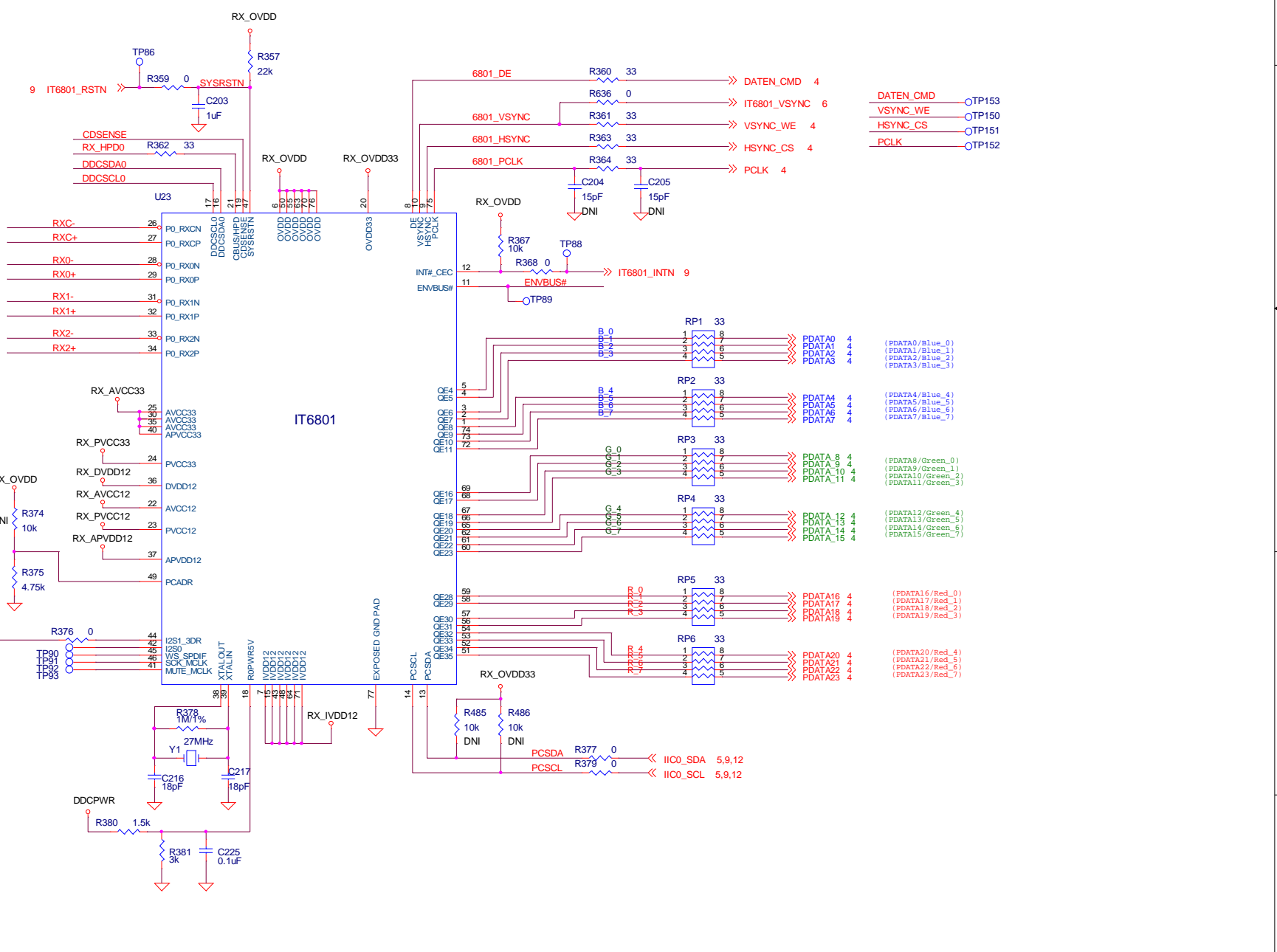
# Block Diagram



<b>TEXAS INSTRUMENTS</b>			Title: DLP4710EVM-LC		
Approver: Jim C.C.	Designer: Ting Chan	Drawer: Ting Chan	Description: Block Diagram		
Size: Custom			Document Number: DLP018		Rev: B.1
Date: Friday, March 18, 2022			Sheet 02 of 12		

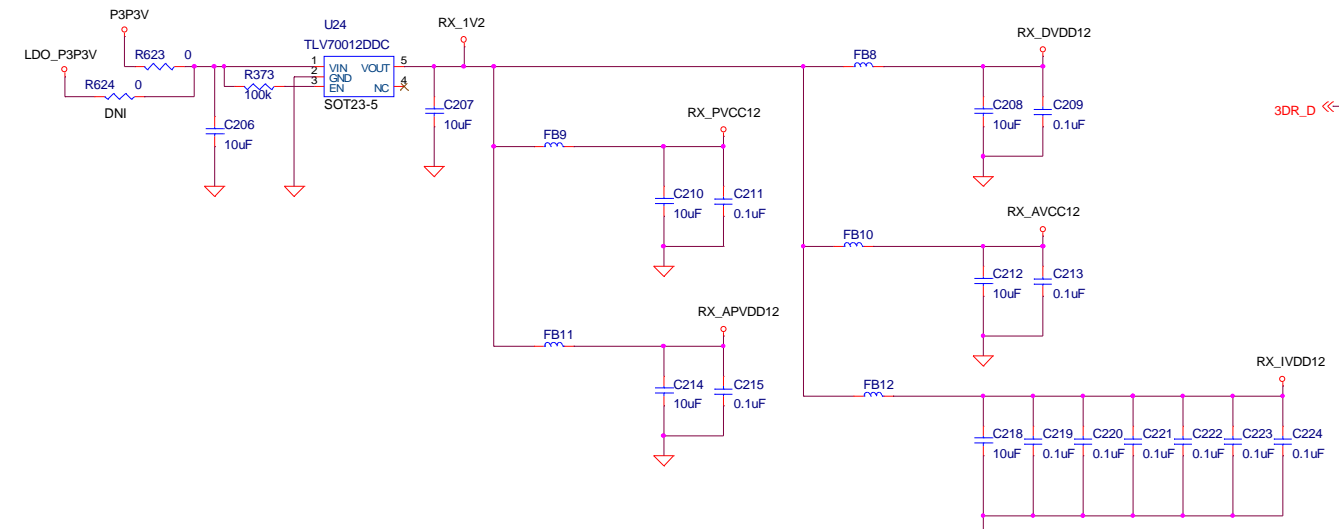


Differential pairs Impedance =100 E1



- DATEN\_CMD >> TP153
- VSYNC\_WE >> TP150
- HSYNC\_CS >> TP151
- PCLK >> TP152

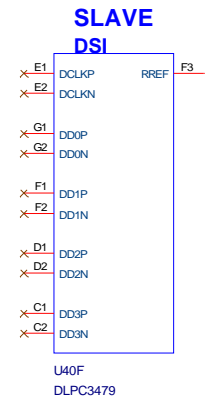
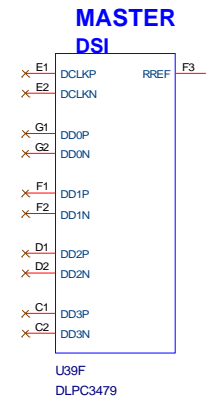
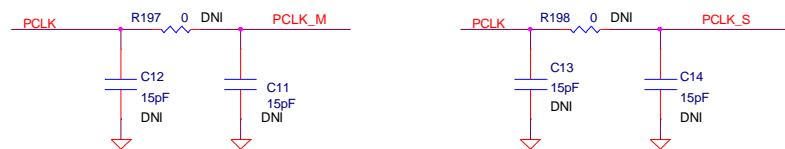
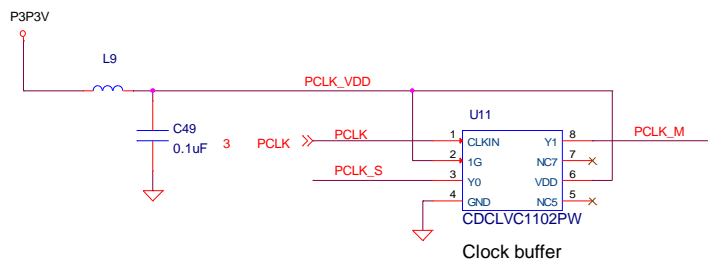
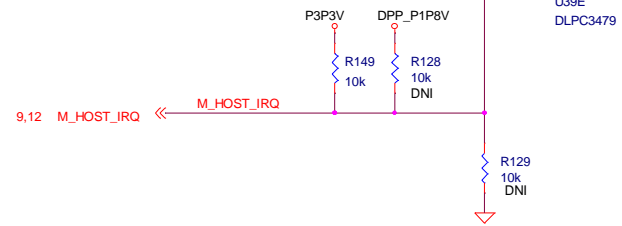
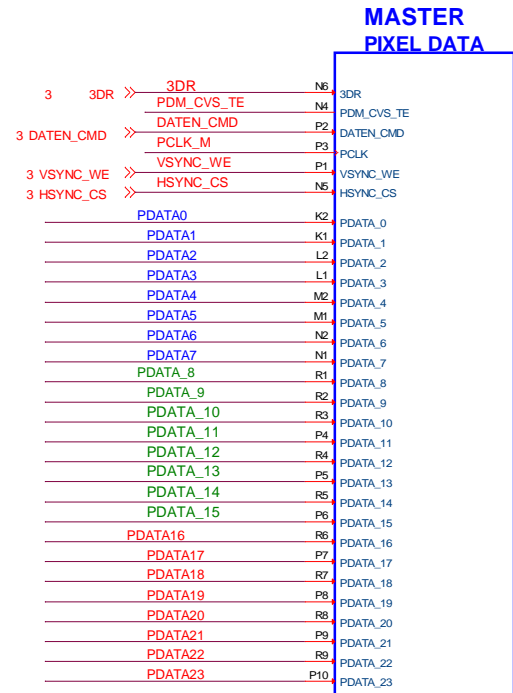
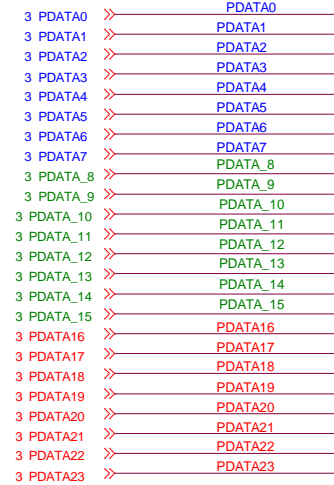
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- (PDATA1/Blue\_1)
- (PDATA2/Blue\_2)
- (PDATA3/Blue\_3)
- (PDATA4/Blue\_4)
- (PDATA5/Blue\_5)
- (PDATA6/Blue\_6)
- (PDATA7/Blue\_7)
- (PDATA8/Green\_0)
- (PDATA9/Green\_1)
- (PDATA10/Green\_2)
- (PDATA11/Green\_3)
- (PDATA12/Green\_4)
- (PDATA13/Green\_5)
- (PDATA14/Green\_6)
- (PDATA15/Green\_7)
- (PDATA16/Red\_0)
- (PDATA17/Red\_1)
- (PDATA18/Red\_2)
- (PDATA19/Red\_3)
- (PDATA20/Red\_4)
- (PDATA21/Red\_5)
- (PDATA22/Red\_6)
- (PDATA23/Red\_7)



			<b>Title:</b> DLP4710EVM-LC	
<b>Approver:</b> Jim C.C.	<b>Designer:</b> Ting Chan	<b>Drawer:</b> Ting Chan	<b>Description:</b> IT6801_HDM	
<b>Size:</b> D			<b>Document Number:</b> DLP4710	<b>Rev:</b> B.1
<b>Date:</b> Friday, March 18, 2022			<b>Sheet:</b> 03 of 12	<b>Page:</b> 1

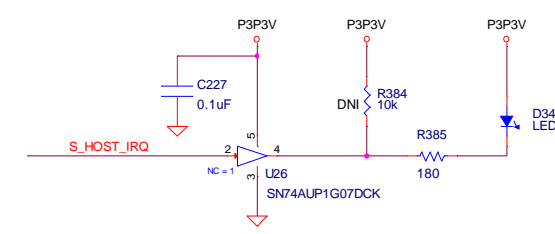
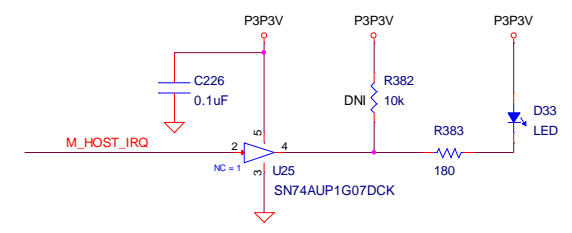
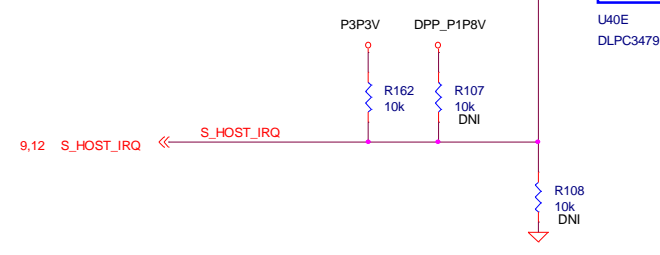
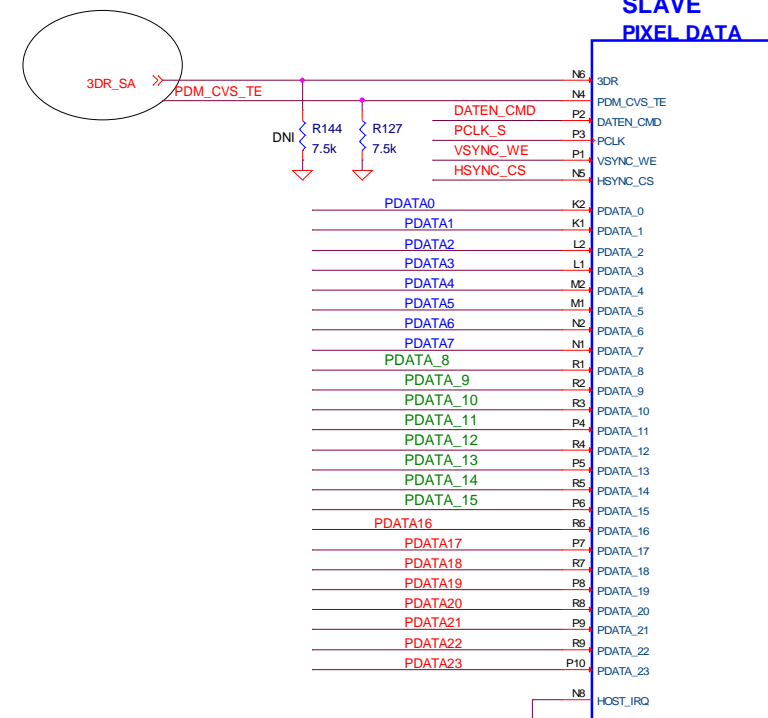
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 (PDATA2/Blue\_2)  
 (PDATA3/Blue\_3)  
 (PDATA4/Blue\_4)  
 (PDATA5/Blue\_5)  
 (PDATA6/Blue\_6)  
 (PDATA7/Blue\_7)  
 (PDATA8/Green\_0)  
 (PDATA9/Green\_1)  
 (PDATA10/Green\_2)  
 (PDATA11/Green\_3)  
 (PDATA12/Green\_4)  
 (PDATA13/Green\_5)  
 (PDATA14/Green\_6)  
 (PDATA15/Green\_7)  
 (PDATA16/Red\_0)  
 (PDATA17/Red\_1)  
 (PDATA18/Red\_2)  
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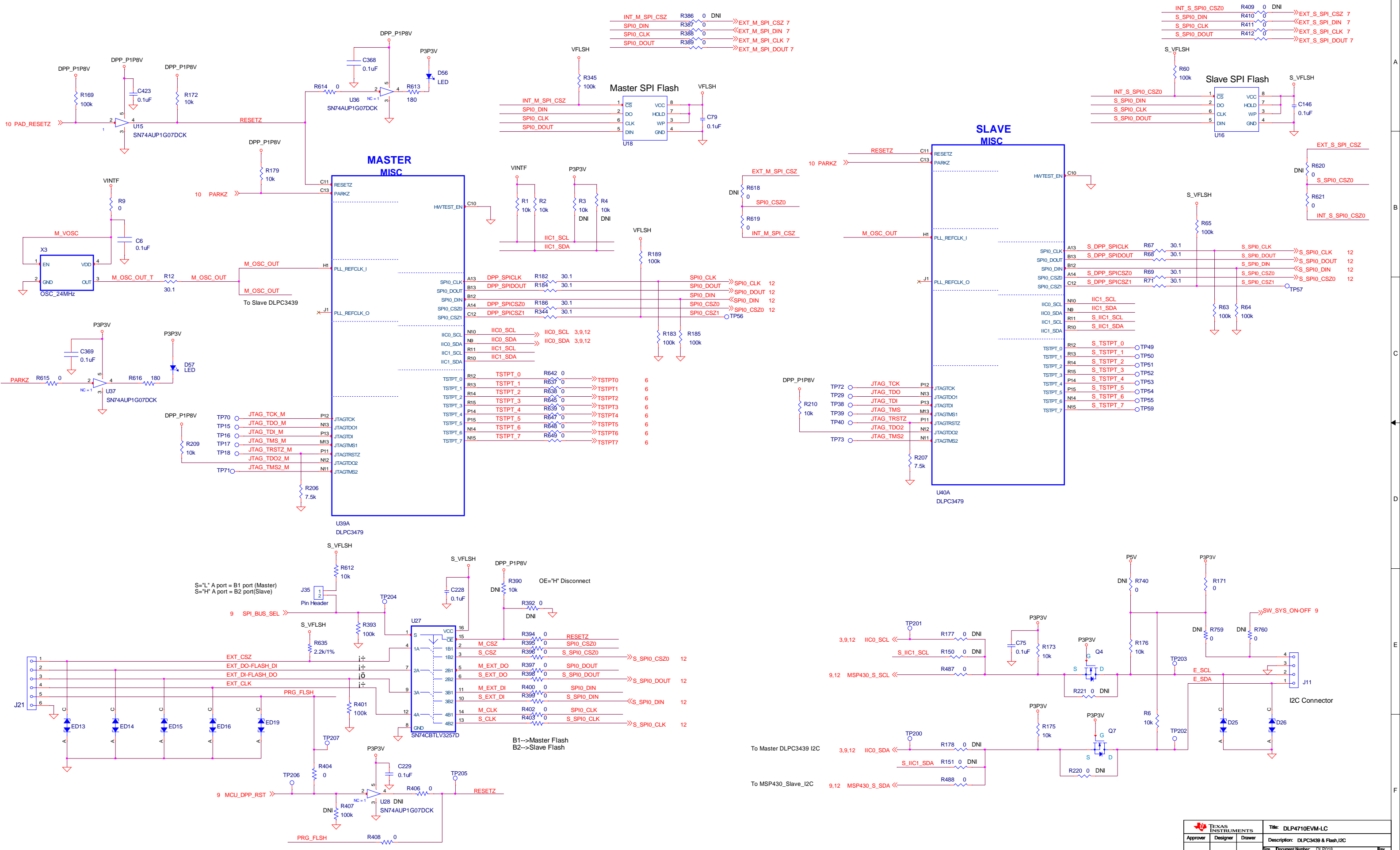
From IT6801  
 HDMI Receiver

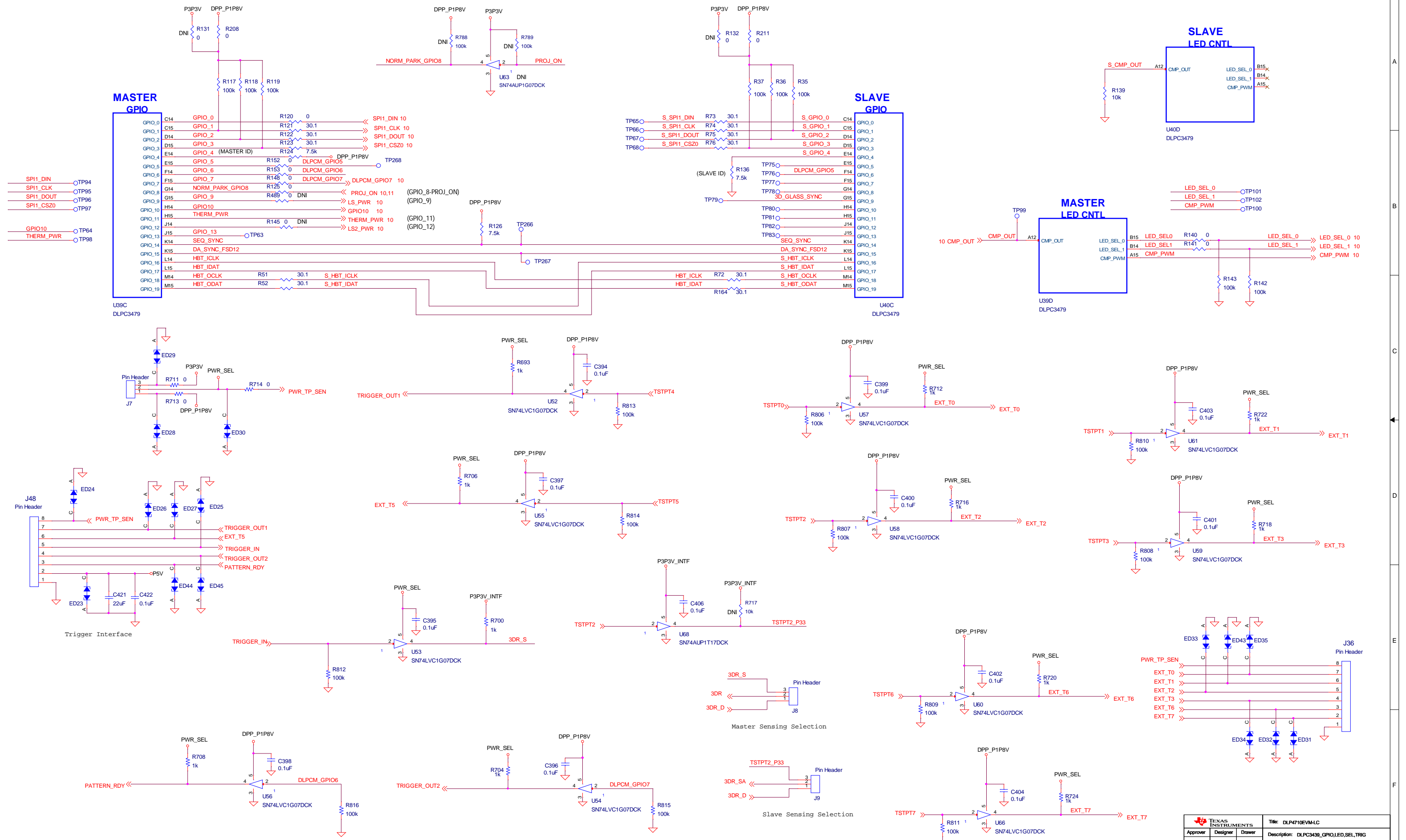


The DSI Port is not used.  
 Inputs are left unconnected per ASIC spec recommendation.

SLAVE  
 PIXEL DATA

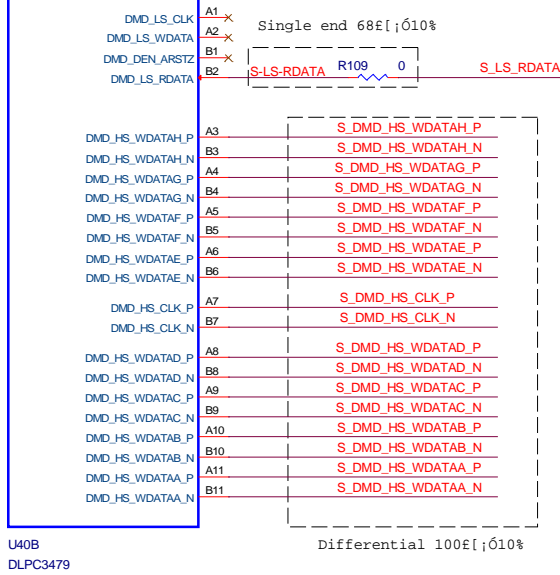




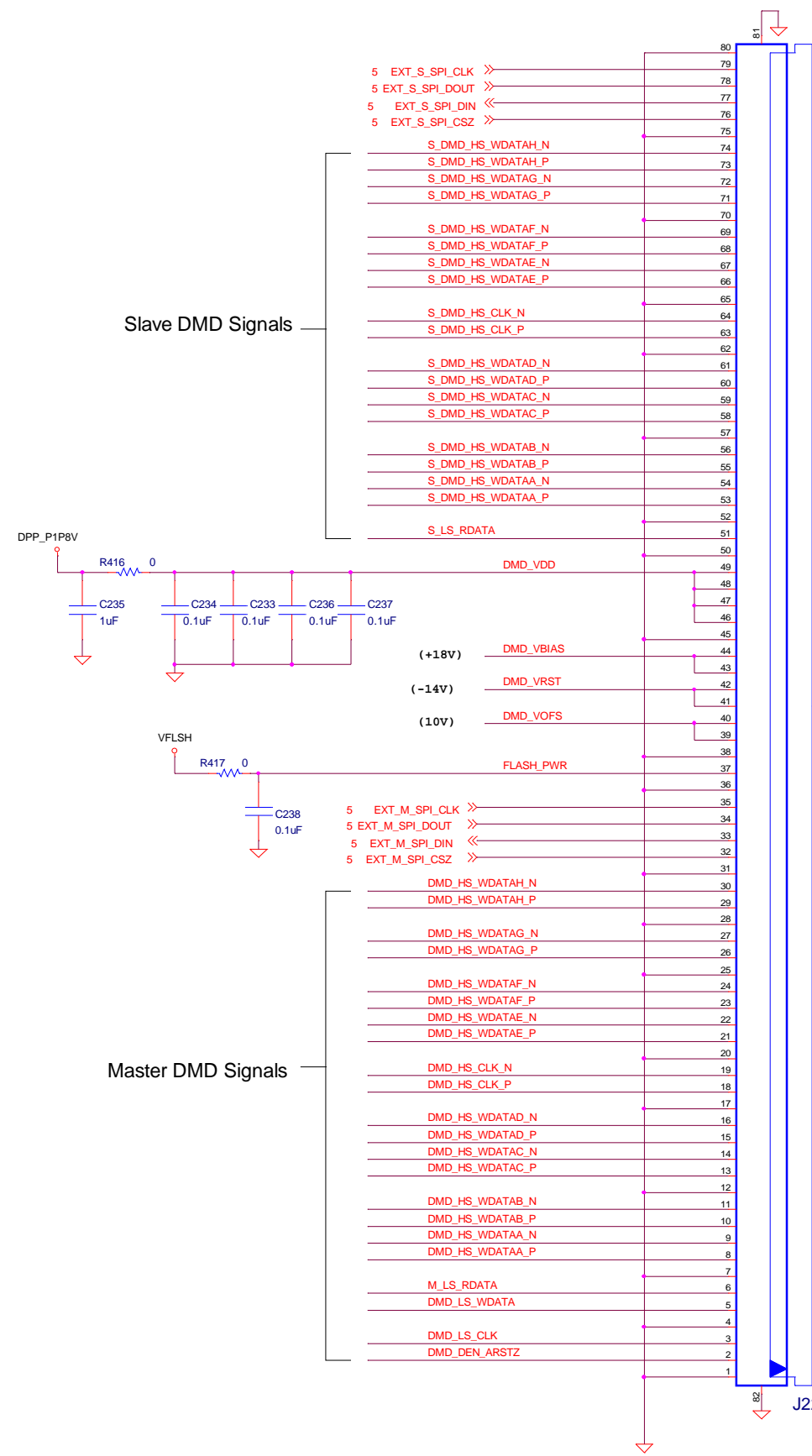


			<b>Title:</b> DLP4710EVM-LC		
<b>Approver:</b> Jim C.C.	<b>Designer:</b> Ting Chan	<b>Drawer:</b> Ting Chan	<b>Description:</b> DLPC3439_GPIO,LED,SEL,TRIG		
<b>Size:</b> D	<b>Document Number:</b> DLPJ018	<b>Rev:</b> B.1	<b>Date:</b> Friday, March 18, 2022		
		<b>Sheet:</b> 06 of 12			

**SLAVE DMD INTERFACE**

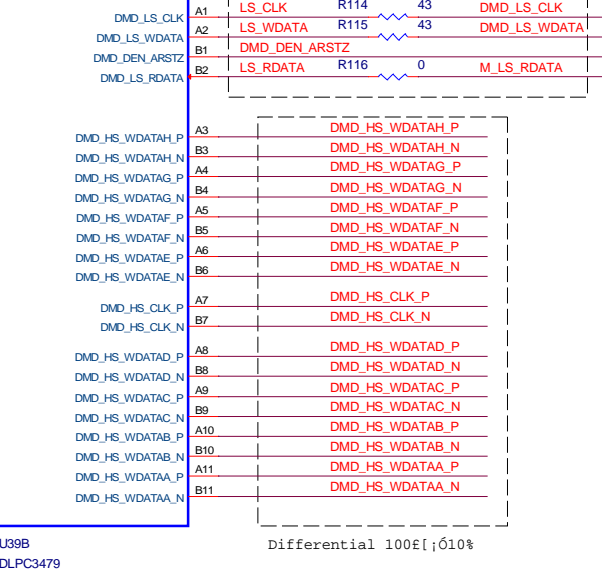


**Slave DMD Signals**

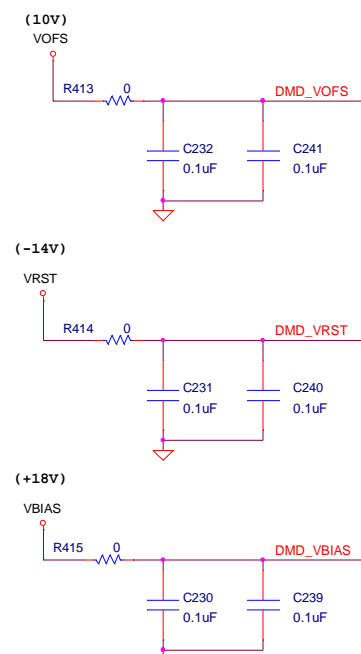
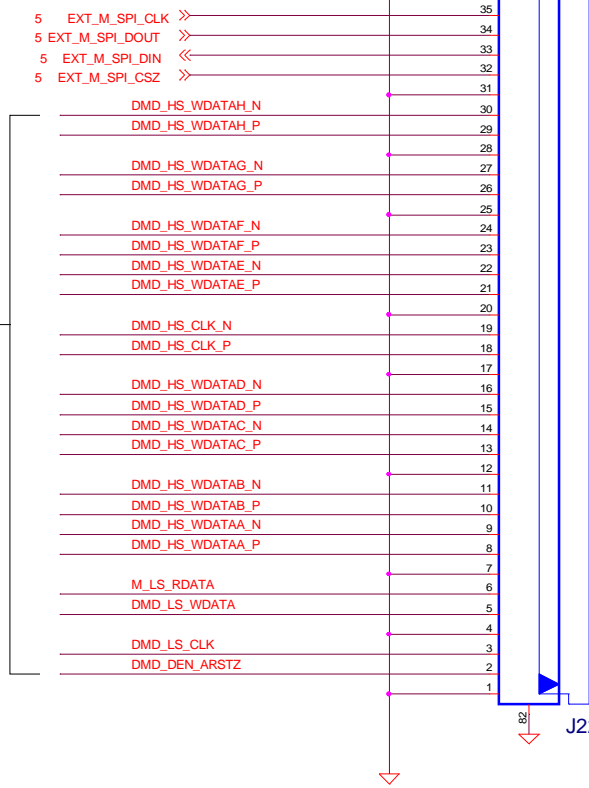


← Cable Insert Direction

**MASTER DMD INTERFACE**



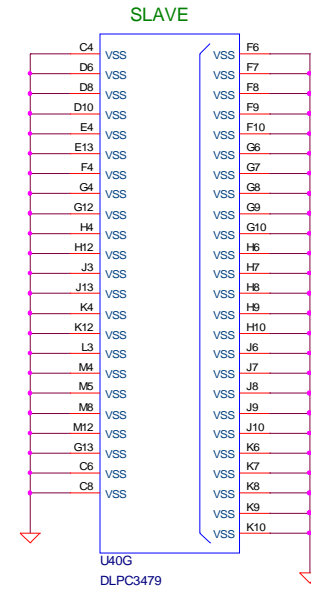
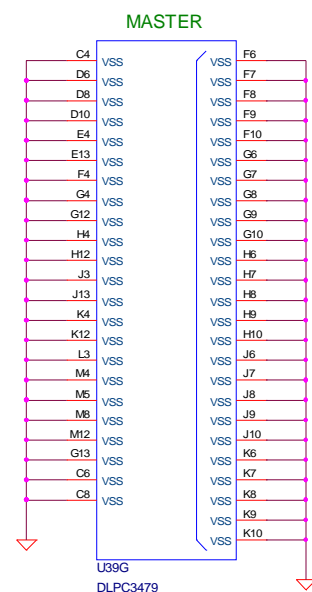
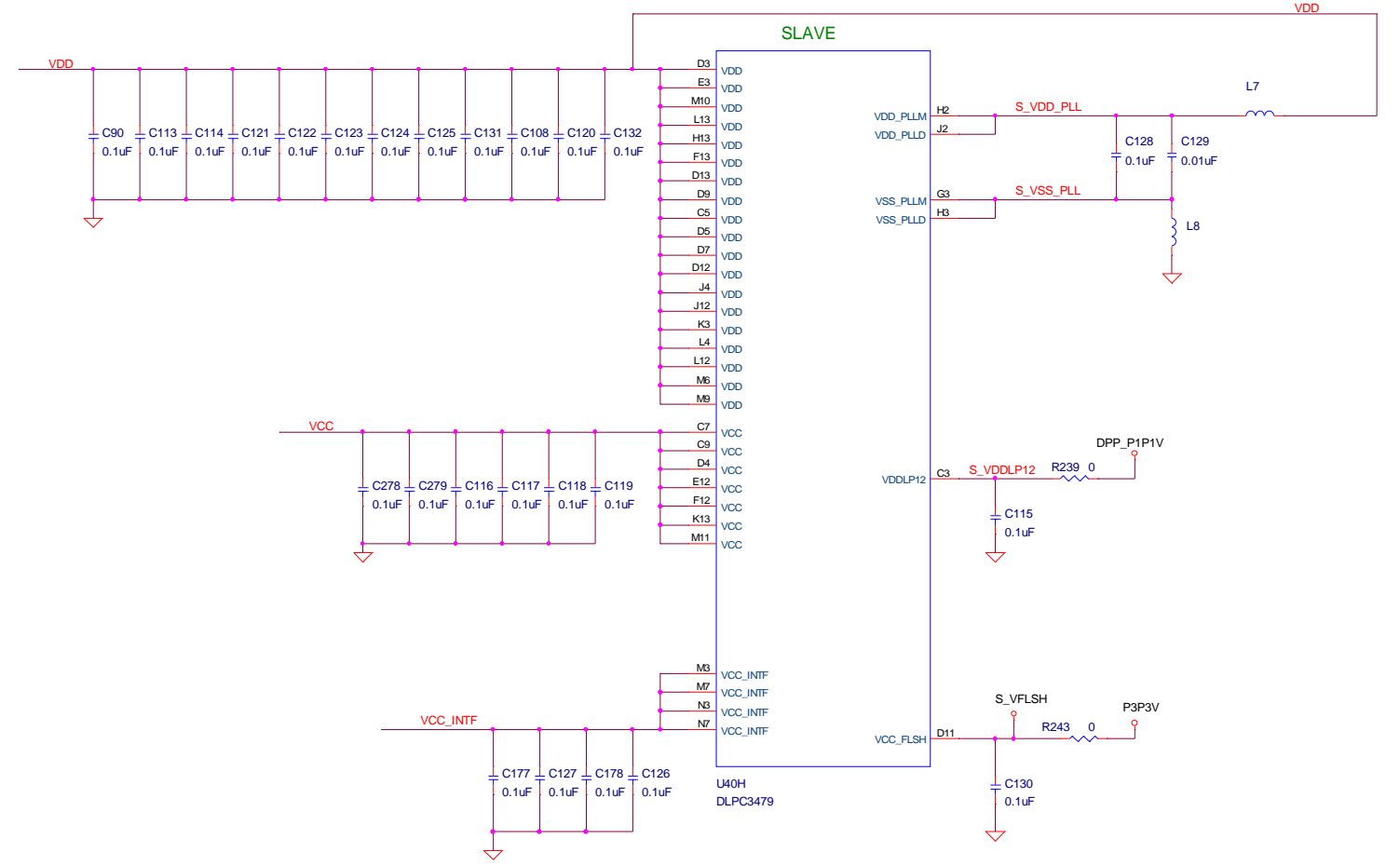
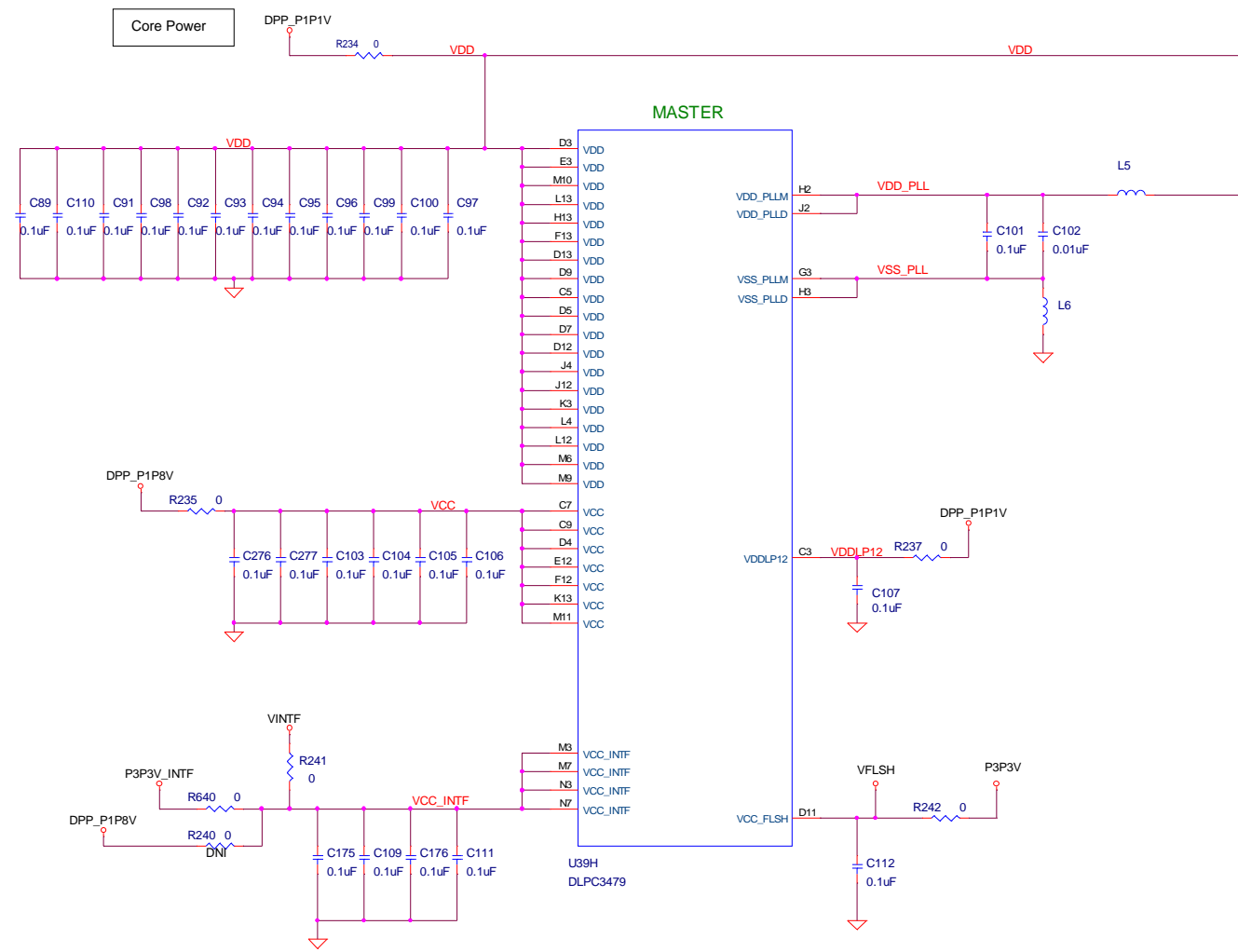
**Master DMD Signals**



**Configuration:**

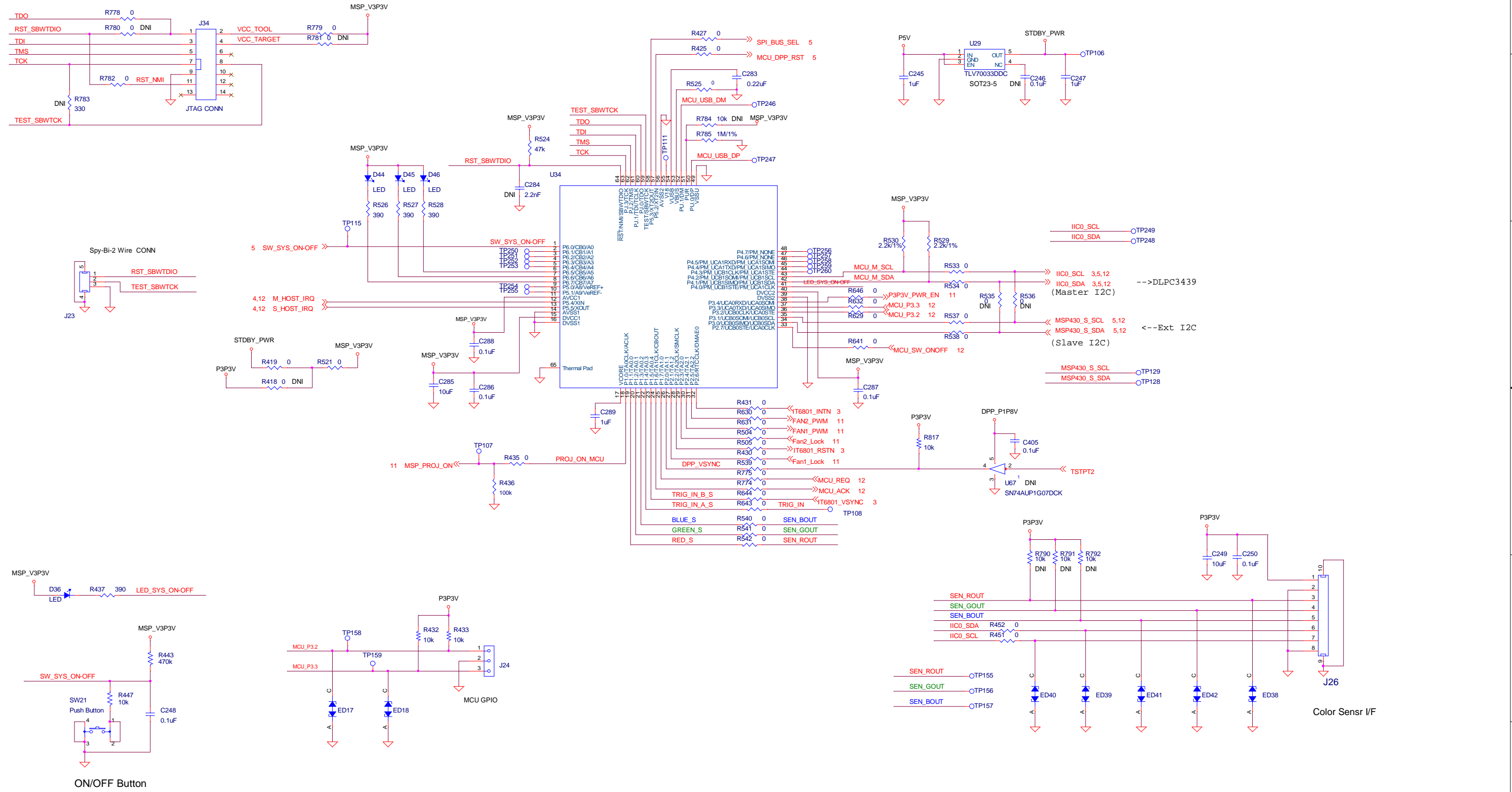
DLPC3479 Side		DMD Side
Option 2 - Master	Option 1 - Slave	
HS_WDATA_E_P	HS_WDATA_D_P	Input DATA_p_0
HS_WDATA_E_N	HS_WDATA_D_N	Input DATA_n_0
HS_WDATA_F_P	HS_WDATA_C_P	Input DATA_p_1
HS_WDATA_F_N	HS_WDATA_C_N	Input DATA_n_1
HS_WDATA_G_P	HS_WDATA_B_P	Input DATA_p_2
HS_WDATA_G_N	HS_WDATA_B_N	Input DATA_n_2
HS_WDATA_H_P	HS_WDATA_A_P	Input DATA_p_3
HS_WDATA_H_N	HS_WDATA_A_N	Input DATA_n_3
HS_WDATA_A_P	HS_WDATA_H_P	Input DATA_p_4
HS_WDATA_A_N	HS_WDATA_H_N	Input DATA_n_4
HS_WDATA_B_P	HS_WDATA_G_P	Input DATA_p_5
HS_WDATA_B_N	HS_WDATA_G_N	Input DATA_n_5
HS_WDATA_C_P	HS_WDATA_F_P	Input DATA_p_6
HS_WDATA_C_N	HS_WDATA_F_N	Input DATA_n_6
HS_WDATA_D_P	HS_WDATA_E_P	Input DATA_p_7
HS_WDATA_D_N	HS_WDATA_E_N	Input DATA_n_7





			<b>Title:</b> DLP4710EVM-LC	
<b>Approver:</b> Jim C.C.	<b>Designer:</b> Ting Chan	<b>Drawer:</b> Ting Chan	<b>Description:</b> DLPC3439_VDD,VSS	
<b>Size:</b> D	<b>Document Number:</b> DLP018	<b>Rev:</b> B.1	<b>Date:</b> Friday, March 18, 2022	
			<b>Sheet:</b> 06	<b>of:</b> 12

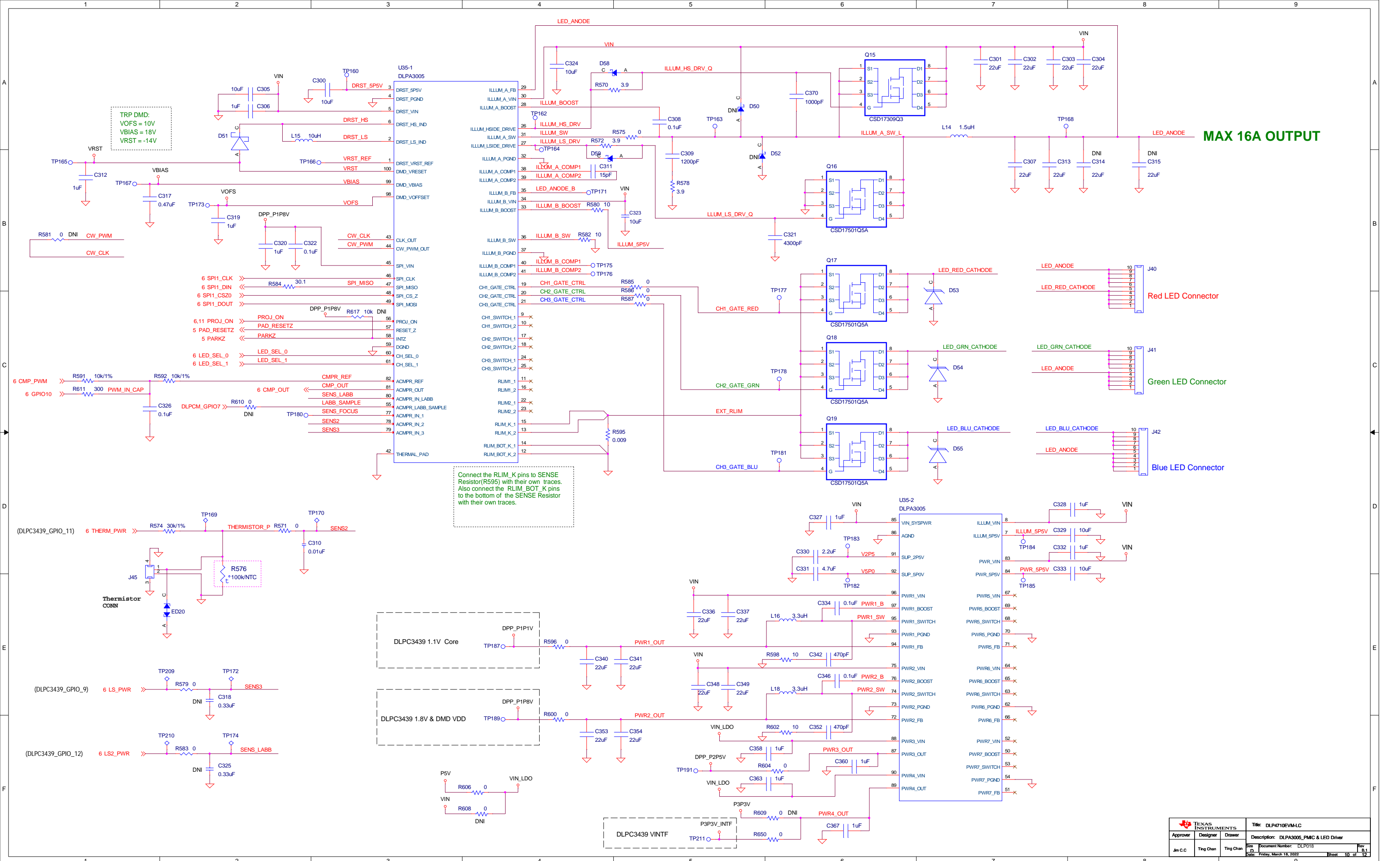




ON/OFF Button

Color Sensr I/F

			<b>Title:</b> DLP4710EVM-LC	
<b>Approver:</b> Jim C.C.	<b>Designer:</b> Ting Chan	<b>Drawer:</b> Ting Chan	<b>Description:</b> MCU_MSP430	
<b>Size:</b> D	<b>Document Number:</b> DLP4710	<b>Date:</b> Friday, March 18, 2022	<b>Sheet:</b> 06 of 12	<b>Rev:</b> B.1

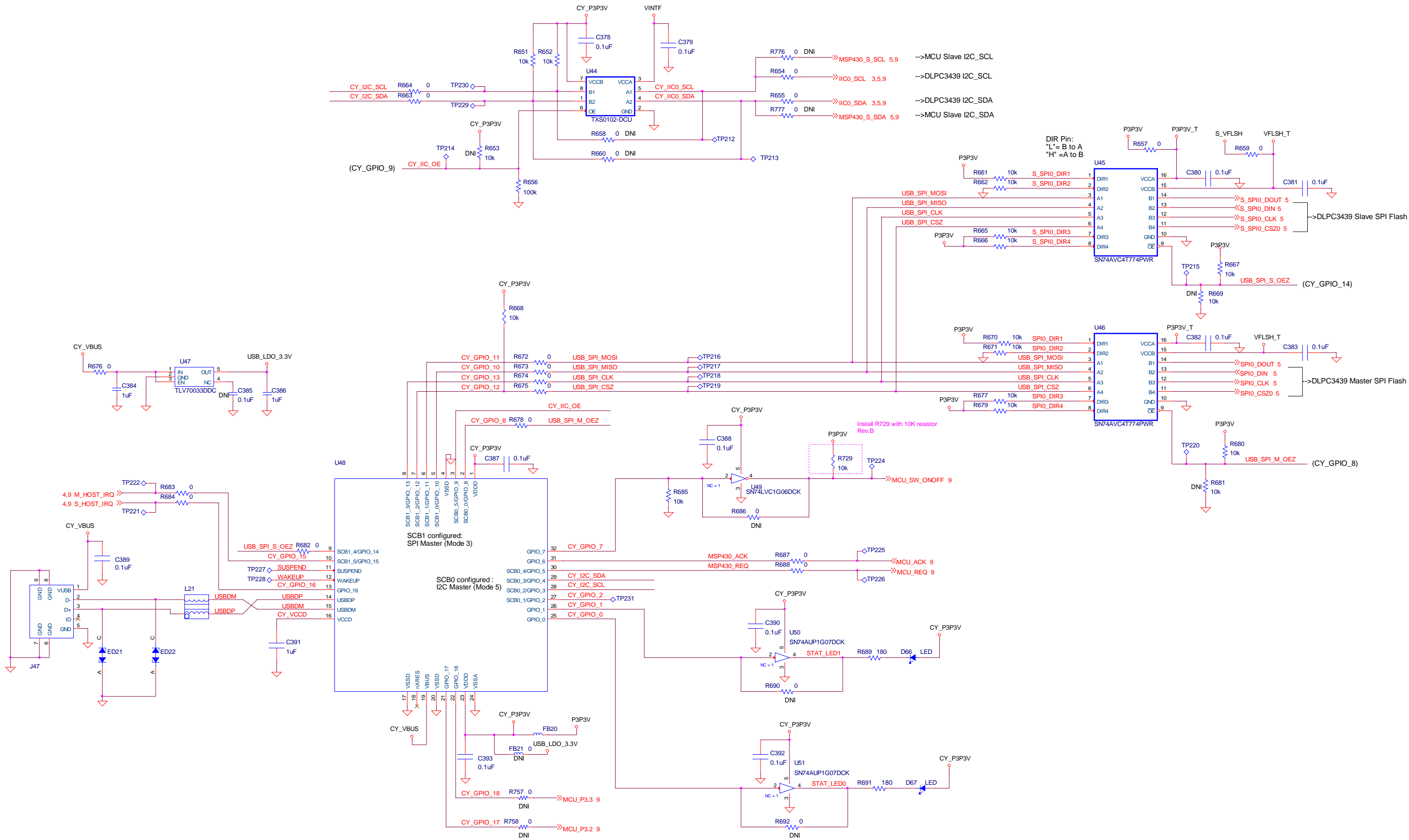


MAX 16A OUTPUT

Connect the RLIM\_K pins to SENSE Resistor (R595) with their own traces. Also connect the RLIM\_BOT\_K pins to the bottom of the SENSE Resistor with their own traces.

			Title: DLP4710EVM-LC	
Approver: Jim C.C.	Designer: Ting Chan	Drawer: Ting Chan	Description: DLP43005_PMC & LED Driver	
Size: D	Document Number: DLP4710	Date: Friday, March 18, 2022	Sheet: 10 of 12	Rev: B.1





			<b>Title:</b> DLP4710EVM-LC	
<b>Approver:</b> Jim C.C.	<b>Designer:</b> Ting Chan	<b>Drawer:</b> Ting Chan	<b>Description:</b> Cypress USB to I2C	
<b>Size:</b> D	<b>Document Number:</b> DLP4710	<b>Date:</b> Friday, March 18, 2022	<b>Sheet:</b> 12 of 12	<b>Rev:</b> B.1