

DLPR LightCrafter(TM) DLPC900 GUI - 5.0.0

Information System Settings Video Mode Pattern Mode Batch Files Peripherals **TEXAS INSTRUMENTS** Firmware

System Controls  
 Connected  Virtual Connects  
 Normal  Standby

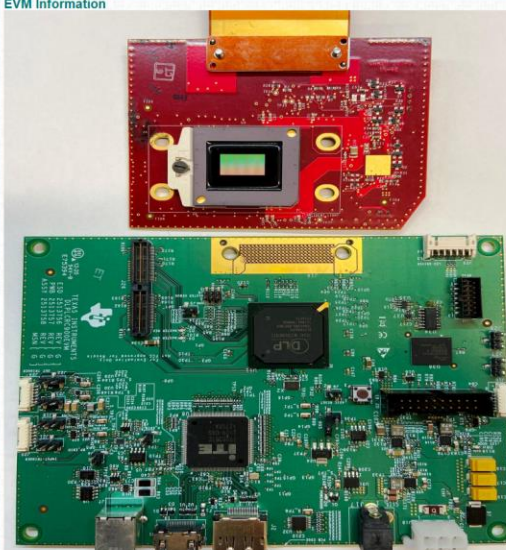
Firmware  
 Version: 6.0.0  
 Tag: dlpc900\_6.0.0\_00B

Chipset Type  
 DLP6500  DLP9000  
 DLP6795  DLP500YX

Operating Mode  
 Video Mode  
 Pre-stored Pattern Mode  
 Video Pattern Mode  
 Pattern On-The-Fly Mode

Status  
 Internal Memory Test Passed  
 Controller/DMD Incompatible  
 Slave Present and Ready  
 Internal Initialization Complete  
 DMD Reset Waveform Controls  
 Forced Swap Error  
 Sequencer Abort Status Flag Error  
 Sequencer Error  
 DMD Micromirrors Parked  
 Sequencer Running  
 Video Running  
 Locked to External Source

### EVM Information



Online Resources  
[DLP6500/DFE DMD Datasheet](#)  
[DLP6500 Tools](#)  
[DLPC900 EVM Users Guide](#)  
[DLPC900 Tools](#)  
[DLPC900 Controller Datasheet](#)  
[DLPC900 Programmers Guide](#)  
[TI DLP Solutions & Services](#)  
**TI E2E™ Community**

Quick Start:  
 1 Connect the EVM to Power  
 2 Connect the EVM to your PC with USB

Start innovating with high resolution, high speed DLP technology!  
 Scan more. Design brighter. Build bigger.

DLPC900  
 TEXAS INSTRUMENTS

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### System Settings

Image Orientation  
 East/West Flip  North/South Flip

Optical Invert  
 Pattern Display Invert Data

LED Control  
 LED Selection:  Automatic  Manual  
 Red  Green  Blue  
 LED Frequency: Frequency  Hz

LED Current(0-255)  
 Red   
 Blue   
 Green   
 Invert PWM

DMD Park  
 DMD Park

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 Tag: dip6500\_6.0.0\_008

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**Video Mode**

Source Settings

Input Configuration

Source Select  
 Parallel RGB  
 30 BITS

Input Port Data Swap  
 Port 1  
 ABC->ABC

Pixel Data Format  
 RGB 444

Parallel Port Configuration  
 Total Pixels Per Line  
 Total Lines Per Frame  
 Active Pixels Per Line  
 Active Lines Per Frame  
 Active Area First Pixel  
 Active Area First Line  
 Bottom Field First Line  
 Pixel Clock (KHz)

Port Config Controls

Pixel Mode Single Pixel  
 Data Port Port 1 Pixel Clock Clock 1  
 Data Enable Port 1 VSync & HSync Port 1

IT6535 Receiver  
 Power Down  HDMI  Display Port

Display Settings

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**Video Mode**

Source Settings

Internal Test Pattern Color

	Red	Green	Blue
Foreground	0	0	0
Background	0	0	0

Display Dimensions

	Display Area	Cropped Area
Start Pixel	0	0
Pixels Per Line	0	0
Start Line	0	0
Lines Per Frame	0	0

De-Gamma LUT Control

Enable  
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Display Settings

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