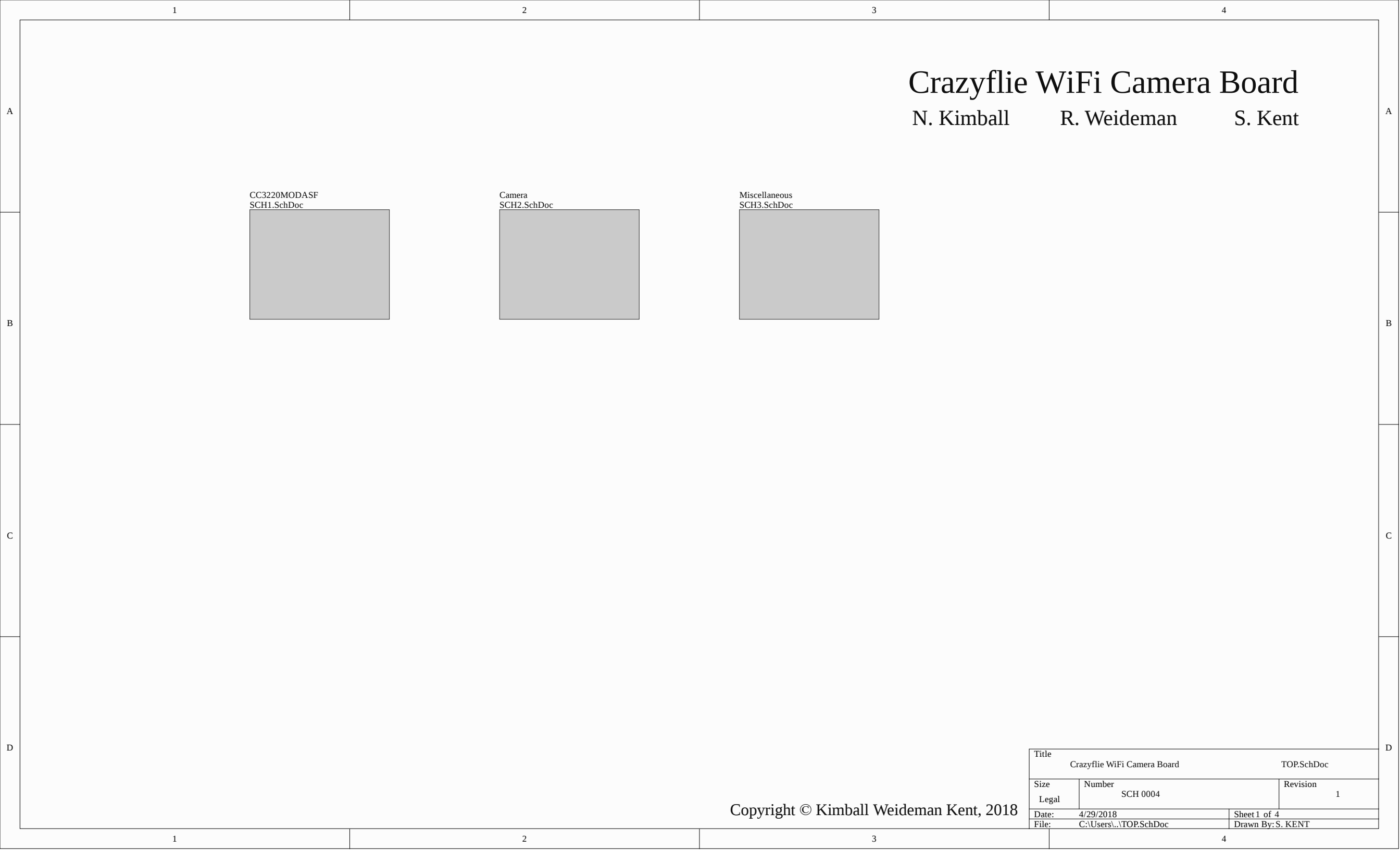
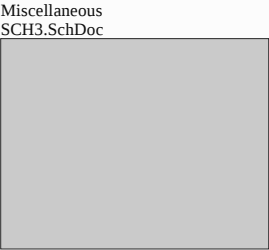
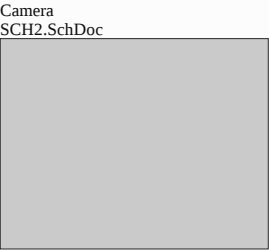
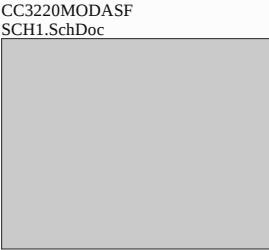




Crazyflie WiFi Camera Board

N. Kimball R. Weideman S. Kent



Title		Crazyflie WiFi Camera Board		TOP.SchDoc	
Size	Legal	Number		SCH 0004	
				Revision	
				1	
Date:		4/29/2018		Sheet 1 of 4	
File:		C:\Users\...\TOP.SchDoc		Drawn By: S. KENT	

SoP - Sense-on-Power - determines the Boot Mode that the board will start in. See section 6.9.1 of the CC3220MODASF Datasheet.

SOP2 - Load code through UART.
SOP1 - Load code through UART or 4-pin JTAG.
SOP0 - Load code through 2-pin SWD
None - Load code through 4-pin JTAG.

SOP0 and SOP1 - restore the factory default image

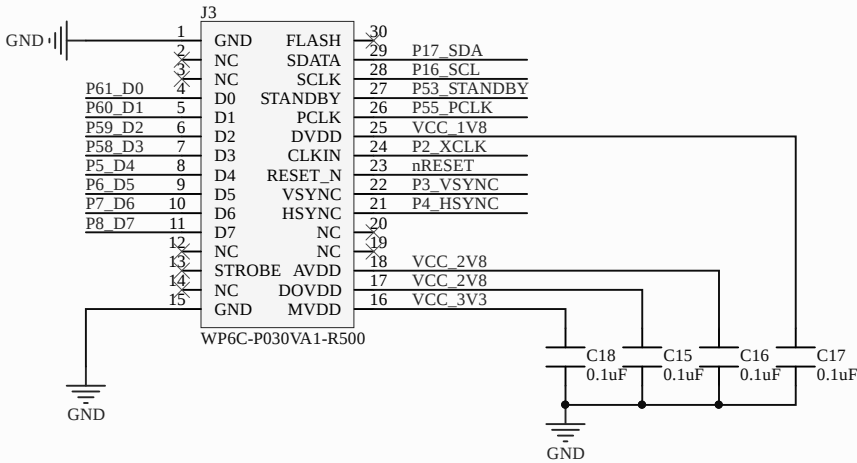
nRESET also controls the camera module.

P50_STANDBY should be driven LOW for normal operation
P57_CAMERA_EN driven HIGH powers the camera.

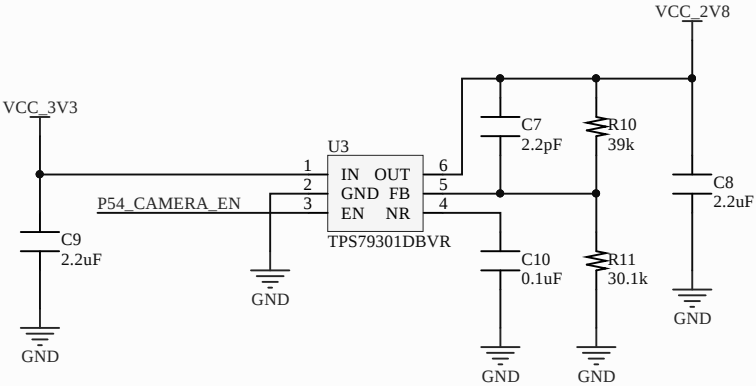
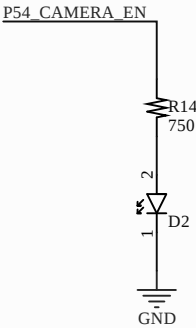
Headers to the Bitcraze Crazyflie

CC3220MODASF

Title		Crazyflie WiFi Camera Board		SCH1.SchDoc	
Size	Legal	Number	SCH 0004		Revision
			1		
Date:	4/29/2018			Sheet2 of 4	
File:	C:\Users\...\SCH1.SchDoc			Drawn By: S. KENT	



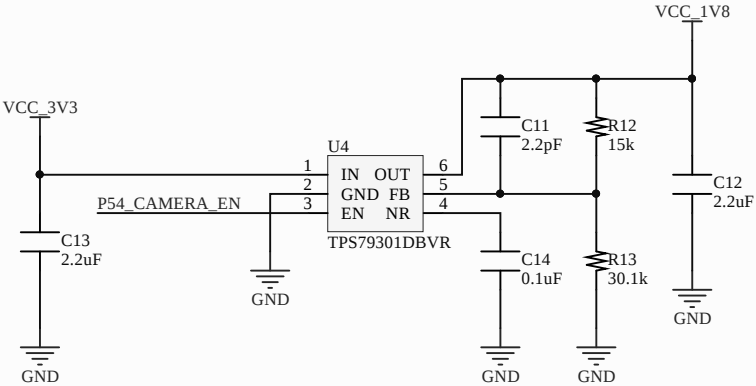
Place Near Camera Connector



Vout ~= 2.8113V

$$V_{out} = V_{ref} * (1 + R_{top}/R_{bot})$$

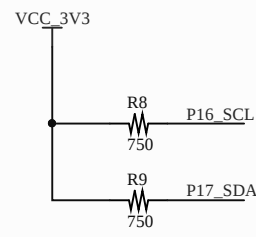
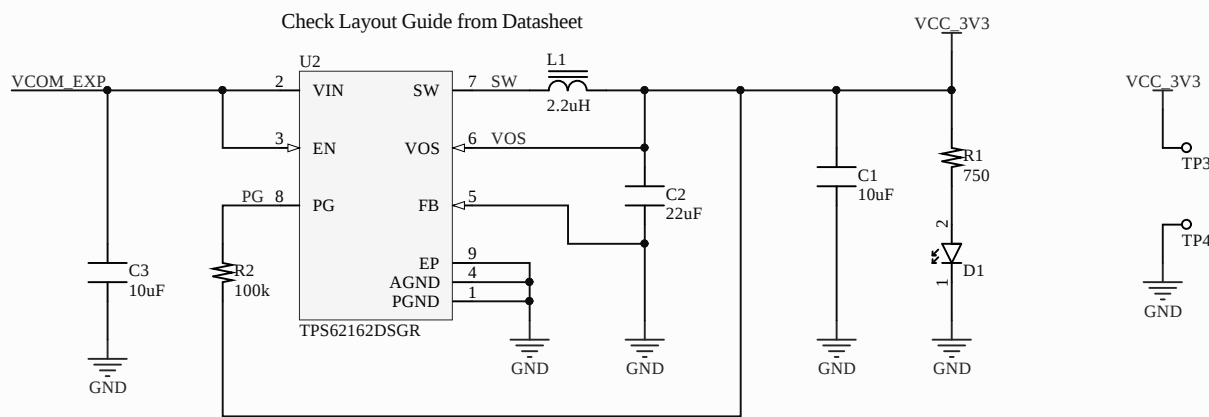
$$V_{ref} = 1.2246V \text{ (internal voltage)}$$



Vout ~= 1.8349V

Camera

Title		Crazyflie WiFi Camera Board		SCH2.SchDoc	
Size	Legal	Number	SCH 0004		Revision
Date:		4/29/2018		Sheet 3 of 4	
File:		C:\Users\...\SCH2.SchDoc		Drawn By: S. KENT	



Miscellaneous

Title		Crazyflie WiFi Camera Board		SCH3.SchDoc	
Size	Legal	Number	SCH 0004		Revision
			1		
Date:	4/29/2018			Sheet4 of 4	
File:	C:\Users\...\SCH3.SchDoc			Drawn By: S. KENT	