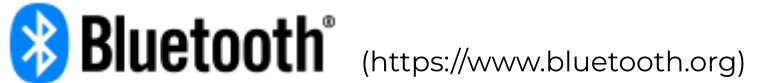


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ICS Details



Consistency Check ▾

<p>Core Specification</p> <p>4.0HCI (4.0 Host Controller Interface)</p> <p>BB (Baseband Conformance)</p> <p>HCI (Host Controller Interface)</p> <p>LL (Low Energy Link Layer)</p> <p>LMP (Link Manager)</p> <p>PROD (Product Type)</p> <p>RF (Radio)</p> <p>RFPHY (Low Energy RF PHY)</p> <p>SUM ICS (Summary ICS)</p> <p>GATT Profiles and Services</p> <p>LE Audio Specifications</p> <p>Mesh Specifications</p> <p>Traditional Profiles</p>	<p>Radio</p> <p>Capability Statement</p> <p>Table 1: RF Capabilities</p> <table border="1"> <thead> <tr> <th>Support</th> <th>Item</th> <th>Capability</th> <th>Reference</th> <th>Status</th> <th>Comment</th> </tr> </thead> <tbody> <tr> <td><input checked="" type="checkbox"/></td> <td>1/1</td> <td>Power Class 1</td> <td>[1] 3</td> <td>C.5</td> <td>C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.</td> </tr> <tr> <td><input type="checkbox"/></td> <td>1/2</td> <td>Power Class 2</td> <td>[1] 3</td> <td>C.5</td> <td>C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.</td> </tr> <tr> <td><input type="checkbox"/></td> <td>1/3</td> <td>Power Class 3</td> <td>[1] 3</td> <td>C.5</td> <td>C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.</td> </tr> <tr> <td><input checked="" type="checkbox"/></td> <td>1/4</td> <td>Power Control</td> <td>[1] 3</td> <td>C.1</td> <td>C.1: Mandatory IF RF 1/1 "Power Class 1" is supported; Optional IF RF 1/2 "Power Class 2" OR RF 1/3 "Power Class 3" is supported, otherwise Excluded.</td> </tr> <tr> <td><input checked="" type="checkbox"/></td> <td>1/5</td> <td>1-slot packets supported</td> <td>[2] 6.5</td> <td>M</td> <td></td> </tr> </tbody> </table>	Support	Item	Capability	Reference	Status	Comment	<input checked="" type="checkbox"/>	1/1	Power Class 1	[1] 3	C.5	C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.	<input type="checkbox"/>	1/2	Power Class 2	[1] 3	C.5	C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.	<input type="checkbox"/>	1/3	Power Class 3	[1] 3	C.5	C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.	<input checked="" type="checkbox"/>	1/4	Power Control	[1] 3	C.1	C.1: Mandatory IF RF 1/1 "Power Class 1" is supported; Optional IF RF 1/2 "Power Class 2" OR RF 1/3 "Power Class 3" is supported, otherwise Excluded.	<input checked="" type="checkbox"/>	1/5	1-slot packets supported	[2] 6.5	M	
Support	Item	Capability	Reference	Status	Comment																																
<input checked="" type="checkbox"/>	1/1	Power Class 1	[1] 3	C.5	C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.																																
<input type="checkbox"/>	1/2	Power Class 2	[1] 3	C.5	C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.																																
<input type="checkbox"/>	1/3	Power Class 3	[1] 3	C.5	C.5: At least one of RF 1/1 "Power Class 1" OR RF 1/2 "Power Class 2" OR 1/3 "Power Class 3" shall be supported.																																
<input checked="" type="checkbox"/>	1/4	Power Control	[1] 3	C.1	C.1: Mandatory IF RF 1/1 "Power Class 1" is supported; Optional IF RF 1/2 "Power Class 2" OR RF 1/3 "Power Class 3" is supported, otherwise Excluded.																																
<input checked="" type="checkbox"/>	1/5	1-slot packets supported	[2] 6.5	M																																	

<input checked="" type="checkbox"/>	1/6	3-slot packets supported	[2] 6.5	O	
<input checked="" type="checkbox"/>	1/7	5-slot packets supported	[2] 6.5	O	
<input checked="" type="checkbox"/>	1/8	79 Channels	[1] 2	M	
<input checked="" type="checkbox"/>	1/9	Support for GFSK modulation	[1] 3.1	M	
<input checked="" type="checkbox"/>	1/10	Support for pi/4-DQPSK modulation	[1] 3.2	C.2	C.2: Mandatory IF (SUM ICS 22/1 "EDR for asynchronous transports (single slot)" OR SUM ICS 22/2 "EDR for asynchronous transports (multi-slot)" OR SUM ICS 22/3 "EDR for synchronous transports" OR SUM ICS 22/4 "EDR for synchronous transports") is supported, otherwise Excluded.
<input checked="" type="checkbox"/>	1/11	Support for 8DPSK modulation	[1] 3.2	C.3	C.3: Mandatory IF (SUM ICS 22/1 "EDR for asynchronous transports (single slot)" OR SUM ICS 22/2 "EDR for asynchronous transports (multi-slot) OR SUM ICS 22/3 "EDR for synchronous transports") is supported; Optional IF (SUM ICS 22/4 "EDR for synchronous transports") is supported, otherwise Excluded.
<input checked="" type="checkbox"/>	1/12	Enhanced Power Control	[1] 3	C.4	C.4: Optional IF SUM_ICCS, 21/8 "Core Specification 3.0" or later AND RF 1/4 "Power Control" is supported, otherwise Excluded.

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