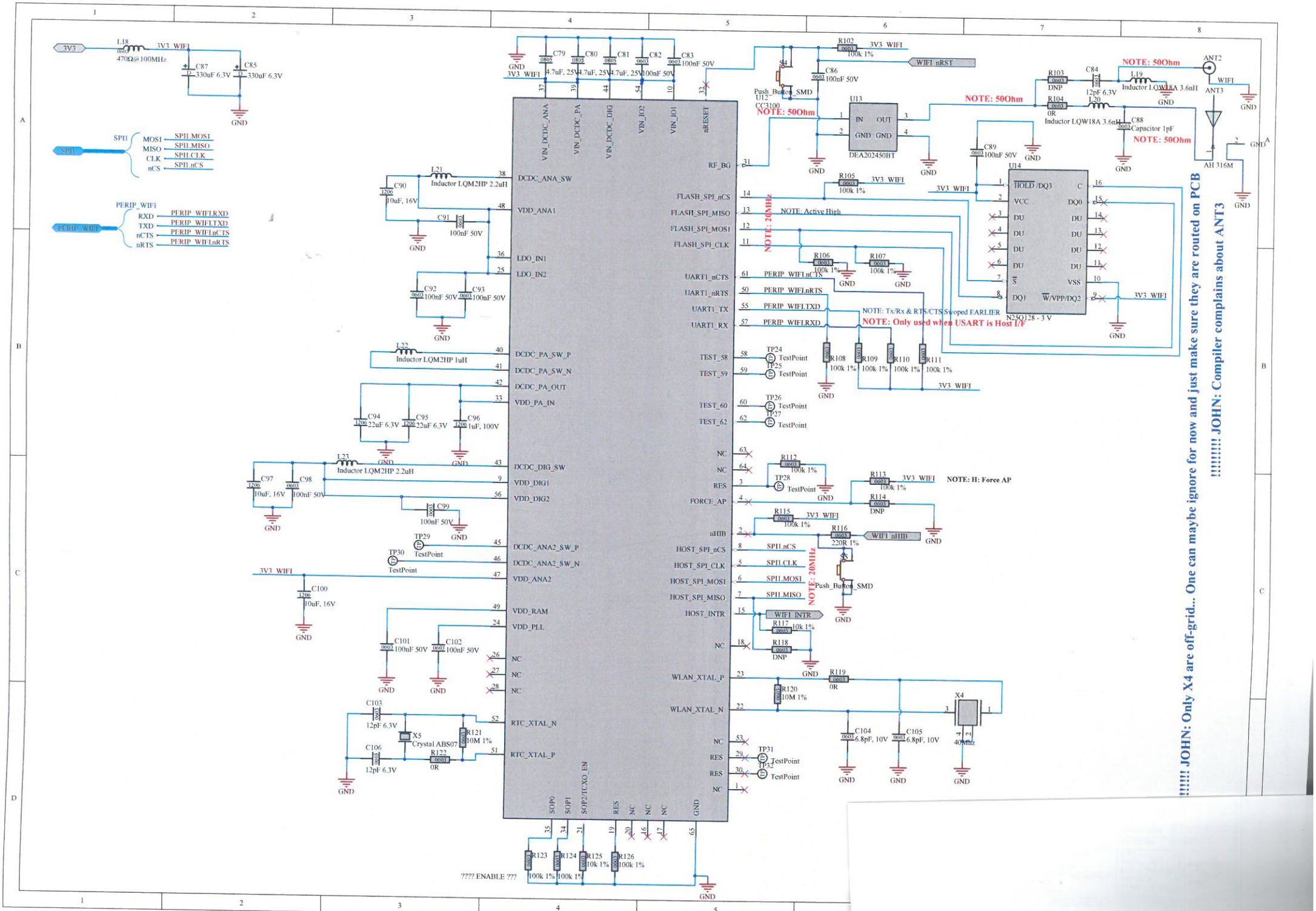

3V3
L18
470Ω@100MHz
3V3_WIFI
C87
330uF 6.3V
C85
330uF 6.3V
GND
SPI1
MOSI
MISO
CLK
nCS
SPI1_MOSI
SPI1_MISO
SPI1_CLK
SPI1_nCS
PERIP_WIFI
RXD
TXD
nCTS
nRTS
PERIP_WIFI_RXD
PERIP_WIFI_TXD
PERIP_WIFI_nCTS
PERIP_WIFI_nRTS
C79 4.7uF, 25V
C80 4.7uF, 25V
C81 4.7uF, 25V
C82 100nF 50V
C83 100nF 50V
VIN_DCDC_ANA
VIN_DCDC_PA
VIN_DCDC_DIG
VIN_IO2
VIN_IO1
nRESET
L21 Inductor LQM2HP 2.2uH
C90 10uF, 16V
C91 100nF 50V
DCDC_ANA_SW
VDD_ANA1
LDO_IN1
LDO_IN2
C92 100nF 50V
C93 100nF 50V
L22 Inductor LQM2HP 1uH
DCDC_PA_SW_P
DCDC_PA_SW_N
DCDC_PA_OUT
VDD_PA_IN
C94 22uF 6.3V
C95 22uF 6.3V
C96 1uF, 100V
L23 Inductor LQM2HP 2.2uH
C97 10uF, 16V
C98 100nF 50V
C99 100nF 50V
DCDC_DIG_SW
VDD_DIG1
VDD_DIG2
TP29 TestPoint
TP30 TestPoint
DCDC_ANA2_SW_P
DCDC_ANA2_SW_N
VDD_ANA2
C100 10uF, 16V
VDD_RAM
VDD_PLL
C101 100nF 50V
C102 100nF 50V
NC
NC
NC
C103 12pF 6.3V
X5 Crystal ABS07
C106 12pF 6.3V
R121 10M 1%
R122 0R
RTC_XTAL_N
RTC_XTAL_P
SOP0
SOP1
SOP2/TCXO_EN
RES
NC
NC
NC
GND
R123 100k 1%
R124 100k 1%
R125 10k 1%
R126 100k 1%
???? ENABLE ???
RF_BG
FLASH_SPI_nCS
FLASH_SPI_MISO
FLASH_SPI_MOSI
FLASH_SPI_CLK
UART1_nCTS
UART1_nRTS
UART1_TX
UART1_RX
TEST_58
TEST_59
TEST_60
TEST_62
NC
NC
RES
FORCE_AP
nHIB
HOST_SPI_nCS
HOST_SPI_CLK
HOST_SPI_MOSI
HOST_SPI_MISO
HOST_INTR
NC
WLAN_XTAL_P
WLAN_XTAL_N
NC
RES
RES
NC
R102 100k 1%
3V3_WIFI
WIFI_nRST
S4 Push_Button_SMD
C86 100nF 50V
U12 CC3100
NOTE: 50Ohm
U13 DEA202450BT
IN
OUT
GND
GND
R103 DNP
R104 0R
C84 12pF 6.3V
L19 Inductor LQW18A 3.6nH
L20 Inductor LQW18A 3.6nH
C88 Capacitor 1pF
ANT2
ANT3
WIFI
AH 316M
C89 100nF 50V
U14 N25Q128 - 3 V
HOLD /DQ3
VCC
DU
S
DQ1
C
DQ0
W/VPP/DQ2
VSS
R105 100k 1%
NOTE: 20MHz
NOTE: Active High
R106 100k 1%
R107 100k 1%
PERIP_WIFI_nCTS
PERIP_WIFI_nRTS
PERIP_WIFI_TXD
PERIP_WIFI_RXD
NOTE: Tx/Rx & RTS/CTS Swoped EARLIER
NOTE: Only used when USART is Host I/F
TP24 TestPoint
TP25 TestPoint
TP26 TestPoint
TP27 TestPoint
R108 100k 1%
R109 100k 1%
R110 100k 1%
R111 100k 1%
R112 100k 1%
TP28 TestPoint
R113 100k 1%
NOTE: H: Force AP
R114 DNP
R115 100k 1%
R116 220R 1%
WIFI_nHIB
S5 Push_Button_SMD
SPI1_nCS
SPI1_CLK
SPI1_MOSI
SPI1_MISO
WIFI_INTR
R117 10k 1%
R118 DNP
R119 0R
R120 10M 1%
X4 40MHz
C104 6.8pF, 10V
C105 6.8pF, 10V
TP31 TestPoint
TP32 TestPoint
!!!!!! JOHN: Only X4 are off-grid... One can maybe ignore for now and just make sure they are routed on PCB
!!!!!!!!! JOHN: Compiler complains about ANT3