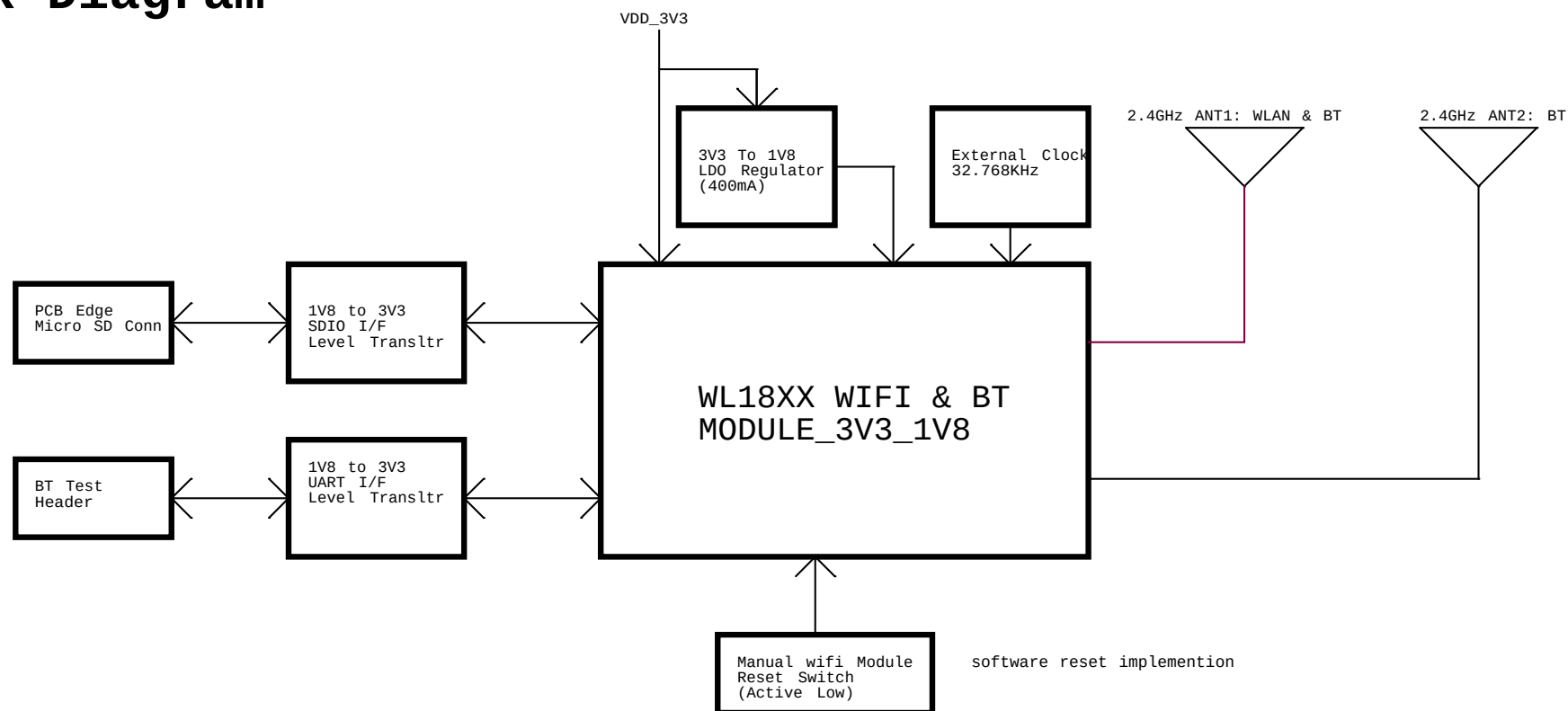
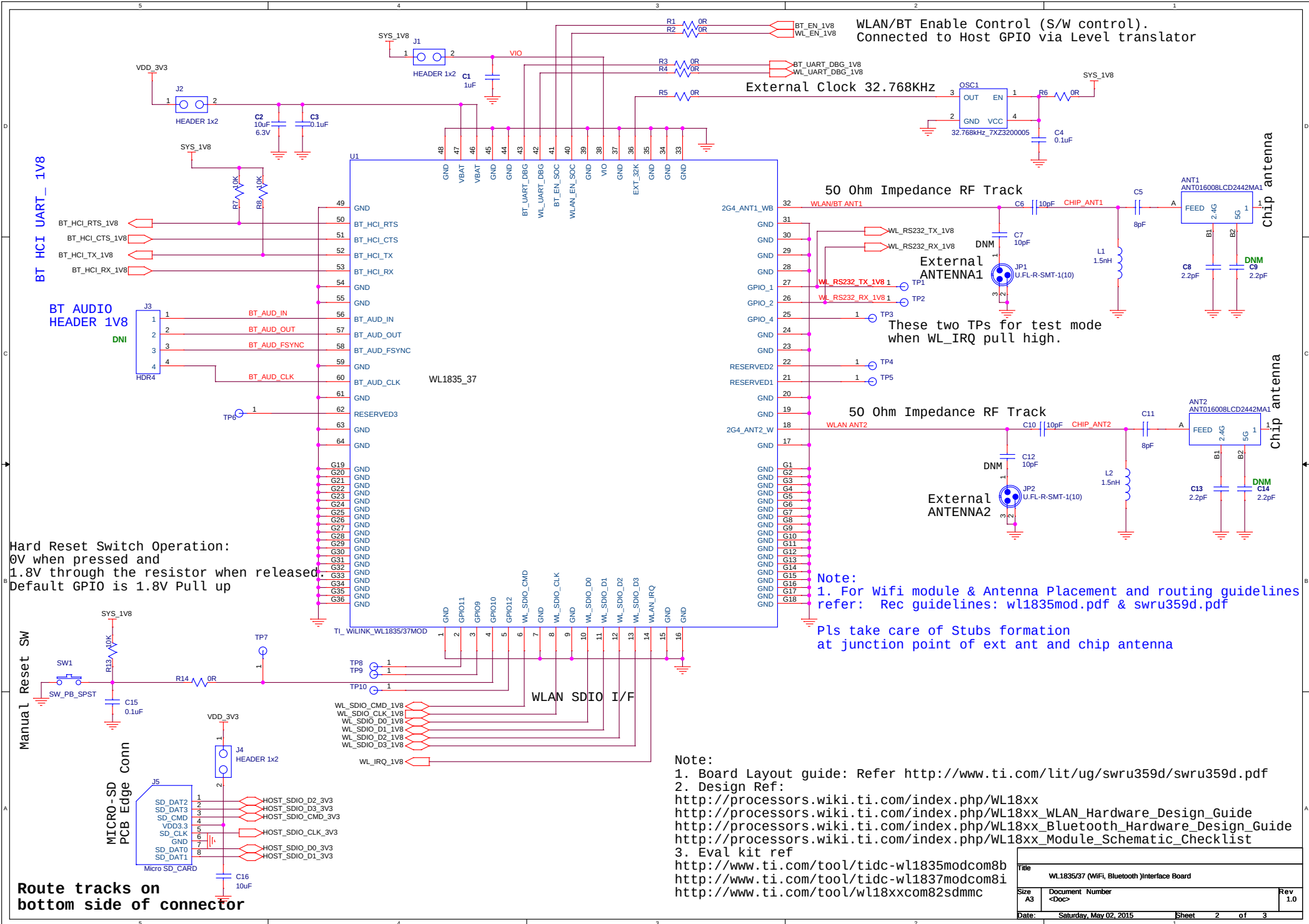


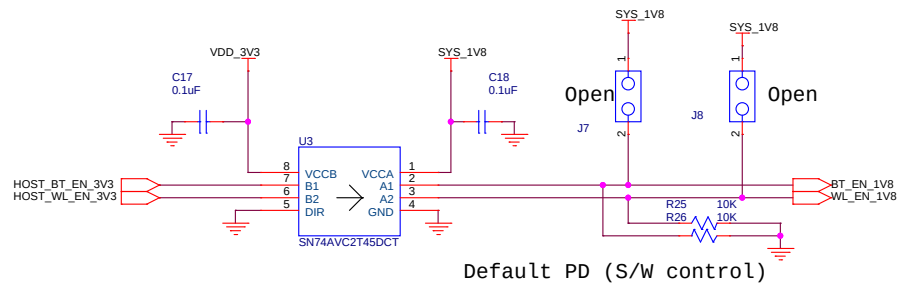
# WL18XX Wifi & BT Interface Board Block Diagram



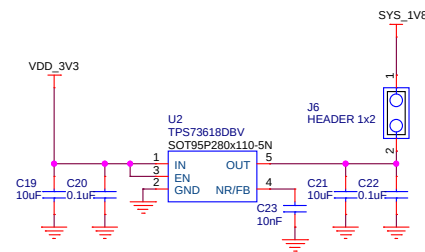
|               |                          |              |
|---------------|--------------------------|--------------|
| Title         |                          |              |
| Block Diagram |                          |              |
| Size<br>A     | Document Number<br><Doc> | Rev<br>1.0   |
| Date:         | Monday, May 04, 2015     | Sheet 1 of 3 |



## Enable BT & WLAN 3V3V to 1.8V Level Translator

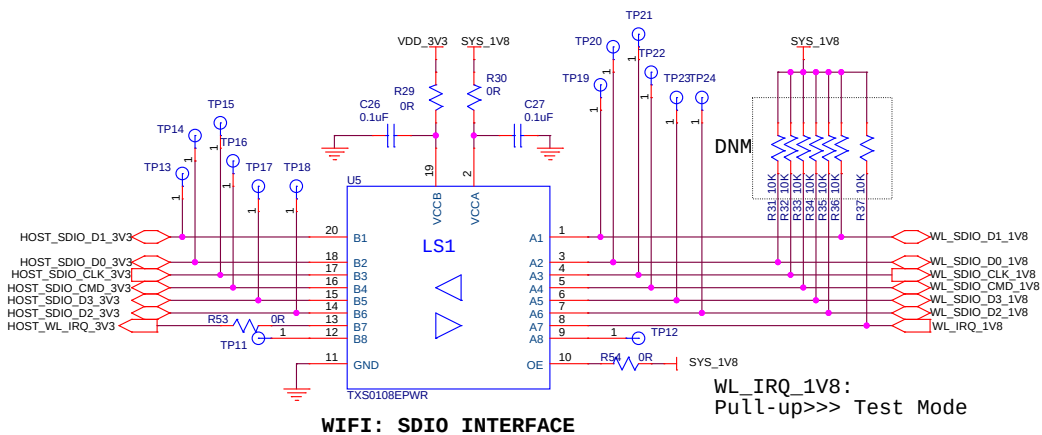


## On Board LD0: 3V3 To 1.8V

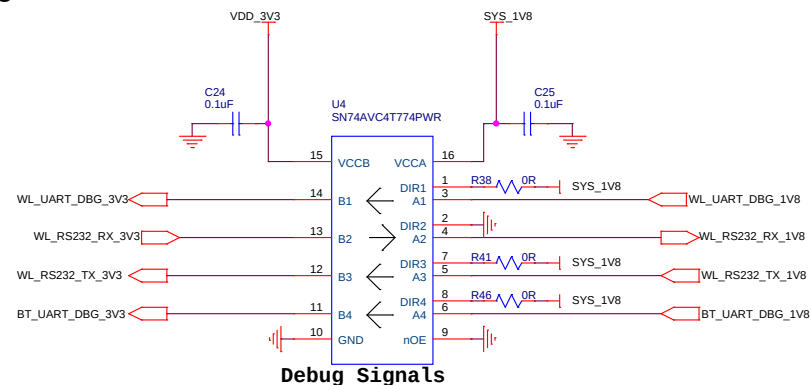


Note:  
1. For Placment & Routing Refer layout Guidelines  
<http://www.ti.com/lit/ds/symlink/tps736.pdf>

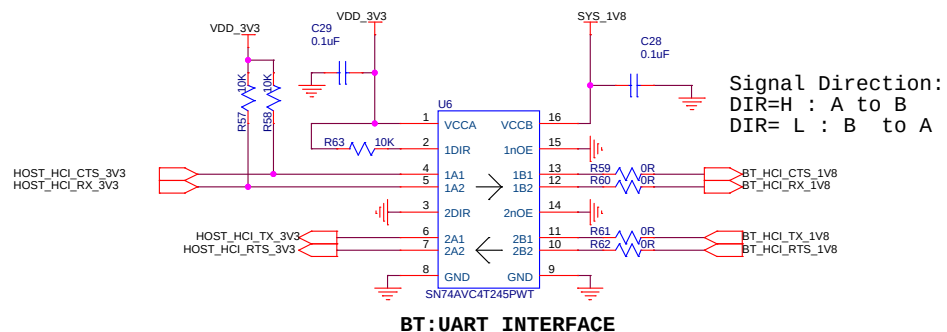
## WiFi SDIO I/F: 1.8V to 3.3V Level Translator



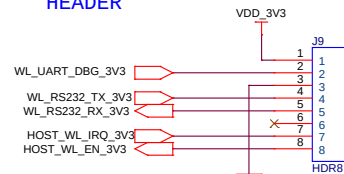
## Debug 1.8V to 3.3V Level Translator



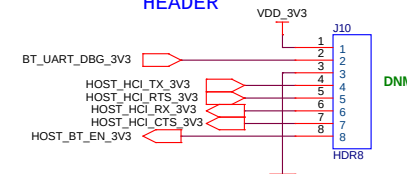
## BT HCI UART I/F: 1.8V to 3.3V Level Shifters



## WLAN TEST HEADER



## BT TEST HEADER



[http://processors.wiki.ti.com/index.php/WL18xx\\_Bluetooth\\_Hardware\\_Design\\_Guide](http://processors.wiki.ti.com/index.php/WL18xx_Bluetooth_Hardware_Design_Guide)

|                                             |                        |              |
|---------------------------------------------|------------------------|--------------|
| Title                                       |                        |              |
| WL1835/37 (WiFi, Bluetooth) Interface Board |                        |              |
| Size                                        | Document Number        | Rev          |
| A3                                          | <Doc>                  | 1.0          |
| Date:                                       | Tuesday, June 16, 2015 | Sheet 3 of 3 |

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