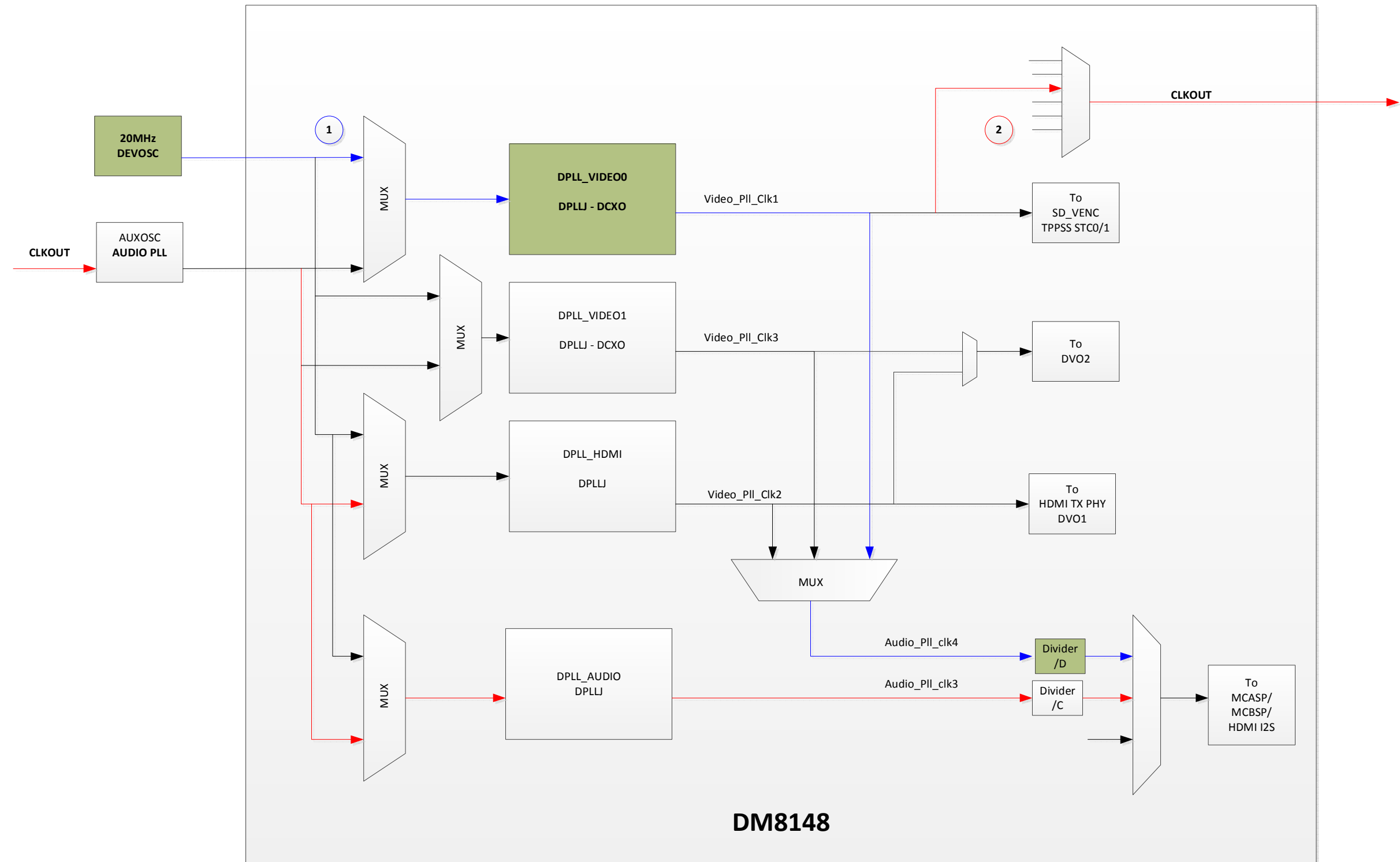




DM8148 – Synchronous AV Clocking Requirement Block Diagram