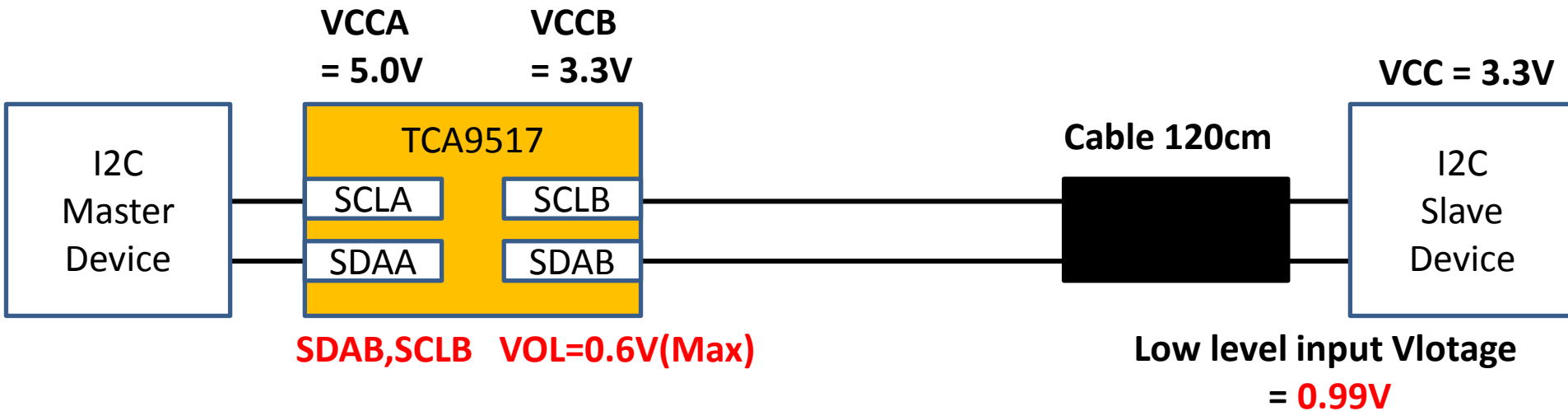


①TCA9517 1chip circuit



TCA9517

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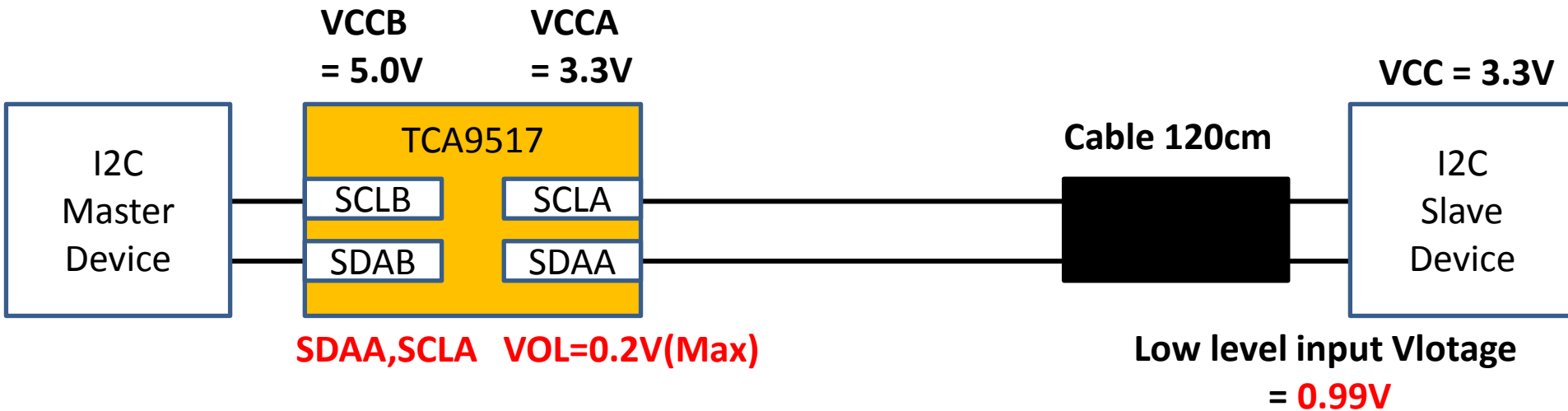
SCPS242A –DECEMBER 2012–REVISED APRIL 2013

ELECTRICAL CHARACTERISTICS

$V_{CCB} = 2.7 \text{ V to } 5.5 \text{ V}$, $GND = 0 \text{ V}$, $T_A = -40^\circ\text{C to } 85^\circ\text{C}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS	V_{CCB}	MIN	TYP	MAX	UNIT
V_{IK}	Input clamp voltage	$I_I = -18 \text{ mA}$	2.7 V to 5.5 V			-1.2	V
V_{OL}	Low-level output voltage	SDAB, SCLB $I_{OL} = 100 \mu\text{A or } 6 \text{ mA}$, $V_{ILA} = V_{ILB} = 0 \text{ V}$	2.7 V to 5.5 V	0.45	0.52	0.6	V
		SDAA, SCLA $I_{OL} = 6 \text{ mA}$			0.1	0.2	

②TCA9517 1chip circuit



*In the circuit of page2,
A-side and B-side are reverse connection
than a circuit of page1.



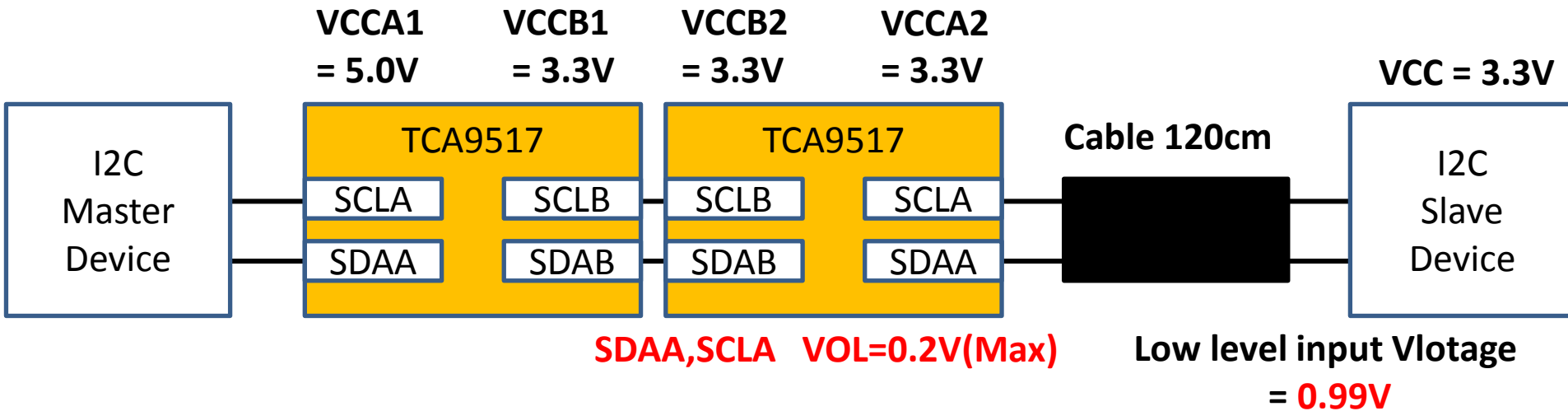
TCA9517

ELECTRICAL CHARACTERISTICS

V_{CCB} = 2.7 V to 5.5 V, GND = 0 V, T_A = –40°C to 85°C (unless otherwise noted)

PARAMETER		TEST CONDITIONS	V _{CCB}	MIN	TYP	MAX	UNIT
V _{IK}	Input clamp voltage	I _I = –18 mA	2.7 V to 5.5 V			–1.2	V
V _{OL}	Low-level output voltage	SDAB, SCLB I _{OL} = 100 μA or 6 mA, V _{ILA} = V _{ILB} = 0 V	2.7 V to 5.5 V	0.45	0.52	0.6	V
		SDAA, SCLA I _{OL} = 6 mA			0.1	0.2	

③TCA9517 2chip circuit



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ELECTRICAL CHARACTERISTICS

$V_{CCB} = 2.7 \text{ V to } 5.5 \text{ V}$, $GND = 0 \text{ V}$, $T_A = -40^\circ\text{C to } 85^\circ\text{C}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS	V_{CCB}	MIN	TYP	MAX	UNIT
V_{IK}	Input clamp voltage	$I_I = -18 \text{ mA}$	2.7 V to 5.5 V			-1.2	V
V_{OL}	Low-level output voltage	SDAB, SCLB $I_{OL} = 100 \mu\text{A or } 6 \text{ mA}$, $V_{ILA} = V_{ILB} = 0 \text{ V}$	2.7 V to 5.5 V	0.45	0.52	0.6	V
		SDAA, SCLA $I_{OL} = 6 \text{ mA}$			0.1	0.2	