

## ***XIO2213 PG2 Errata***

---

*HPA- CIS*

### **ABSTRACT**

This document is provided to summarize the errata seen on XIO2213 PG 2.0 silicon during validation.

### **Contents**

<b>1</b>	<b>UR bit Incorrectly set in the Uncorrectable Error Status Register when the ANFES bit is set</b>	<b>3</b>
1.1	Detailed Description .....	3
1.2	Overall Impact .....	3
1.3	Workaround Proposal .....	3
1.4	Corrective Action .....	3



## **1 UR bit Incorrectly set in the Uncorrectable Error Status Register when the ANFES bit is set**

### **1.1 Detailed Description**

After a configuration transaction to an invalid function or a memory transaction to in an invalid memory window of the XIO2213, the Unsupported Request bit in the Uncorrectable Error Status Register and the Advisory Non-Fatal Error Status bit in the Correctable Error Status Register are both set. In this scenario, only the Advisory Non-Fatal Error Status bit should be set as this does not result in an uncorrectable error.

### **1.2 Overall Impact**

This will cause a failure in the PCI SIG PTC Gold suite testing to Spec. revision 1.1, revision 1.0a will continue to pass.

### **1.3 Workaround Proposal**

None at this time as software does not implement AER.

### **1.4 Corrective Action**

None for current device.

## **IMPORTANT NOTICE**

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

**TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.**

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor, does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

Copyright © 2008, Texas Instruments Incorporated