

THIS PU RESISTOR IS FOR FUTURE PROCESSOR COMPATIBILITY

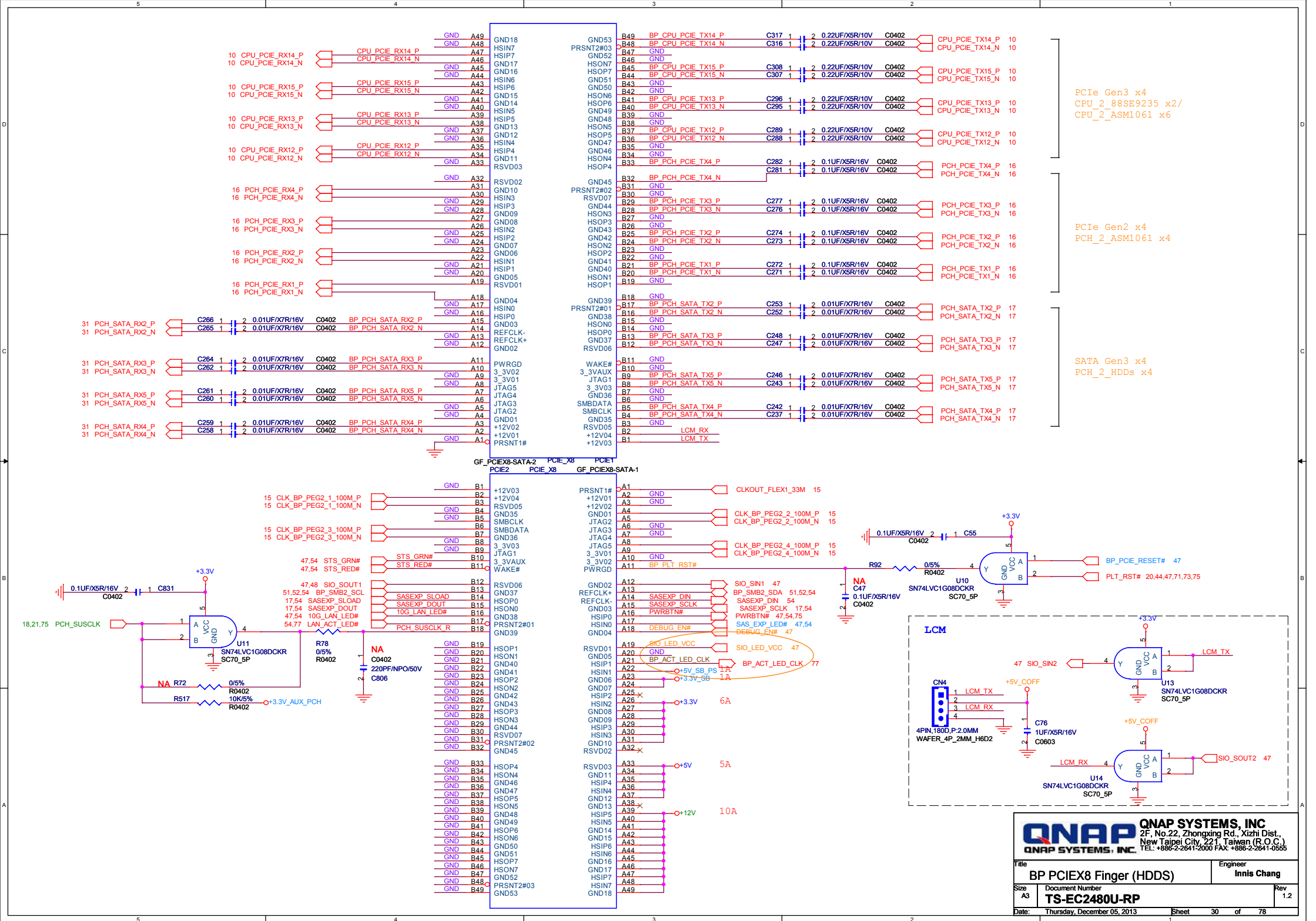
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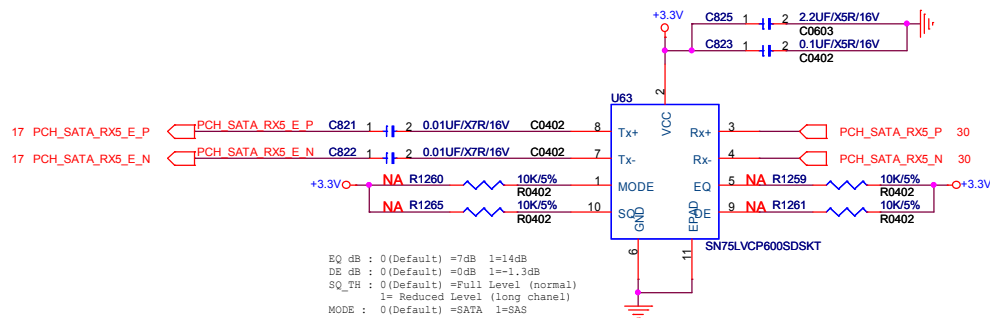
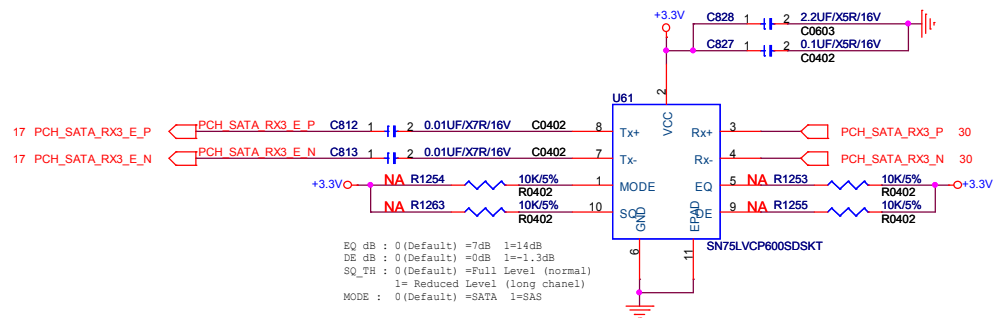
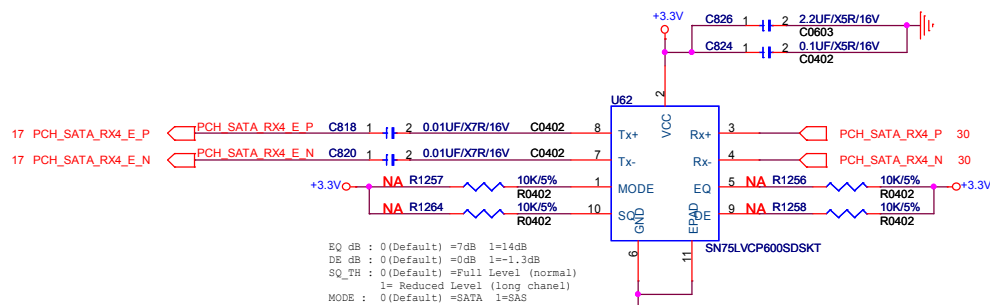
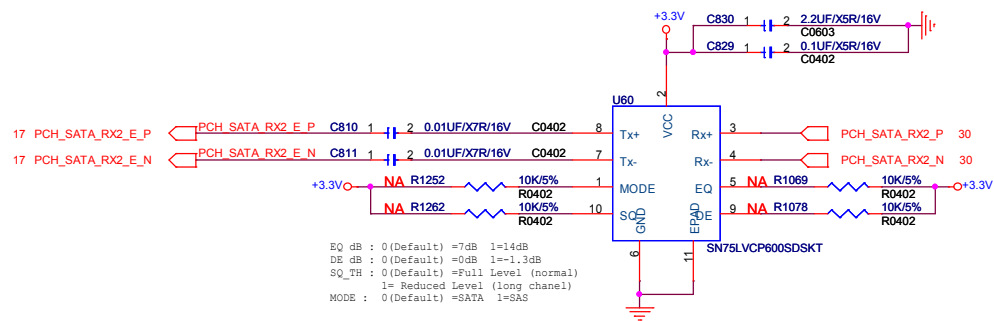
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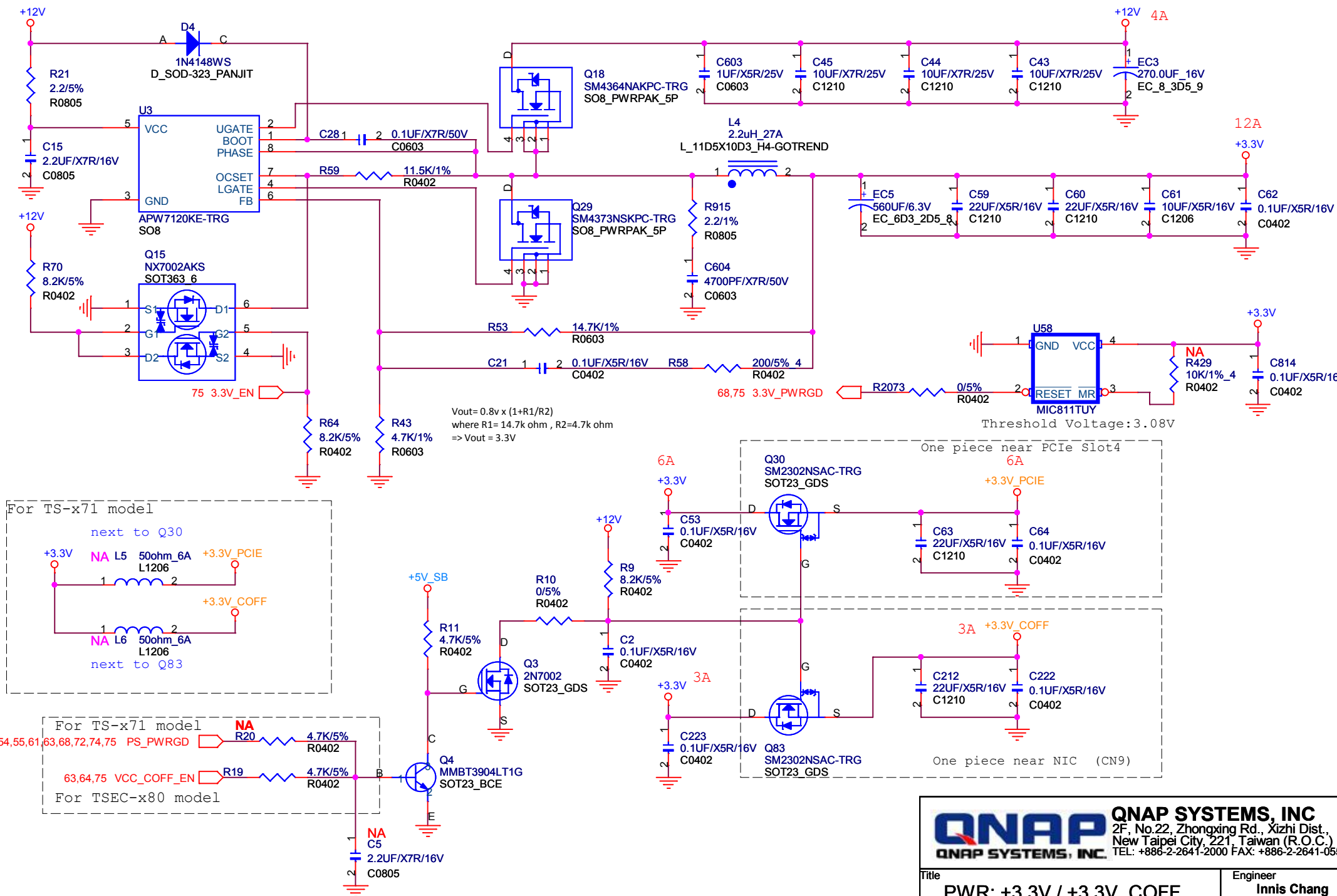
Title: **PCH - SATA/PWM** Engineer: **Innis Chang**

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