f-Squared Noise by Arthur Kay Jan 9, 2007

The Op-Amp current noise spectral density curve may contain a region at high frequency where the voltage noise increases with frequency. The increase in current noise is directly proportionate to frequency (i.e. if frequency increases by a factor of 10 current noise will increase by a factor of 10). This noise is called f-squared noise because the power spectral density is proportionate to f-squared (remember $P = i^2R$). Figure 1 shows how current noise increases by a factor of 10 per decade of frequency. Note that 1/f noise will typically decrease by the square root of 10.

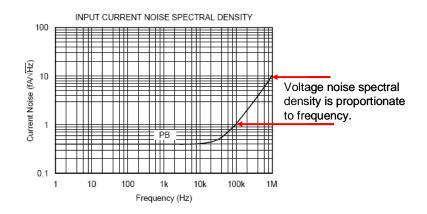


Figure 1: Current Noise Spectral Density Diagram Showing f-Squared Noise

The f-squared noise region is typically the result of voltage noise interacting with the capacitive reactance of the input capacitance. For this reason, some engineers do not consider f-squared noise as current noise. The important thing to keep in mind when analyzing this effect is that this noise will be generated automatically if voltage noise and the input capacitance are properly modeled. So, if you are including the input capacitance in your op-amp model, you should not use a current noise spectral density curve that contains the f-squared region.

Figure 2 and Figure 3 summarize the two possible ways to analyze a noise problem that contains f-squared noise. In figure 2, the f-squared noise is not included in the noise source and so, the input capacitance is included. This is the recommended way of analyzing f-squared noise because it models the actual circuit condition. Note that the actual effect of the input capacitance will very depending on the circuit configuration. For example, C2 in figure 2 is shorted out by Vg1. In a different configuration, C2 may play an important roll. In figure 3, the noise source models the f-squared region and so the input capacitance is not included in the model. This method of noise analysis may produce accurate results; however, the f-squared effect is the same regardless of circuit configuration. Be careful not to count the effect of f-square noise twice as is shown in figure 4.

Unfortunately, data sheets are not all consistent regarding f-squared noise. Some data sheets show the f-squared region and others assume the effect will be accounted for by the proper modeling on the input capacitance.

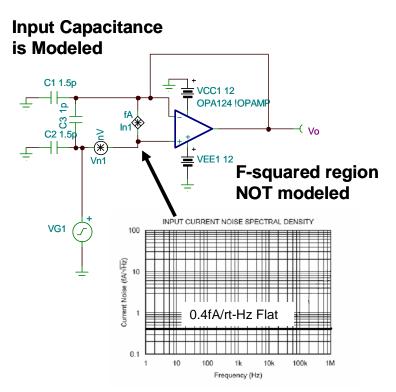


Figure 2: f-Squared region generated by Noise Voltage Over Input Capacitance

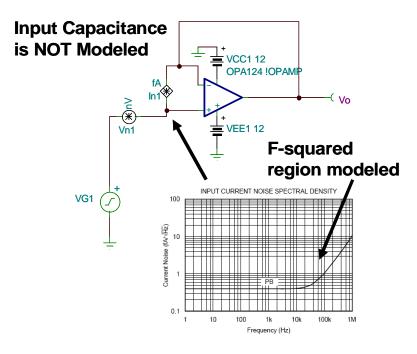


Figure 3: f-Squared region included in Current Source

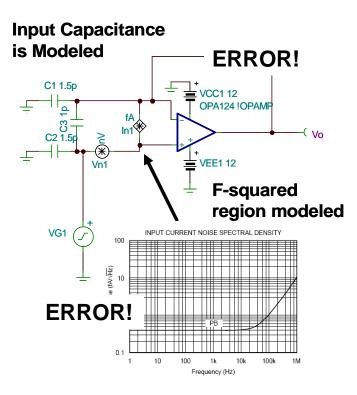


Figure 4: Wrong way to analyze f-squared noise.

Exception to the f-Squared Rule:

Up to this point we have assumed that f-squared noise is the interaction of the voltage noise source and the input capacitance. While in general this is true, in some cases there is an additional source of noise. Figure 5 illustrates the differential input stage of an op-amp including parasitic capacitance and noise sources. The voltage noise sources Vn1 and Vn2 are correlated and so, the voltage noise is rejected by the op-amps common mode rejection. However, these sources will interact with the parasitic capacitance to generate f-squared current noise. This f-squared noise is in addition to the f-squared noise that was discussed previously. Further complicating the matter is the fact that most data sheets will not describe this effect. It must be stressed, however, that this effect is rarely a significant factor in the overall noise.

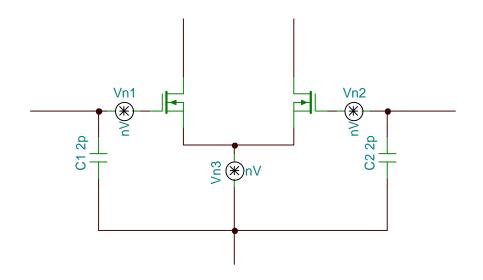


Figure 5: Internal Op-Amp Noise configuration Leading to Additional f-Squared Noise

Hand calculations of f-squared noise:

Assuming that the f-squared noise it results from an interaction of the voltage noise source and the reactance of the parasitic input capacitance, then the f-squared noise spectral density curve can be predicted using simple hand calculations. The first part of the calculation is to derive a formula for the f-squared portion of the current noise spectral density curve (see figure 6). The second part of the calculation is to integrate the current spectral density curve squared (power spectral density). The square root of this quantity is taken to convert the result to current (see figure 7). Finally, the current from the f-squared region is combined with current from the broad band region (see figure 8). Note that in this example the f-squared noise dominates.

$$X_{c} = \frac{1}{2 \cdot \pi \cdot f \cdot C_{dif}}$$
$$i_{spec}(f) = \frac{e_{n}}{X_{c}} = e_{n} \cdot \left(2 \cdot \pi \cdot f \cdot C_{dif}\right)$$

For this example:

$$e_n = 5 \cdot 10^{-9}$$
 $C_{dif} = 1 \cdot 10^{-12}$
 $i_{spec}(f) = 3.142 \cdot 10^{-20} \cdot f$

Figure 6: Derive the Current Spectral Density Function for f-Squared Region

$$i_{n_f_squ} = \sqrt{\int_{f_1}^{f_2} i_{spec}(f)^2 df}$$

$$i_{n_f_squ} = \sqrt{\int_{1}^{10^6} (3.14210^{-20} \cdot f)^2 df} = 18.14 \times 10^{-12}$$
 A rms

Figure 7: Compute Noise Current From f-Squared Region

$$i_{nBB} = i_{BB} \sqrt{BW_n}$$

$$i_{nBB} = (0.5 \cdot 10^{-15}) \sqrt{(10^6 - 1)} = 500 \times 10^{-15}$$

$$i_{n_total} = \sqrt{i_{n_f_squ}^2 + i_{nBB}^2}$$

$$i_{n_total} = \sqrt{(18.14 \times 10^{-12})^2 + (500 \times 10^{-15})^2} = 18.147 \times 10^{-12}$$
 A rms

Figure 8: Combine f-Squared Region with Broadband Region

Tina Spice Analysis Including the f-Squared Region:

For this example:

SPICE Noise Analysis of circuits containing the f-squared region follows the same procedures outlined in Part IV of this series. So the first step to doing any noise analysis is to test the model. Proving that the f-squared region of the SPICE model is correct is complicated by the fact that data sheets may not show this region in the current spectral density curve. Also, some amplifiers do not have a 1/f region. The hand calculation is one way to determine if f-squared noise is present.

Figure 9 shows how to build a Tina SPICE model that includes the f-squared effect. The procedure outlined in Part IV is used to configure the noise sources and the generic op-amp. The only additional step is to include the common mode and differential input capacitance. The input capacitance specification is listed in the data sheet as shown in Figure 10. Figure 11, 12 and 13 show the Tina SPICE listing for the noise sources and the generic op-amp. Figure 14 shows the current noise spectral density curve as measured by the current to voltage converter CCV1 and the data sheet current noise curve. Note that the f-squared region from the Tina Spice Simulation closely follows the data sheet curve.

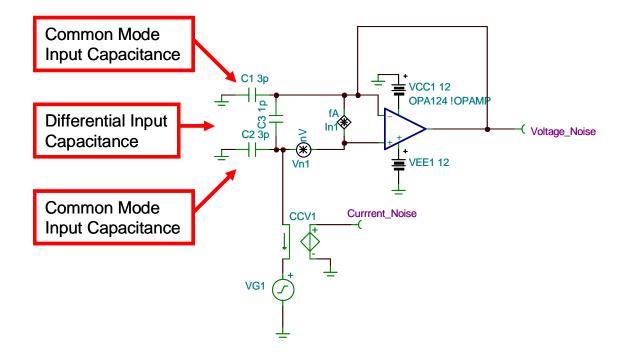


Figure 9: Build a Tina Spice Model

Input Offset Current	V _{CM} = 0VDC	±1	±5	pА
IMPEDANCE Differential Common-Mode		10 ¹³ 1 10 ¹⁴ 3		Ω pF Ω pF
VOLTAGE RANGE		 		

Figure 10: Input Capacitance from Specification for OPA124

1 f-squared_noise:Vn1 [MACRO] - Schematic	Editor
File Edit Insert View Analysis T&M Tools Help	
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Basic (Switches (Meters (Sources (Semiconductors (Op	toelectronics Spice Macros (Gates (F
 BEGIN PROG NSE NANO VOLT/RT-HZ SUBCKT VNSE 1 2 BEGIN SETUP OF NOISE GEN - NAN INPUT THREE VARIABLES SET UP VNSE 1/F 	
* NU/RHS AF 1/F FREQ 1/f	noise = 40nV/rtHz 10Hz
* SET UP VNSE FB * NV/PH2 FLATBAND PARAM NVR=6 * END USER INPUT	Broadband noise
* START CALC VALS	

Figure 11: Voltage Noise Source Configuration

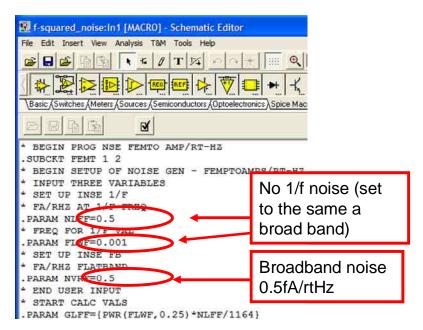


Figure 12: Current Noise Source Configuration

Library		Tolerance Model	1	1	
Tina					
Model		Model Parameters			
Standard		Usage: General			
Lype		Open loop gain [·]	1.77M		
IDPAMP LM301A LM318 LM709 TL061C TL071C TL071C TL081C uA741 uA741A uA741C	×	Output resistance [Dhm] Maximum slew rate [V/s] Dominant pole [Hz] Input offset voltage [V] Input offset voltage [V] Input offset current [A] Offset voltage tco [V/C] Current doubling int. [C]	75 EB4 950m 0 0 0 0 0 10 2	>	Open Loop gain and dominant pole from data sheet.
1/13		Outp. offs. lim. [Vcc+] [V]	6	1	511001.

Figure 13: Generic Op-Amp Configuration

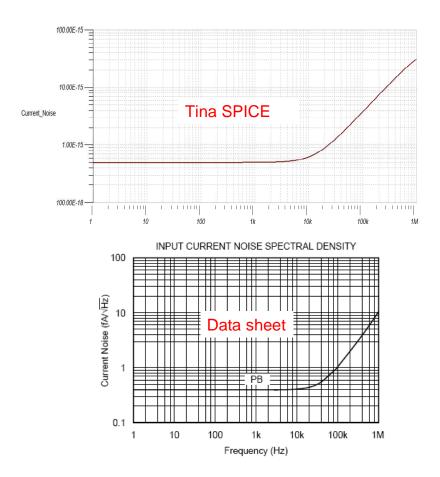


Figure 14: Tina Spice Results Compared with Data Sheet