

Msp430f5529 Memory Map ... from data sheet (p20) & user guide (p71)

19-Aug-13

Description	Starting adr hex	Ending adr hex	Starting adr dec	Ending adr dec	Size dec	512 segs	subtotal decimal	subtotal hex
SFR	0	FF	0	255	256			
peripherals	100	FEF	256	4079	3824			
descriptor type	FF0	FF3	4080	4083	4			
start adr of descriptor structure	FF4	FF7	4084	4087	4			
..... gap	FF8	FFF	4088	4095	8		4096	1000
ROM								
BSL0	1000	11FF	4096	4607	512	1		
BSL1	1200	13FF	4608	5119	512	1		
BSL2	1400	15FF	5120	5631	512	1		
BSL3	1600	17FB	5632	6139	508	0.99		
BSL signature location	17FC	17FF	6140	6143	4	0.01	2048	800
Flash								
info D	1800	187F	6144	6271	128	0.25		
info C	1880	18FF	6272	6399	128	0.25		
info B	1900	197F	6400	6527	128	0.25		
info A	1980	19FF	6528	6655	128	0.25		
device descriptor	1A00	1A7F	6656	6783	128	0.25		
..... gap	1A80	1BFF	6784	7167	384	0.75	1024	400
RAM								
ram sector USB	1C00	23FF	7168	9215	2048	ram		
ram sector 0	2400	2BFF	9216	11263	2048	ram		
ram sector 1	2C00	33FF	11264	13311	2048	ram		
ram sector 2	3400	3BFF	13312	15359	2048	ram		
ram sector 3	3C00	43FF	15360	17407	2048	ram	10240	2800
Flash								
main code memory bank 0	4400	C3FF	17408	50175	32768	64		
main code memory bank 1a	C400	FDFE	50176	65023	14848	29		
..... gap	FE00	FF7F	65024	65407	384	0.75		
interrupt vector	FF80	FFFF	65408	65535	128	0.25		
main code memory bank 1b	10000	143FF	65536	82943	17408	34		32768
main code memory bank 2	14400	1C3FF	82944	115711	32768	64		
main code memory bank 3	1C400	243FF	115712	148479	32768	64	131072	20000

Msp430fr5720 Memory Map ... from data sheet (p22, p33)

20-Aug-13

Description	Starting adr hex	Ending adr hex	Starting adr dec	Ending adr dec	Size dec	subtotal decimal	subtotal hex
SFR	0	FFF	0	4095	4096	4096	1000
ROM							
BSL0	1000	11FF	4096	4607	512		
BSL1	1200	13FF	4608	5119	512		
BSL2	1400	15FF	5120	5631	512		
BSL3	1600	17FF	5632	6143	512	2048	800
FRAM							
info B	1800	187F	6144	6271	128		
info A	1880	18FF	6272	6399	128		
NA mirror to info B	1900	197F	6400	6527	128		
NA mirror to info A	1980	19FF	6528	6655	128	512	200
FRAM							
device descriptor (TLV)	1A00	1A7F	6656	6783	128	128	80
Unknown							
..... gap	1A80	1BFF	6784	7167	384	384	180
RAM							
RAM	1C00	1FFF	7168	8191	1024	1024	400
Unknown							
..... gap	2000	C1FF	8192	49663	41472	41472	A200
FRAM							
main code memory	C200	FF7f	49664	65407	15744		
main interrupt vectors	FF80	FFFF	65408	65535	128	15872	3E00