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SCHMATIC CONTENTS:

- 01 - TITLE
- 02 - MCU 80 (F28055)
- 03 - ISO JTAG USB

Release Notes:

- R1.0 - Initial Release
- R1.1 - Added ADC Conditioning such that ADC channels A1, A3, A6, B1, B4, B6, and B7 can be configured to have a gain of 1.
- R1.2 - Fixed silkscreen; changed U1.27; added switch to allow PGA-GNDs to be grounded locally or externally.



Title: F2805x ISO-controlCard

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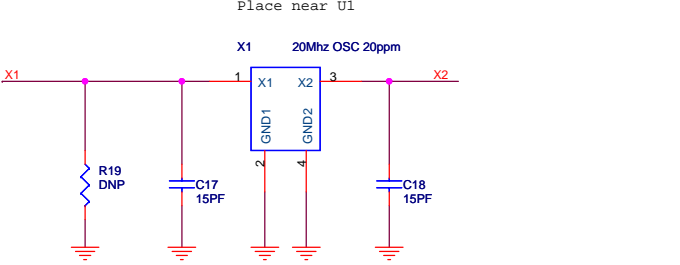
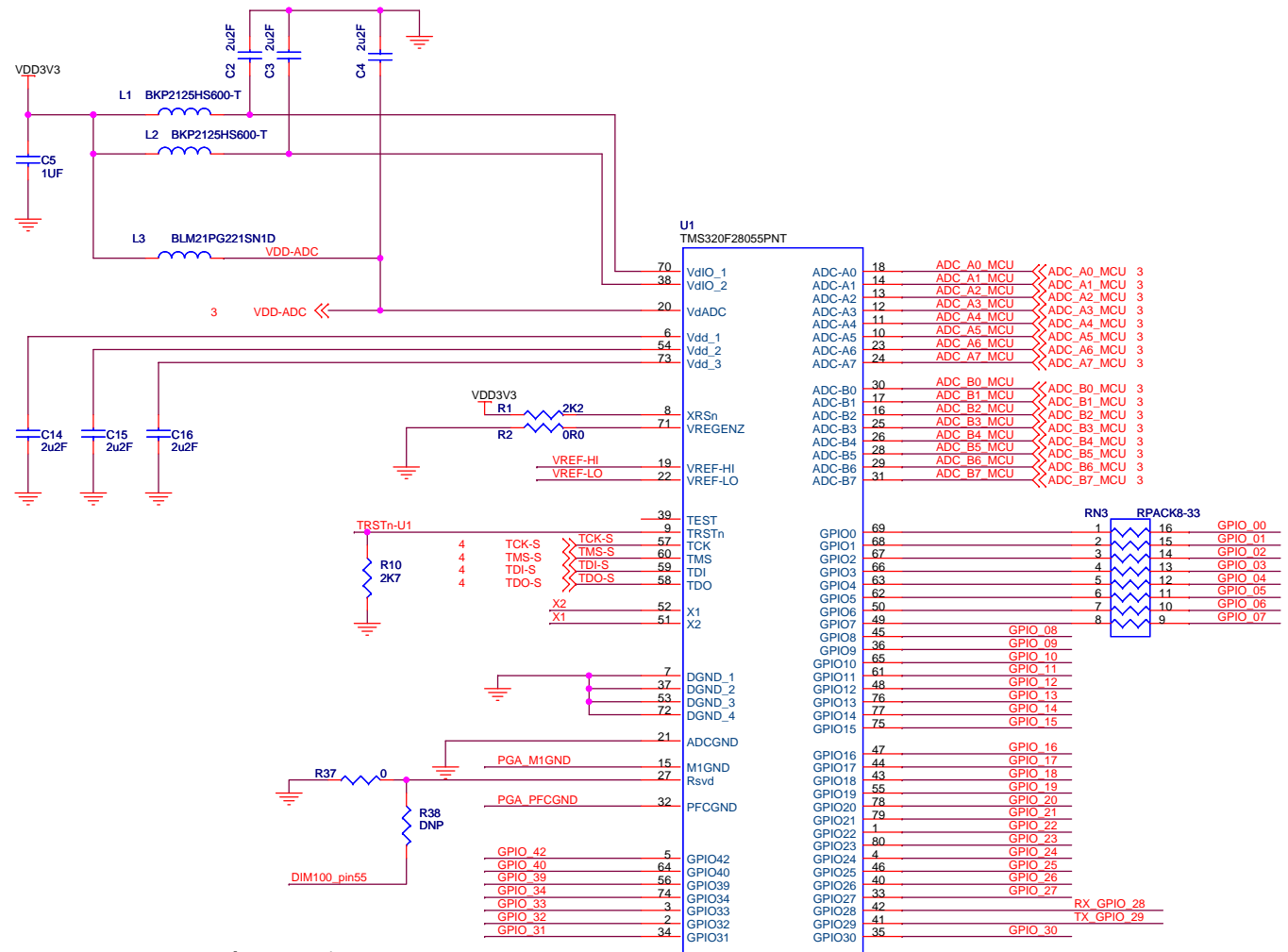
F2805x ISO-controlCard.DSN

Revision:
1.2

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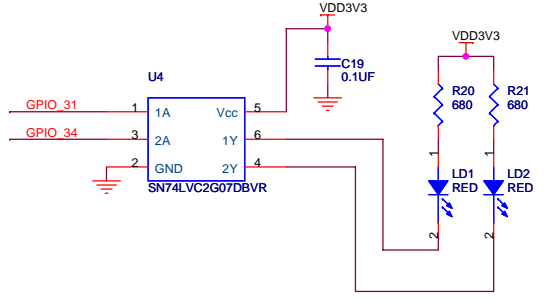
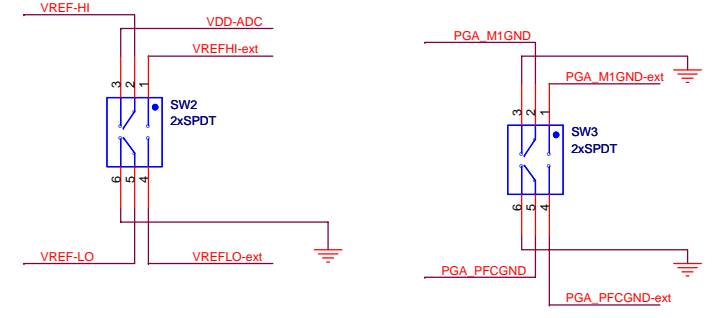
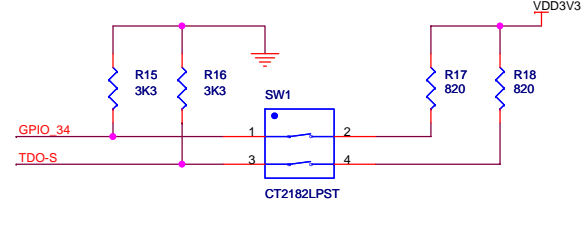
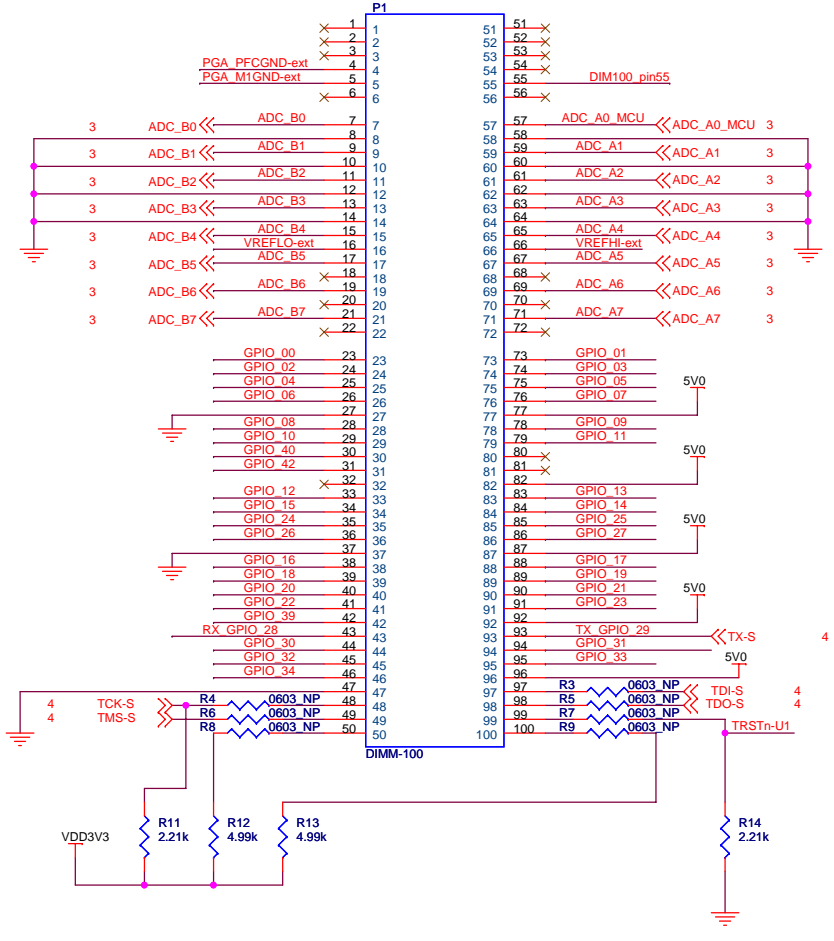
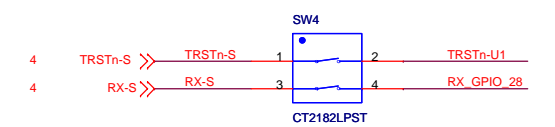
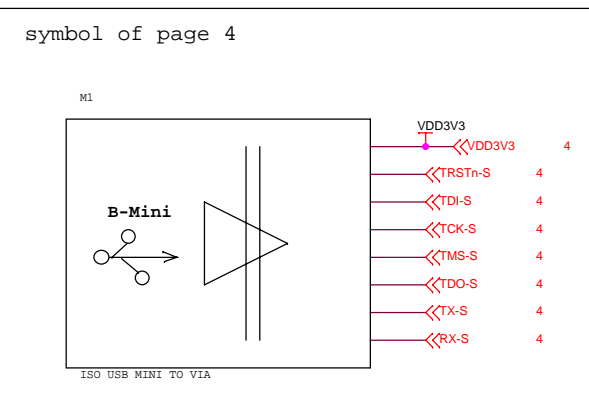
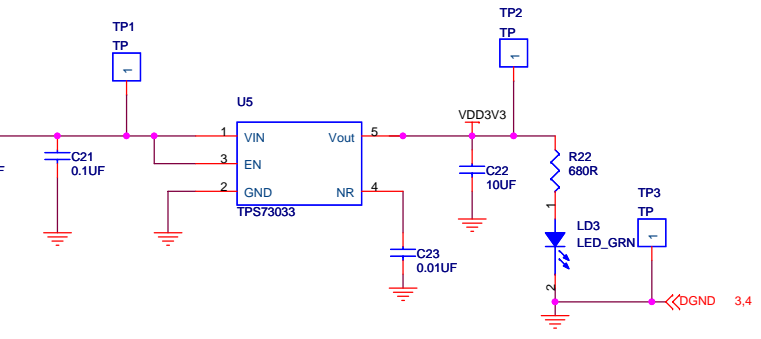
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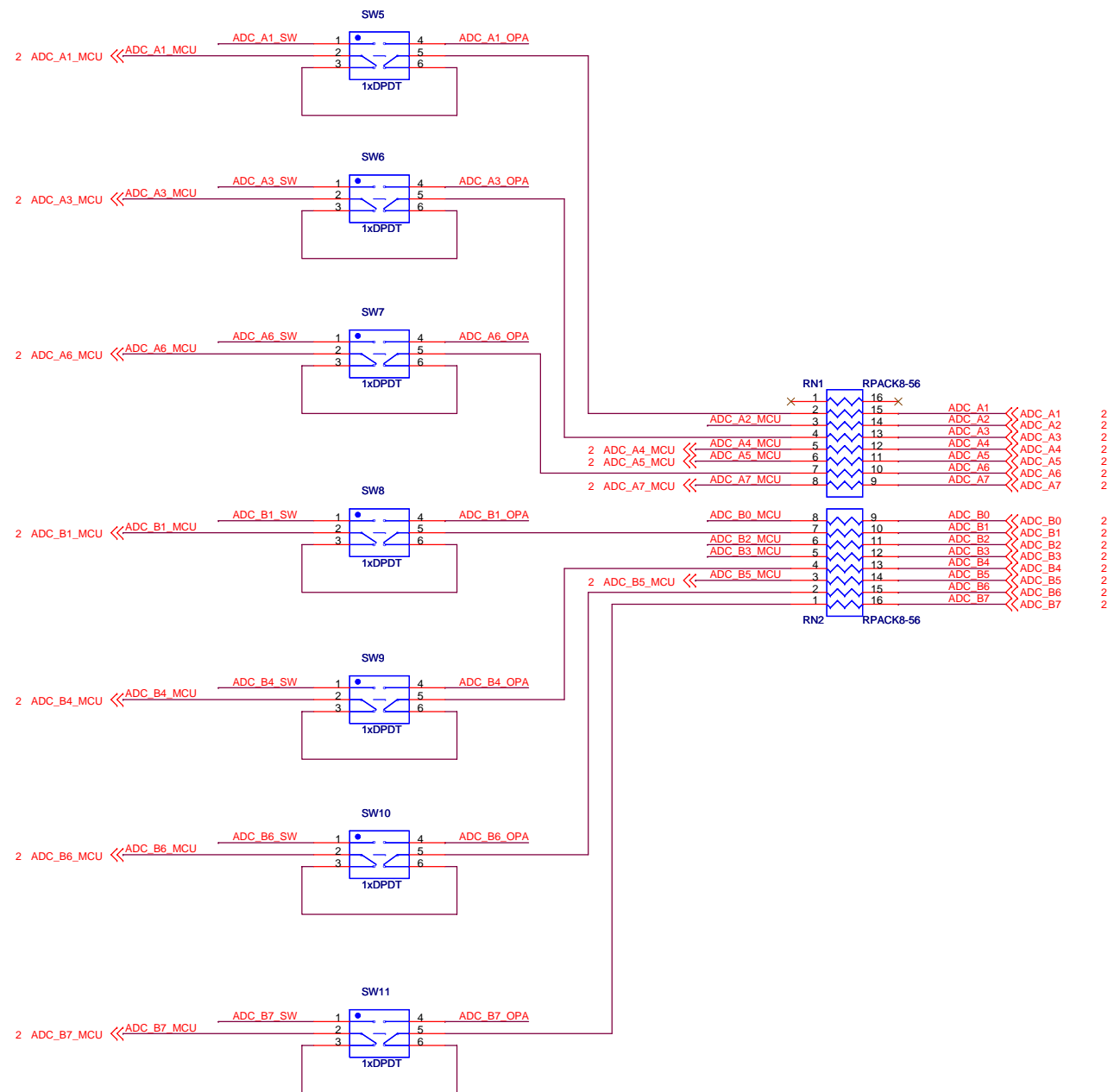
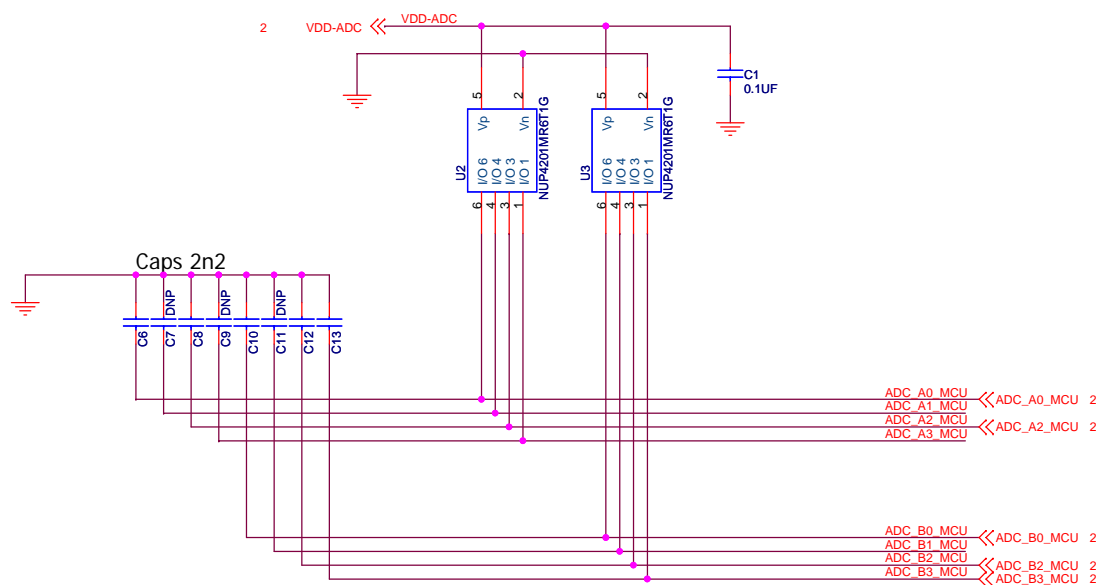
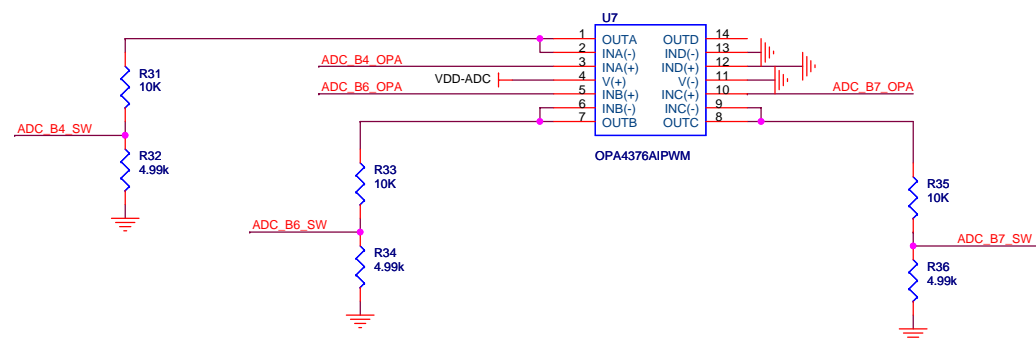
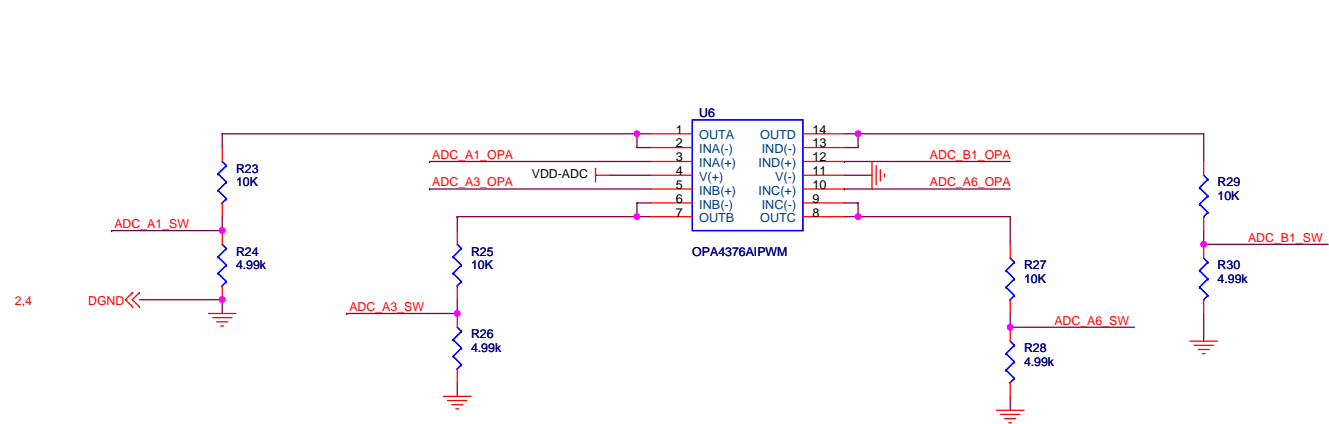


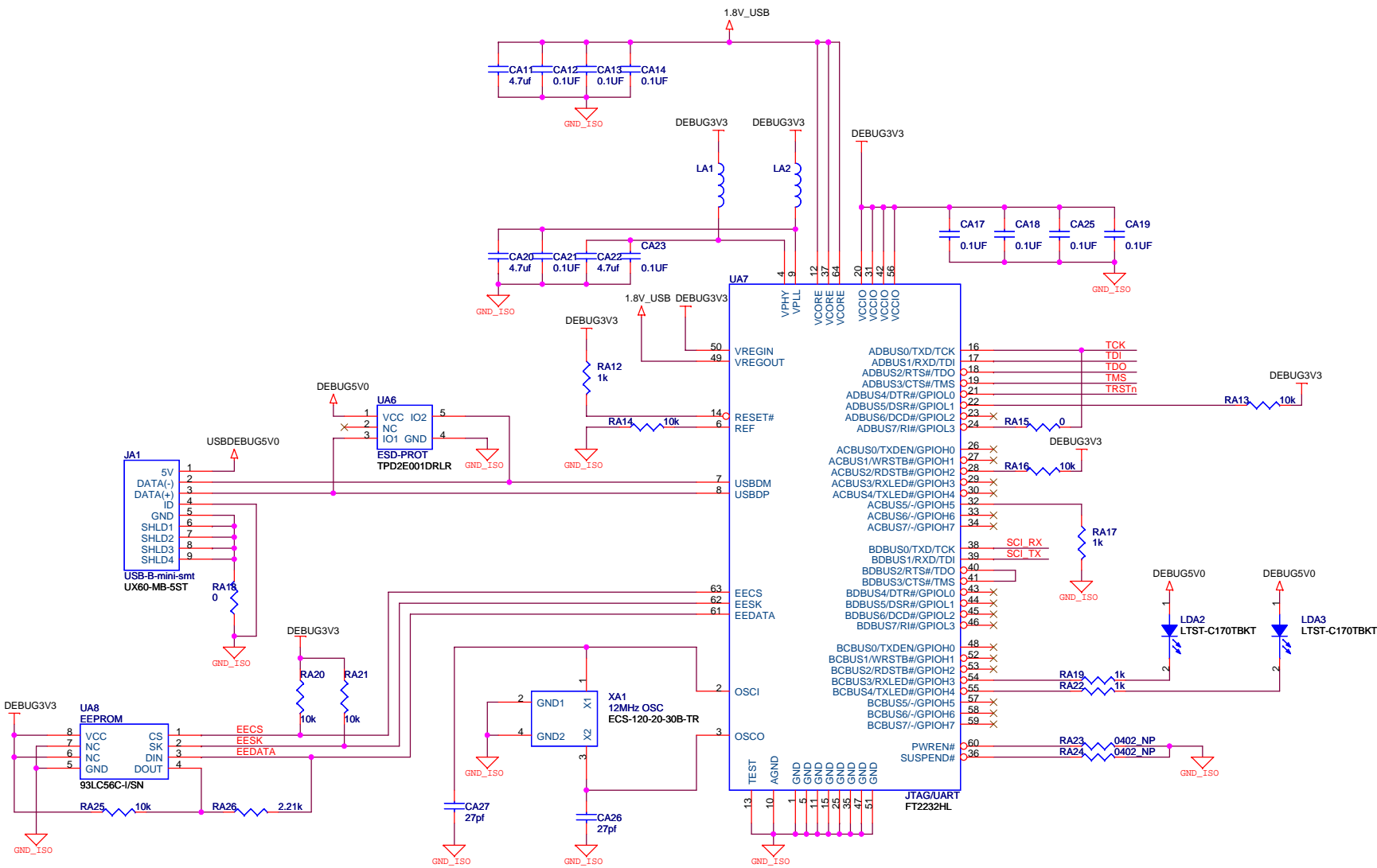
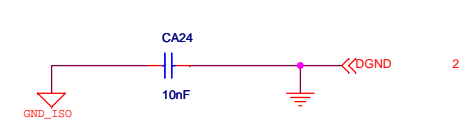
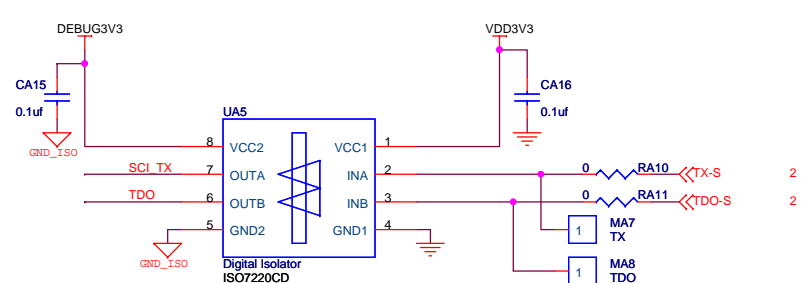
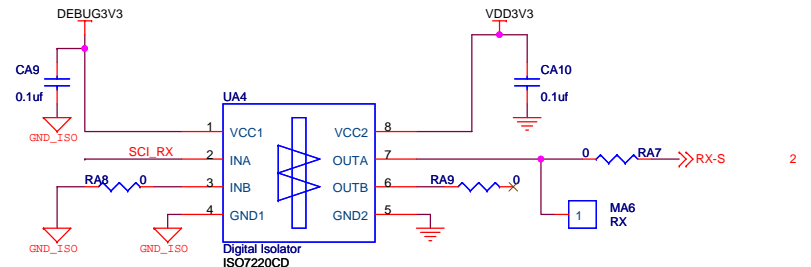
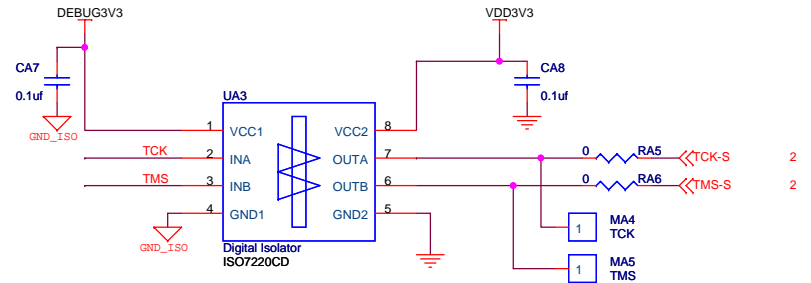
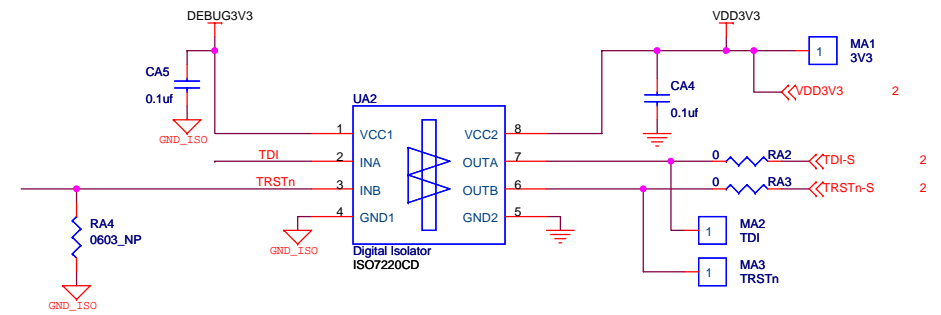
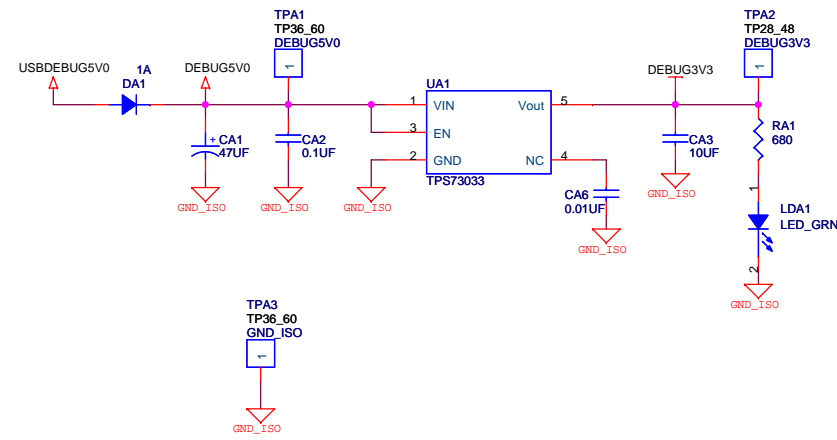
GPIO001 and GPIO034 are used by default.
To connect a legacy JTAG:
GPIO001 => EMD0
GPIO034 => EMU1

If internal crystal only is desired, X1 can be de-populated.
R4 should be populated with 0R0, and C30-C31 should be de-populated to fall in line with the datasheet.



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