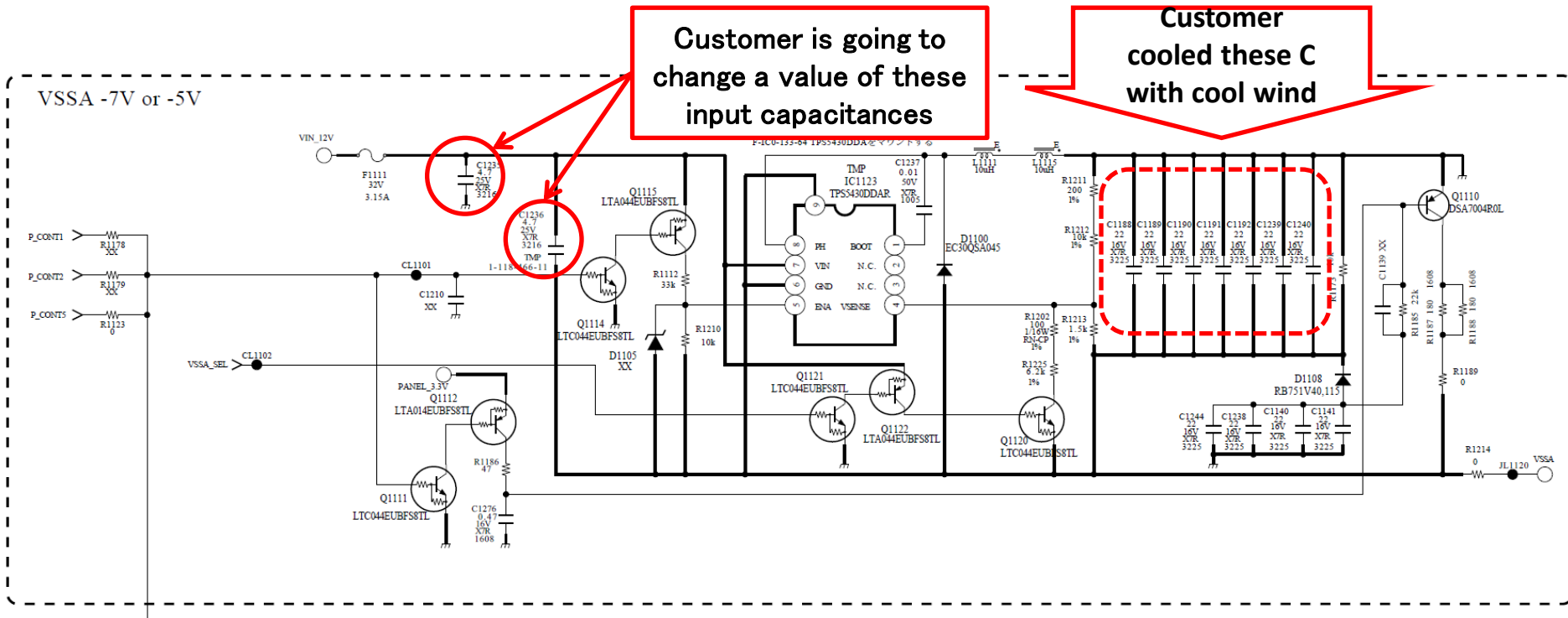


Customer board layout (TPS5430) and their problem



■ Problem and its features

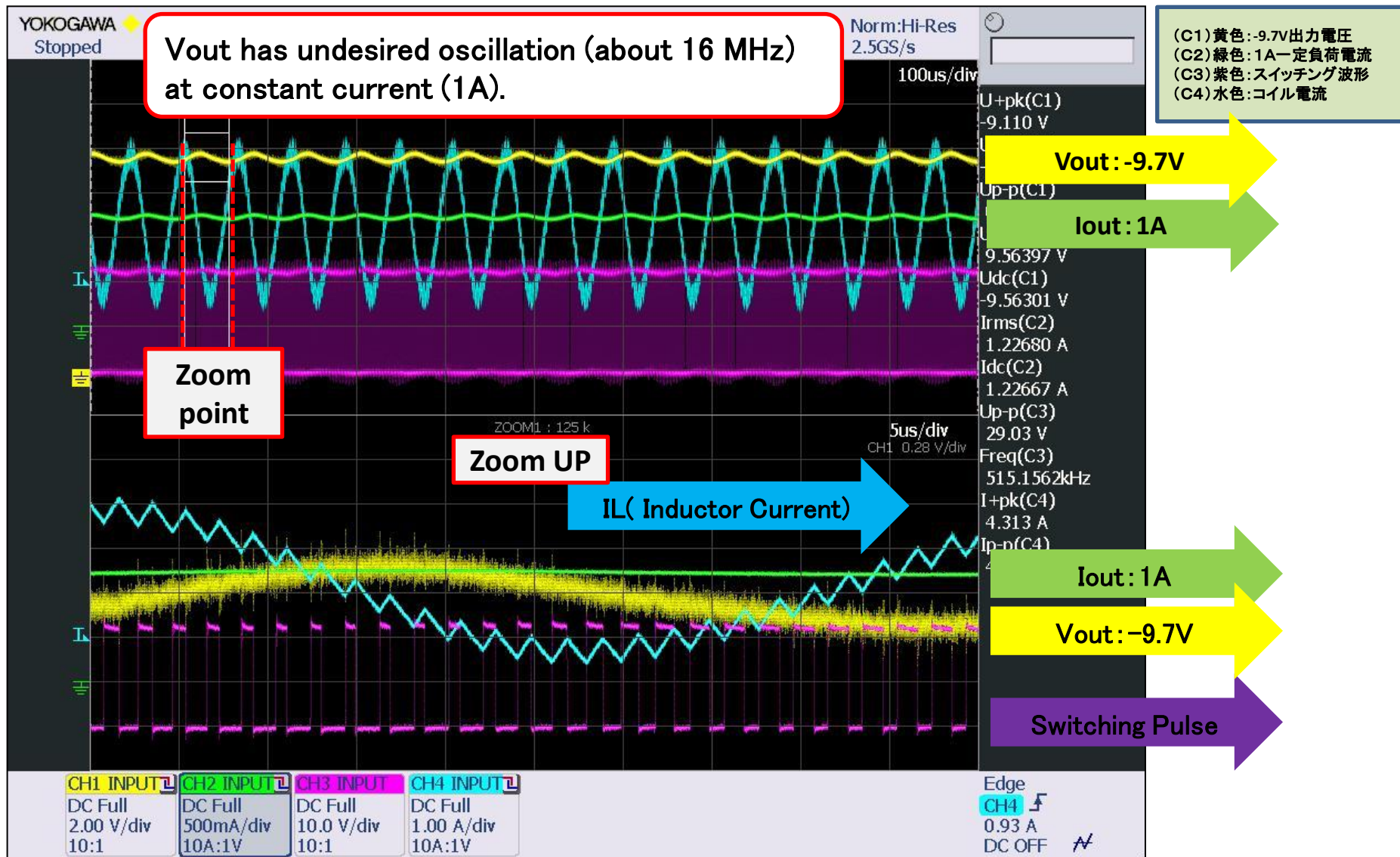
- ①An undesired oscillation of V_{out} was confirmed with a constant current.
(Especially, undesired oscillation is remarkable with the high load current.)

- ②In this case of ①, undesired oscillation might not happen.

Under the situation, this problem occurred when a customer cooled these parallel capacitances of the output side with a cold wind.

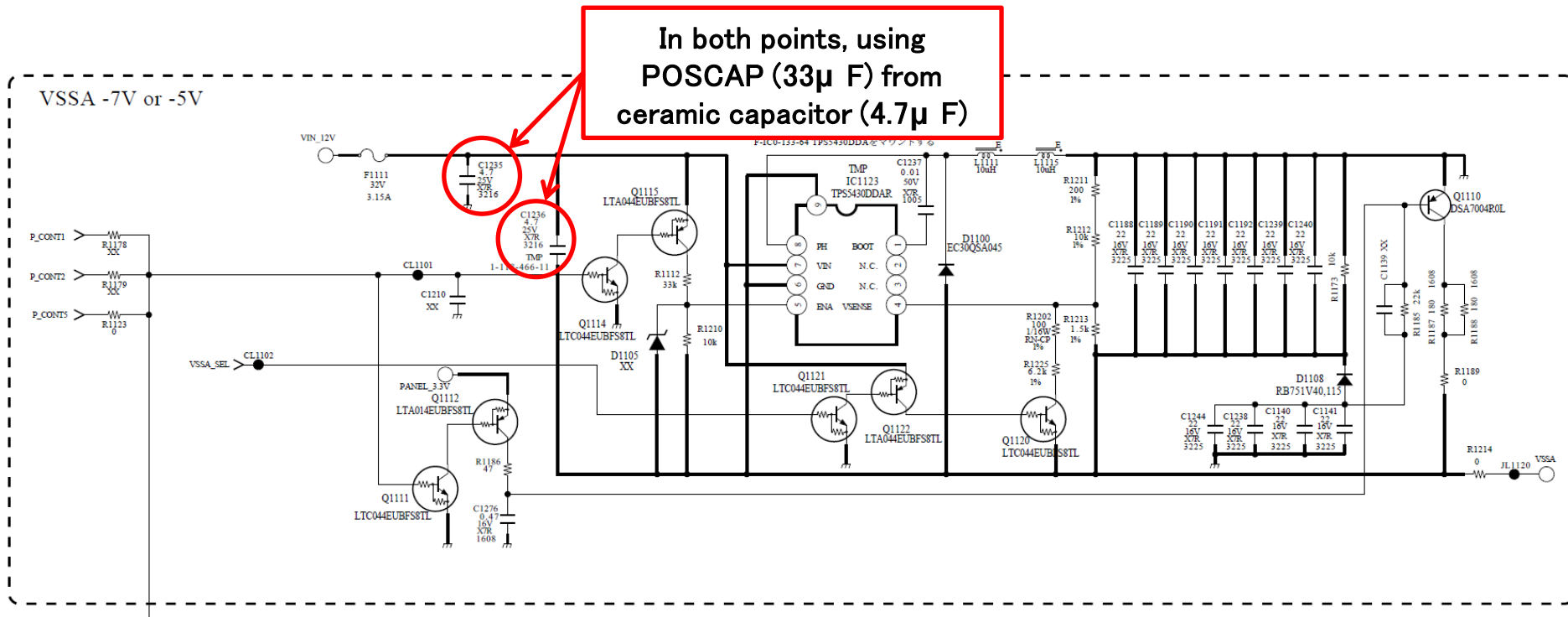
- ③Although the low load current, this problem occurred when its part was cooled.

Waveform at Iout:1A on customer board



考察: 負荷が大きくなると発振することは分かっている。・・・定常(一定)負荷で起きる。
コイルに流れる電流が通常発振周波数では無い所で周期的(約16kHz)で変動しているのが分かる。

Customer's countermeasure and result of their board



■ Their countermeasure

Customer tried a change of these input capacitances. (value/kind)

These points are C1235 and C1236 on customer's block diagram.

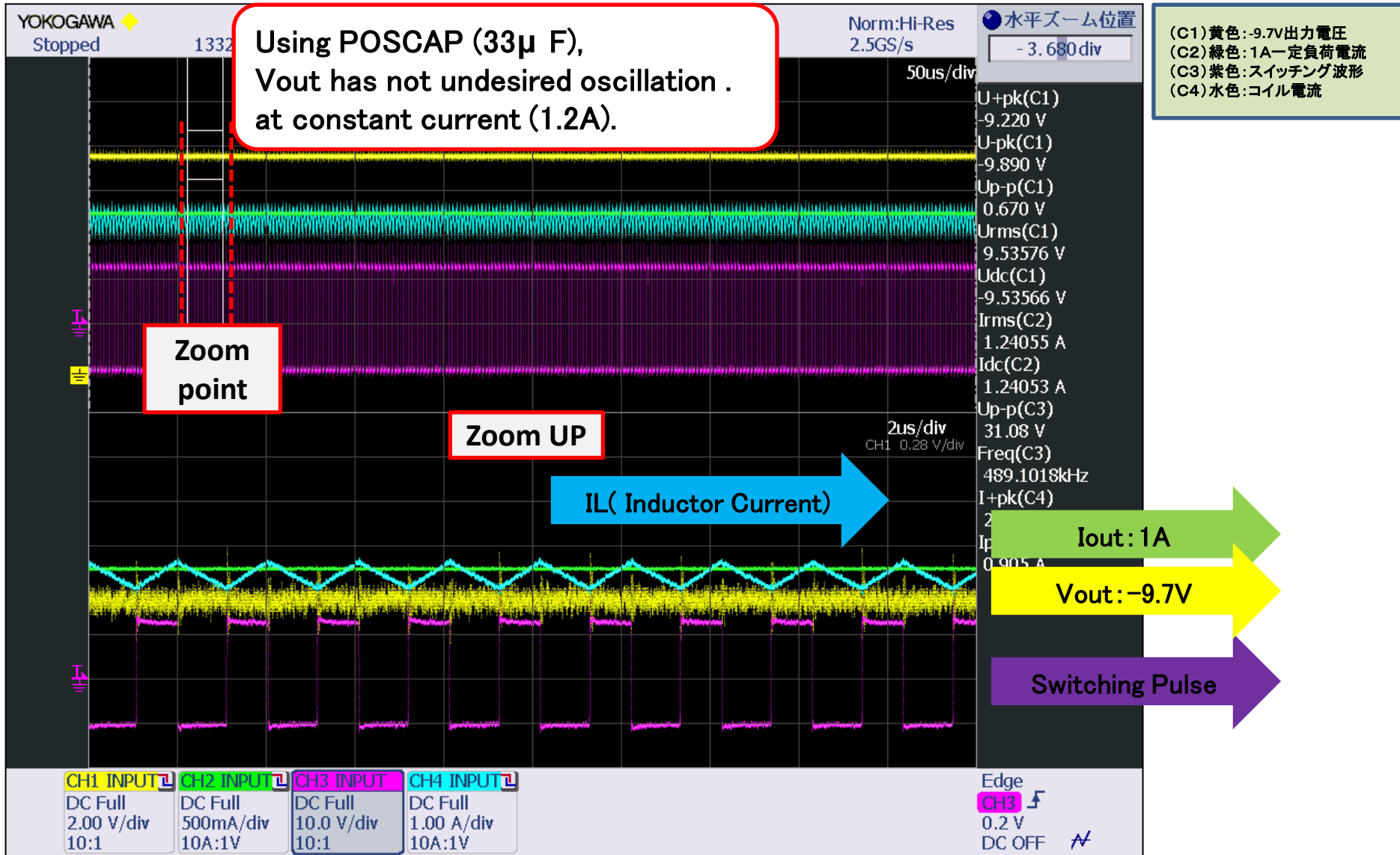
Before Ceramic capacitance 4.7μ F

After POSCAP 33μ F

■ Result of this countermeasure

Even if low temperature and high load current, this undesired oscillation did not occur.

Waveform at Iout:1A after their countermeasure



考察: 出力波形、コイル電流、それにスイッチング波形に異常は見当たらない。